

FIG. 1

FIG. 2

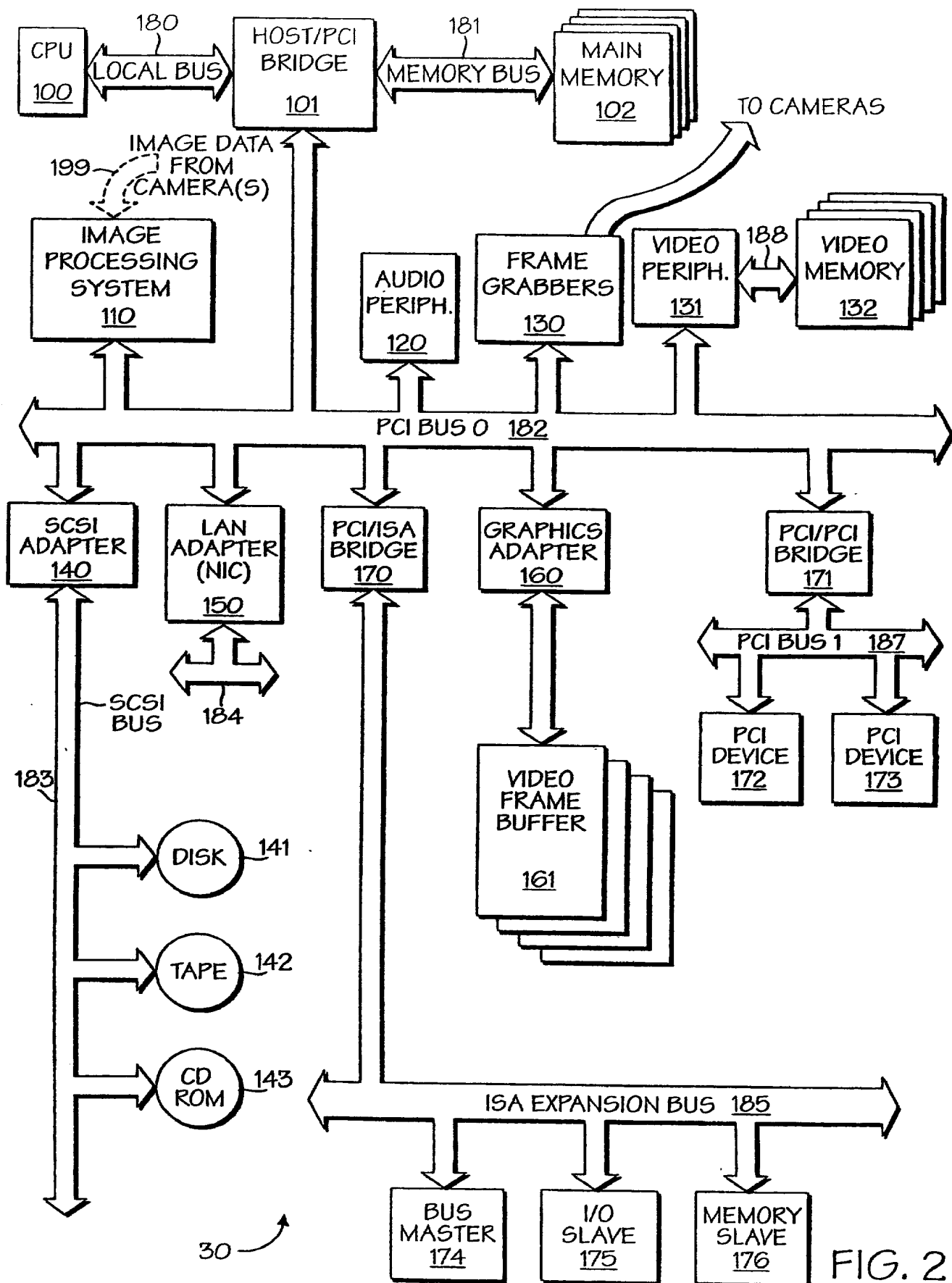


FIG. 2

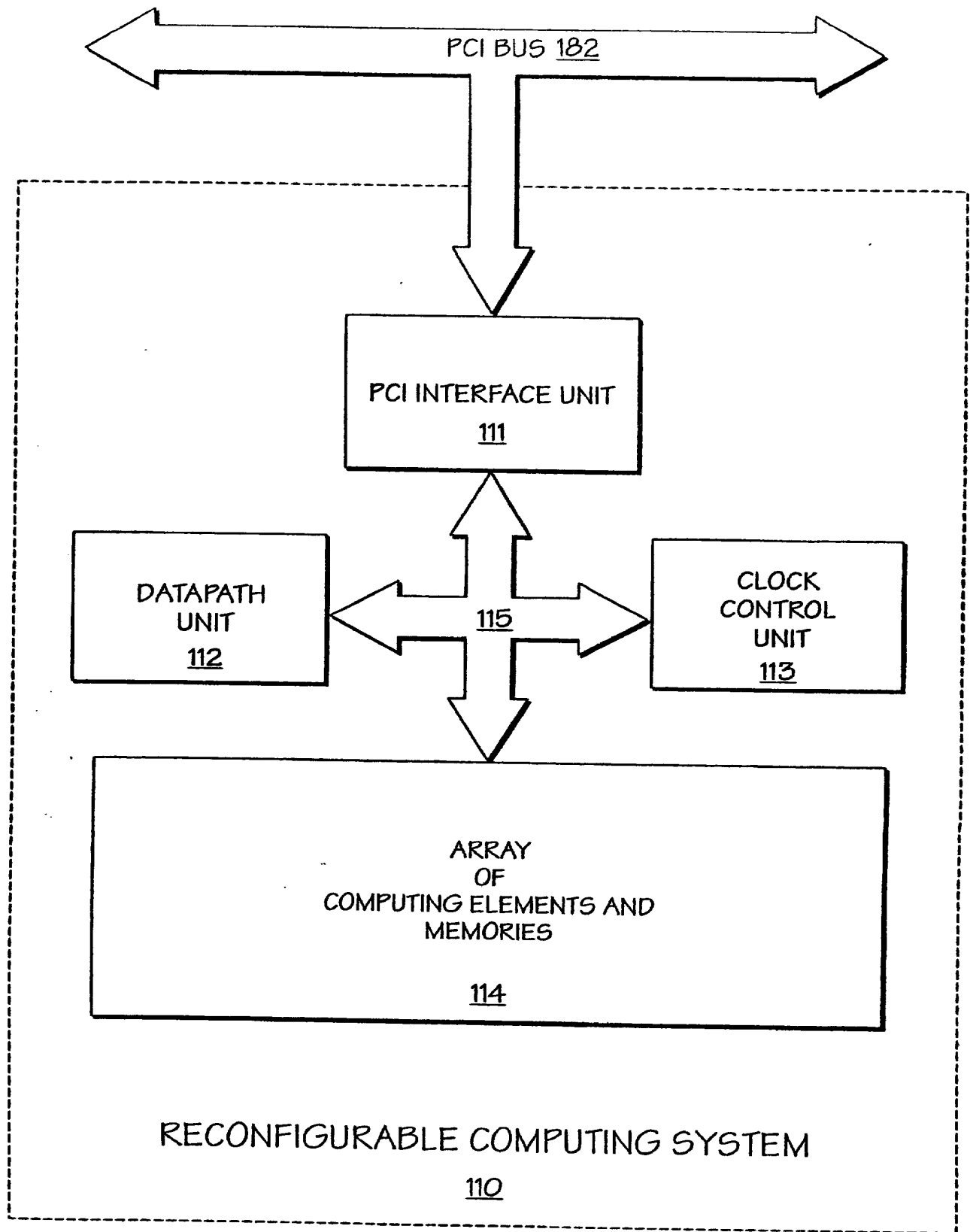


FIG. 3

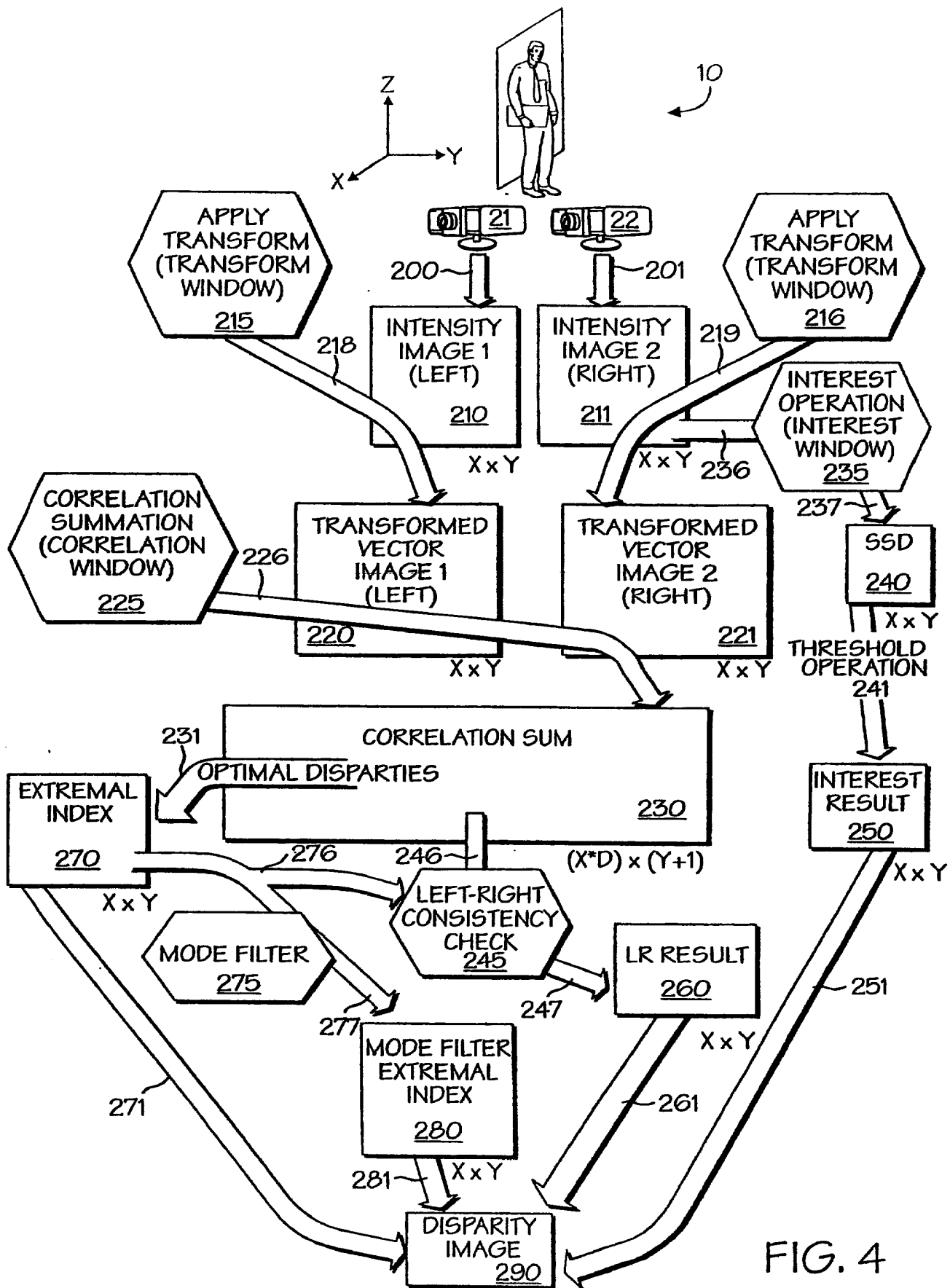


FIG. 4

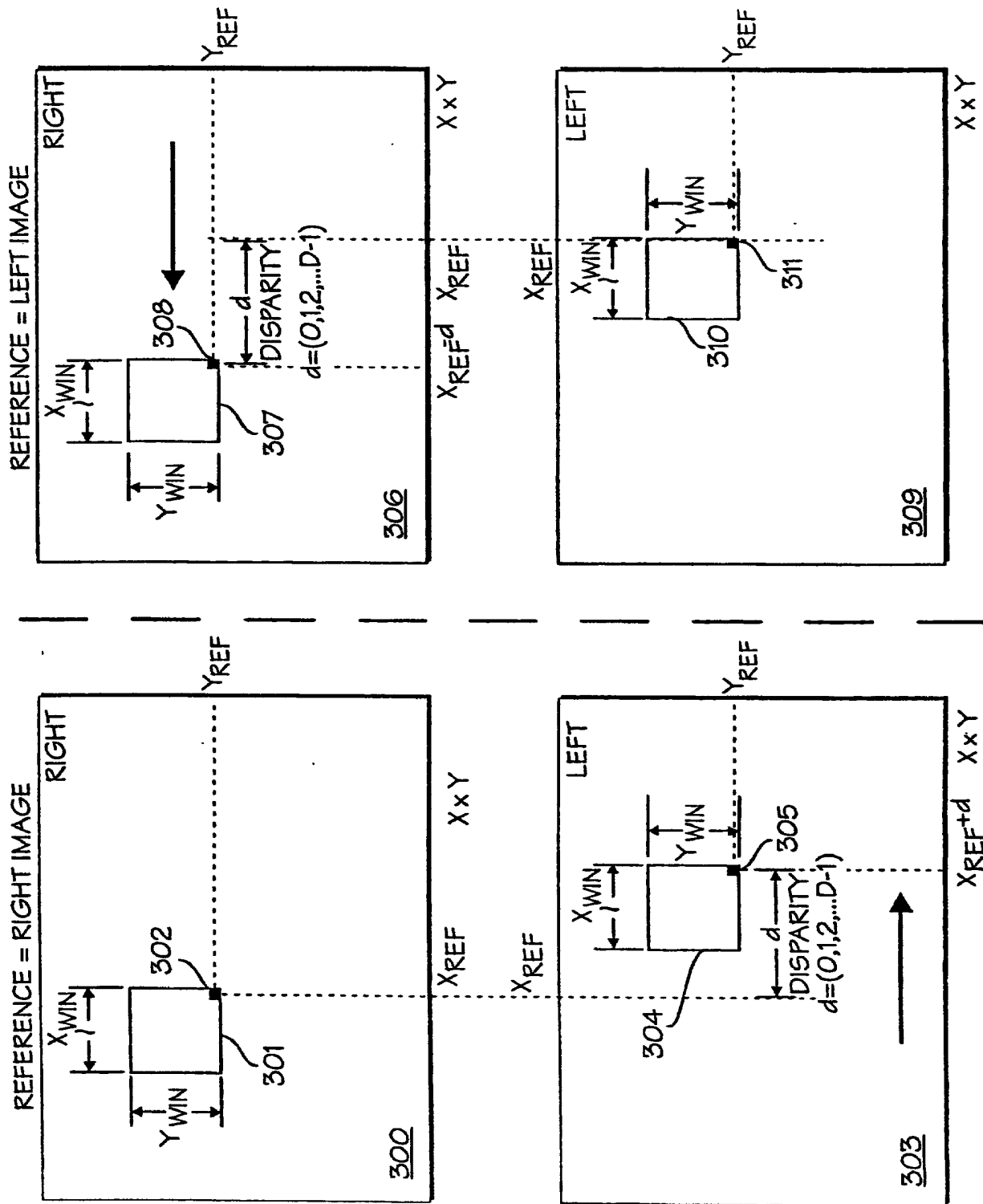
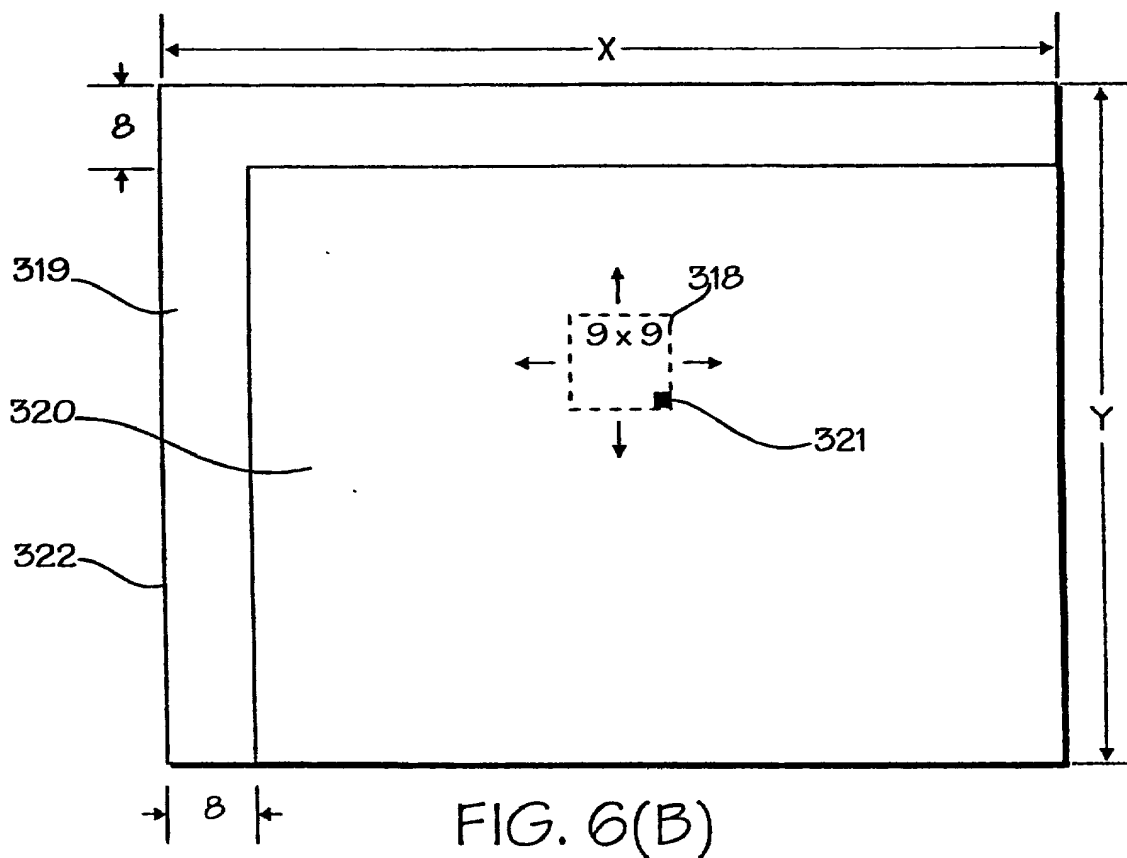
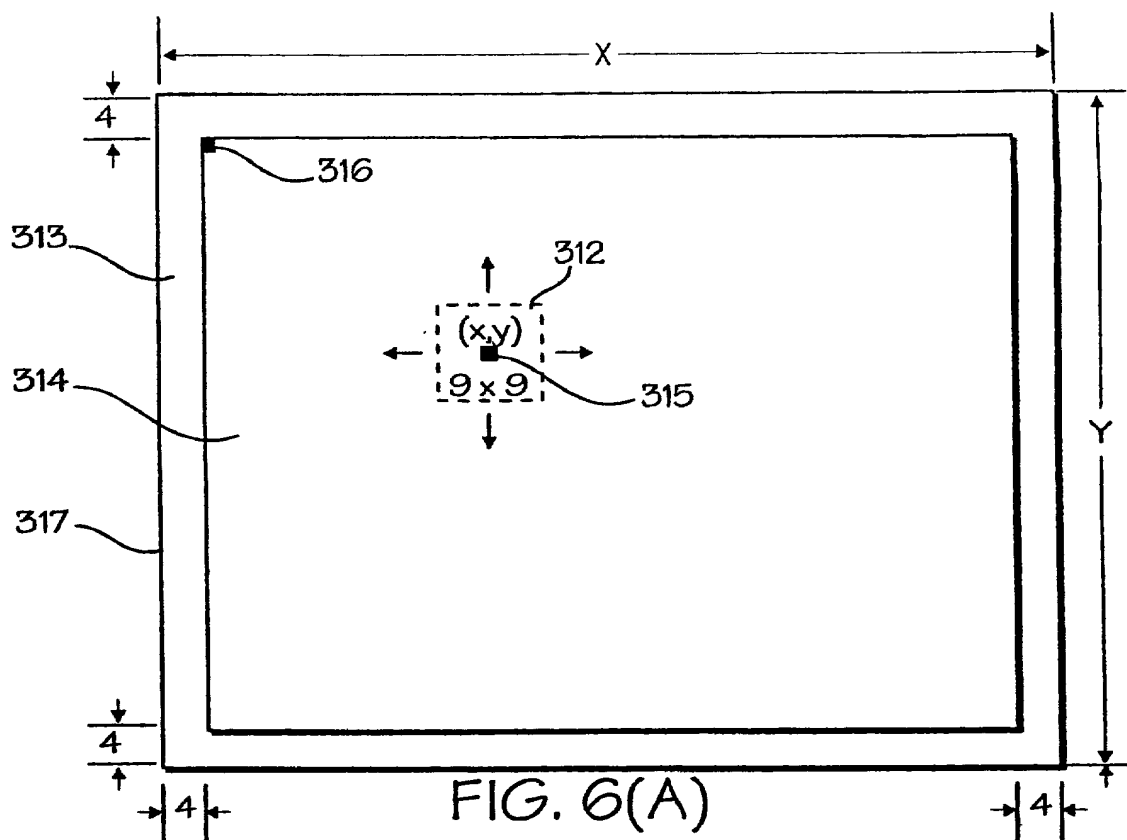


FIG. 5(A)

FIG. 5(B)



DATA FOR CENSUS VECTOR  
CENTERED AT (x,y)

		X								
		-4	-3	-2	-1	0	1	2	3	4
Y	-4						1		2	
	-3	3		4		5		6		
	-2		7		8		9		10	
	-1	11		12		13		14		
	0		15		16	(x,y)		17		18
	1		19		20		21		22	
	2			23		24		25		26
	3		27		28		29		30	
	4			31		32				

324

↑  
323

Fig. 7

Fig. 8(A)

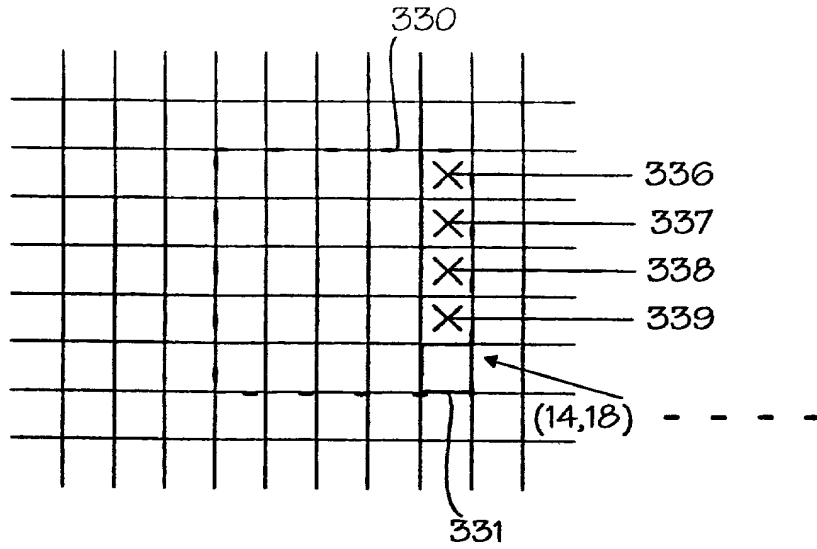


Fig. 8(B)

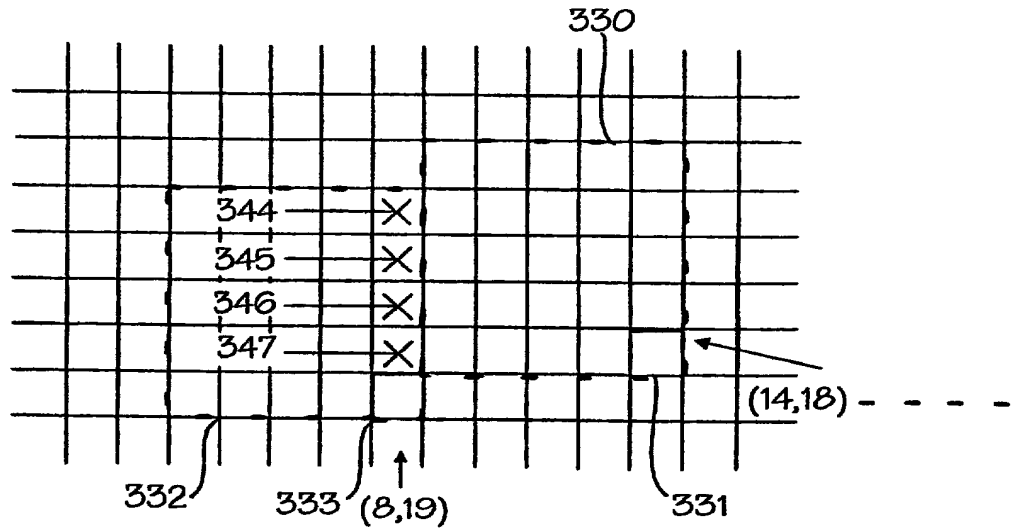


Fig. 8(C)

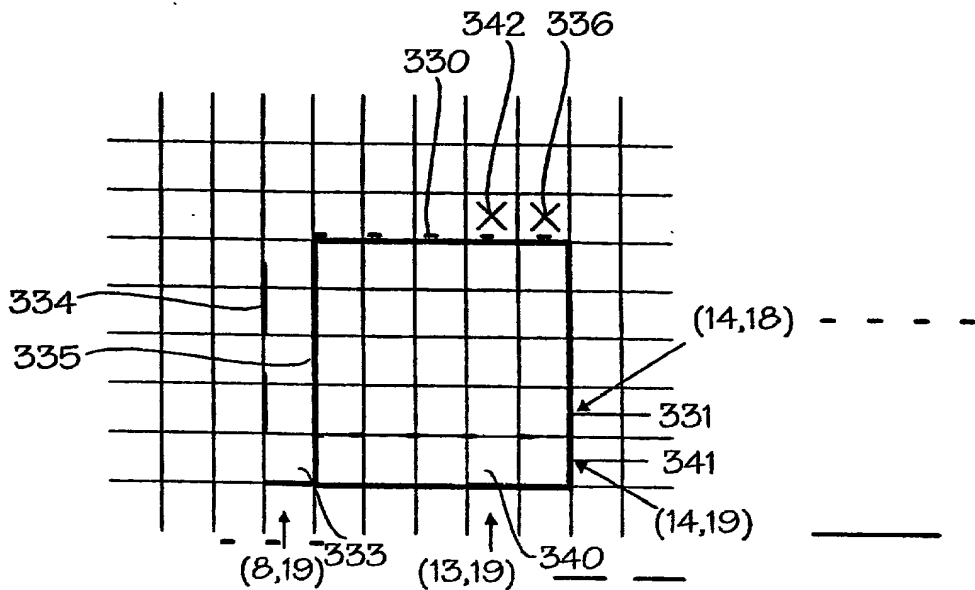




Fig. 9(A)

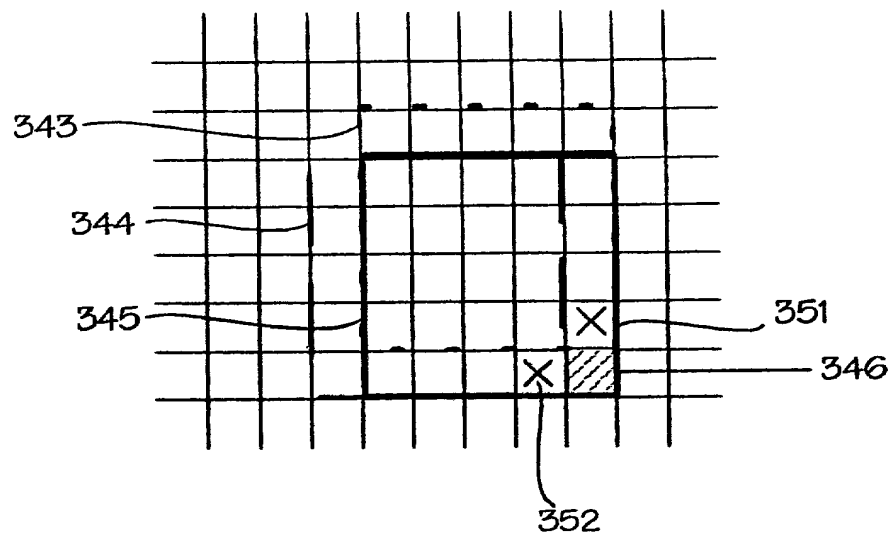


Fig. 9(B)

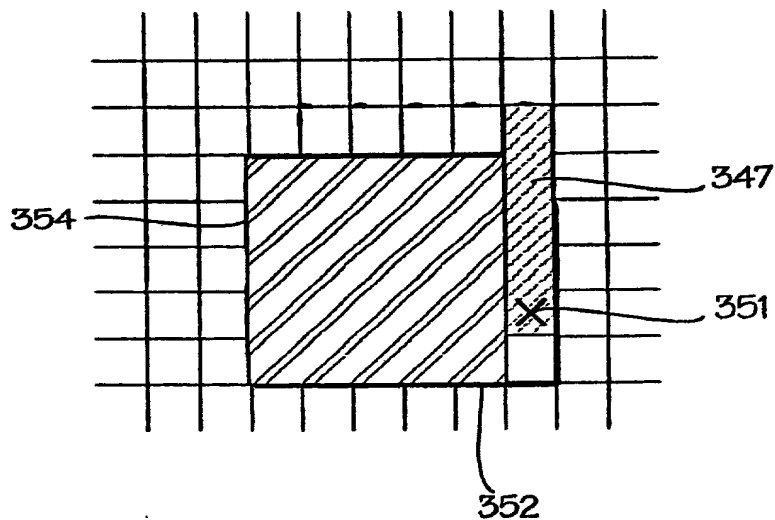
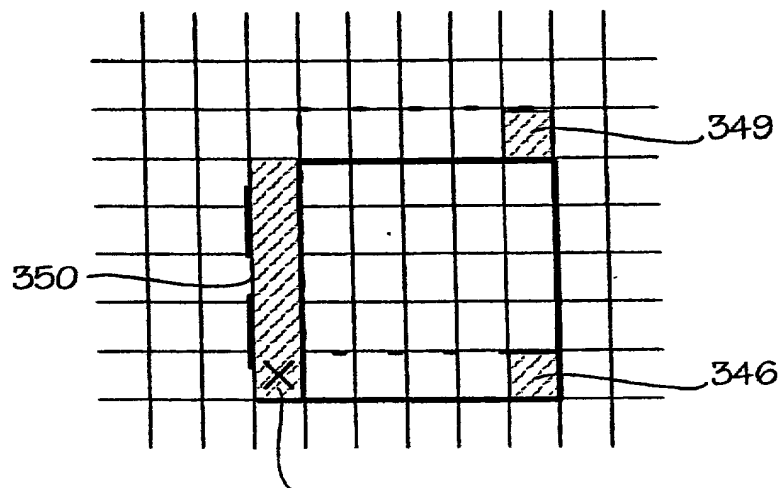


Fig. 9(C)



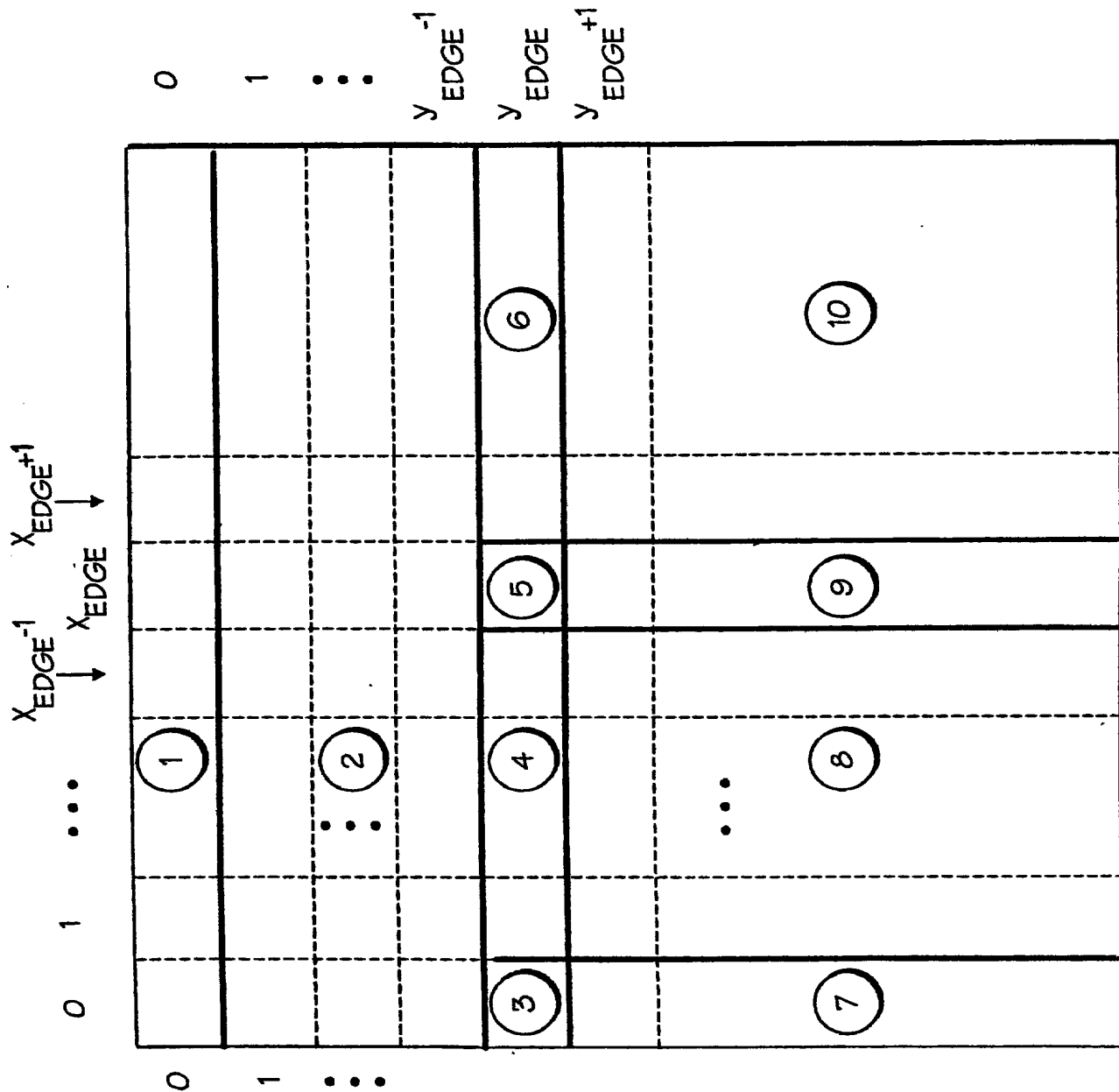


Fig. 10A

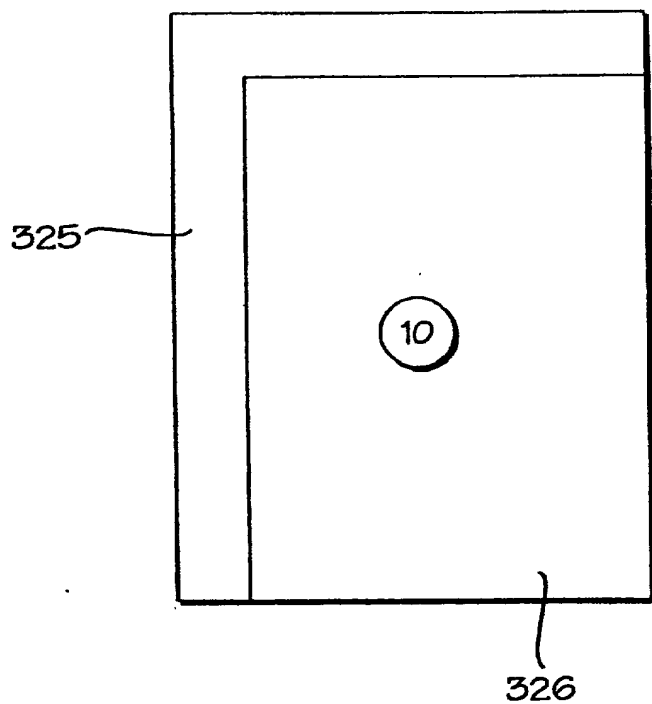


Fig. 10(B)

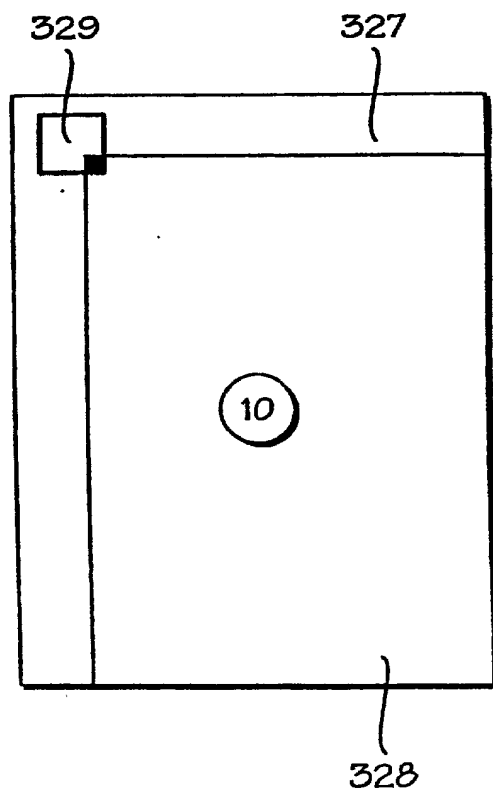


Fig. 10(C)





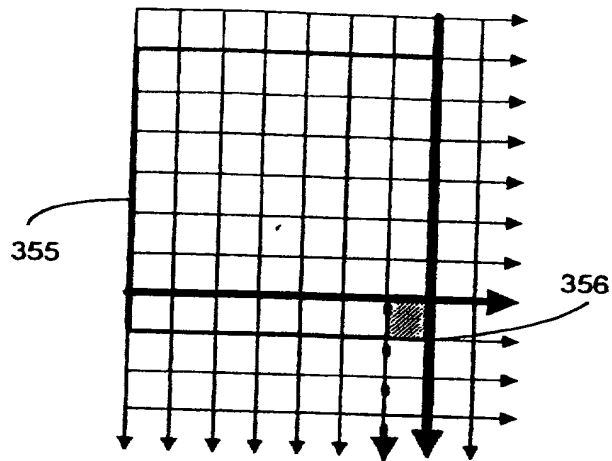


Fig. 11 (I)

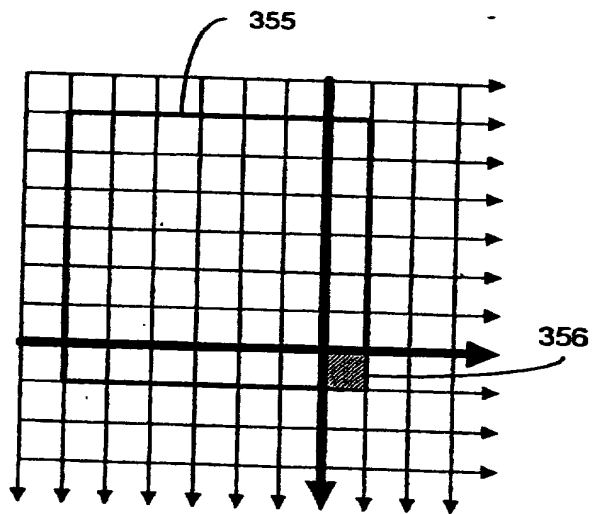


Fig. 11 (J)

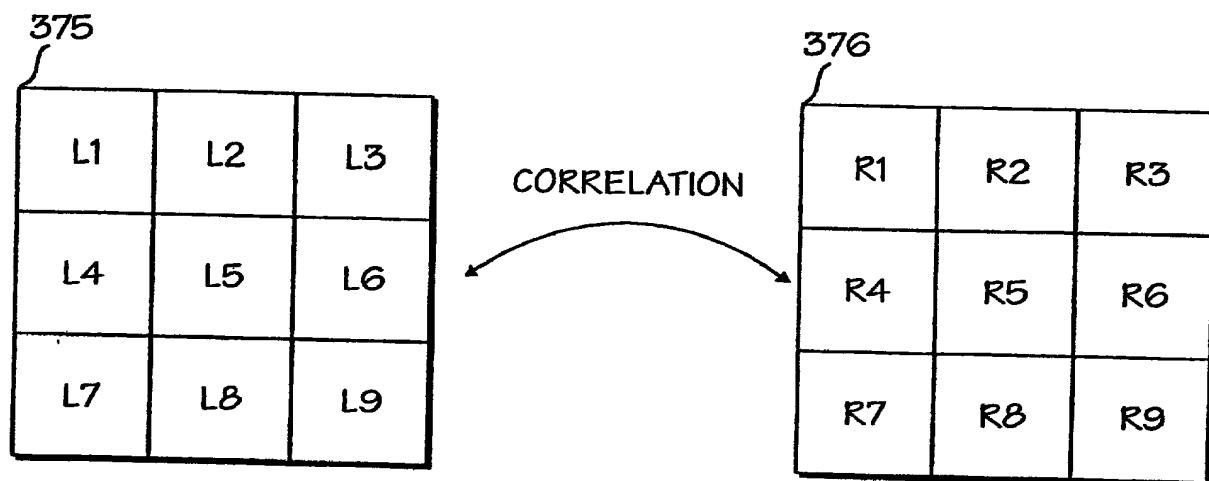


Fig. 12

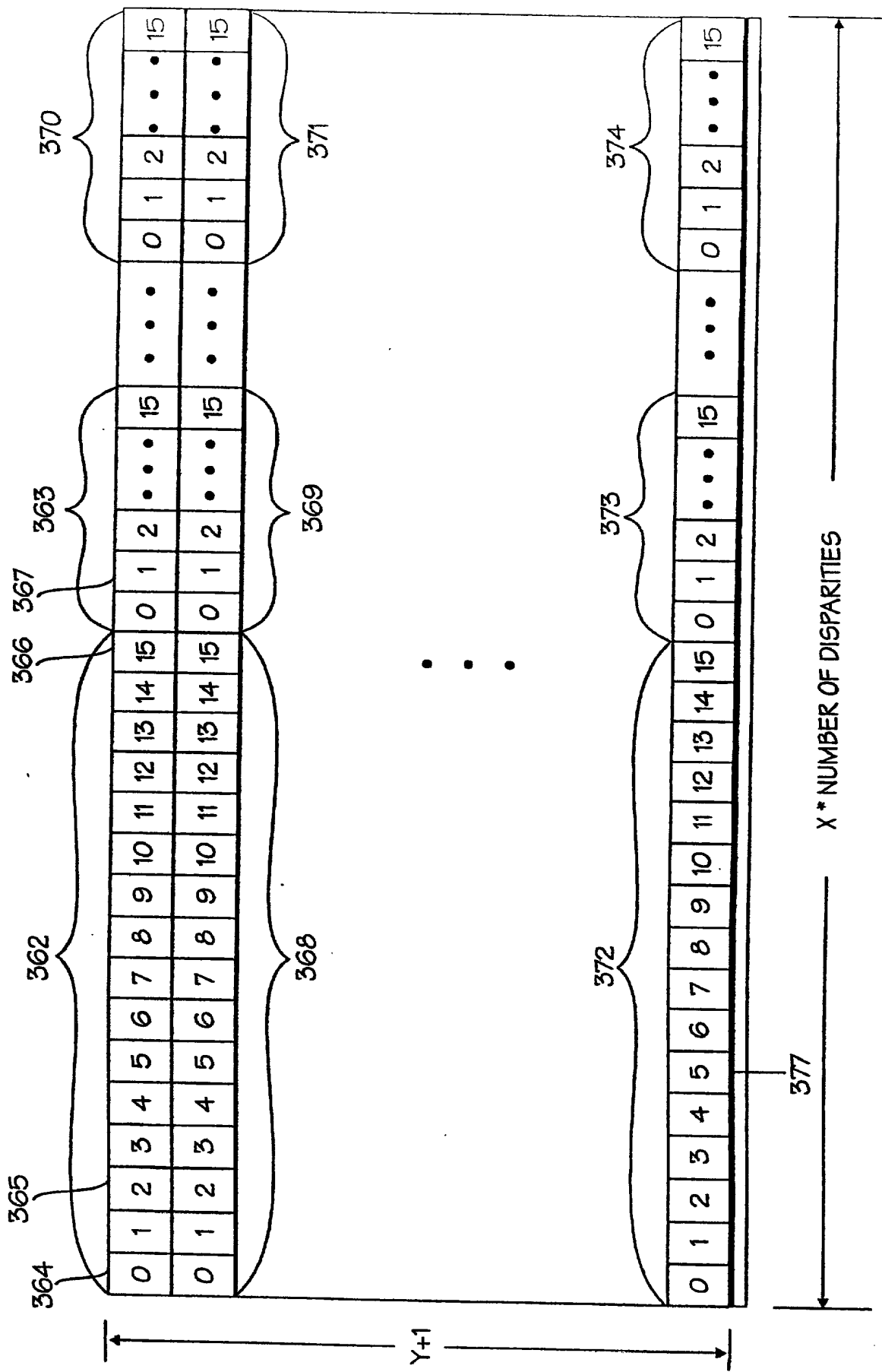
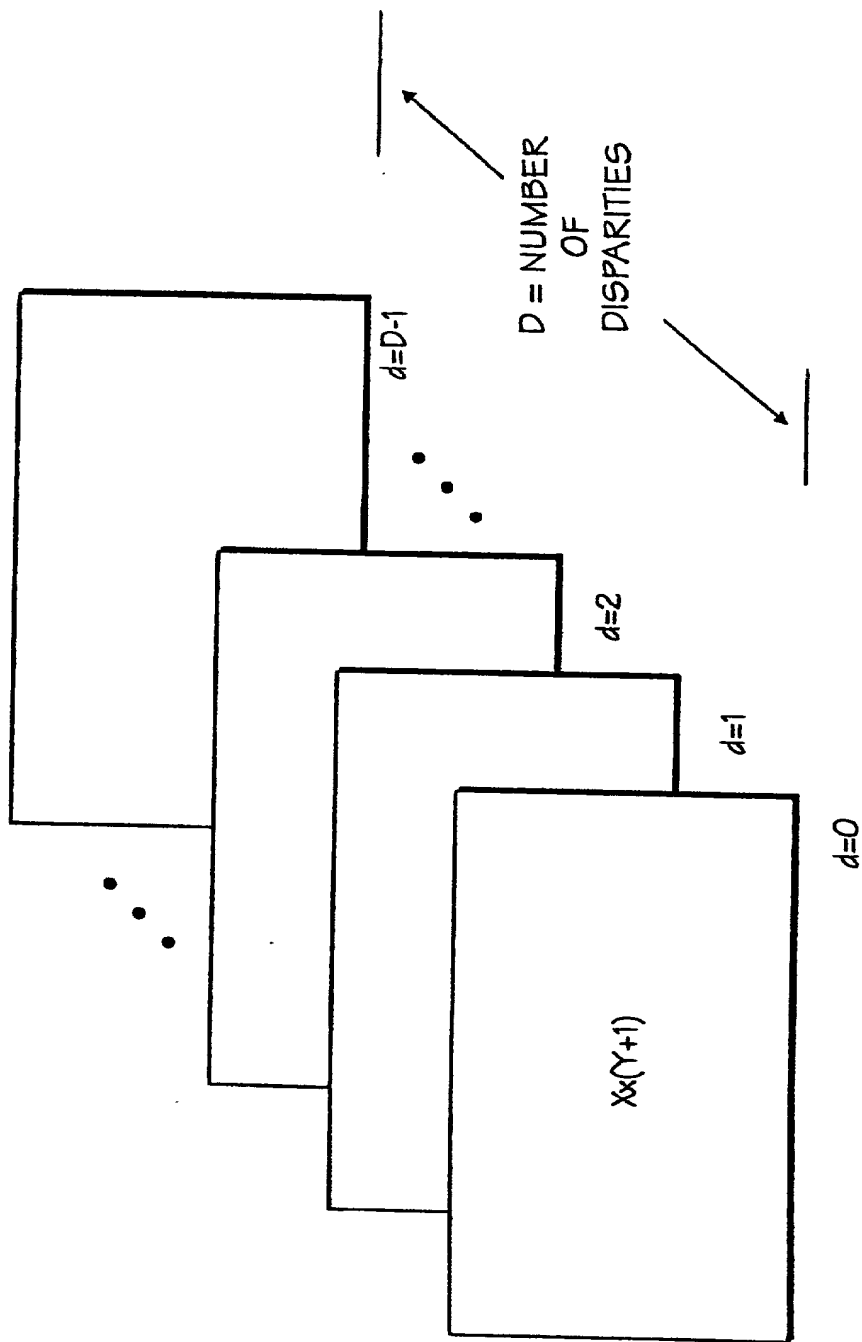


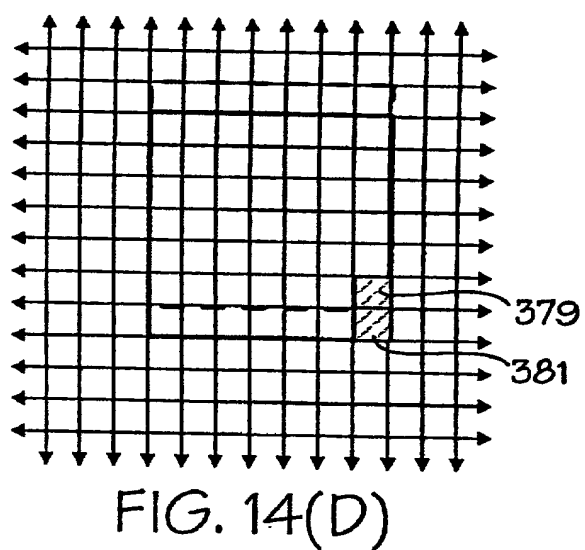
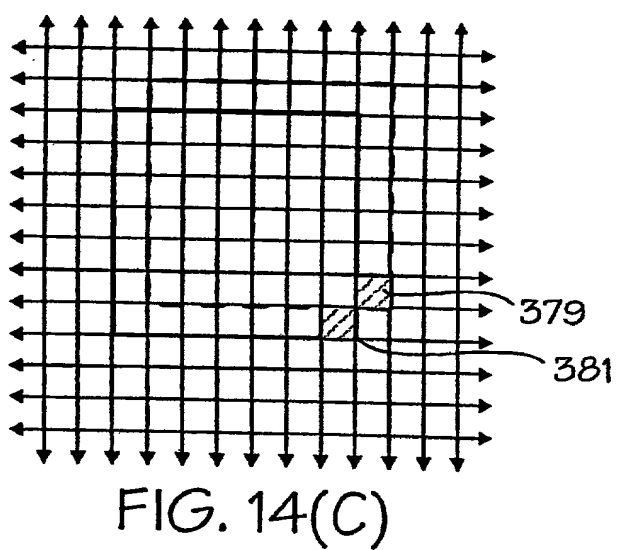
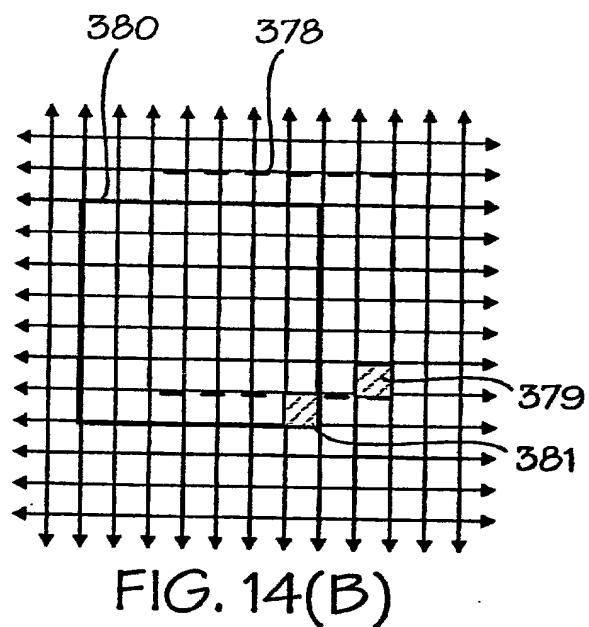
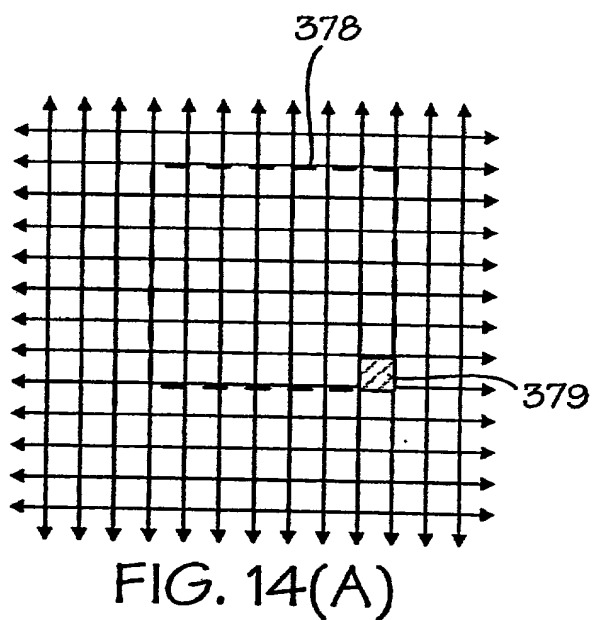
Fig. 13A





(B)

Fig. 13B





RIGHT IMAGE = REFERENCE  
LEFT IMAGE RIGHT IMAGE

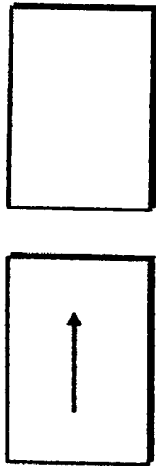


Fig. 16A Fig. 16B

RIGHT IMAGE = REFERENCE  
LEFT IMAGE RIGHT IMAGE

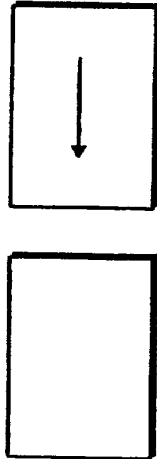


Fig. 16C Fig. 16D

CENSUS VECTORS  
LEFT IMAGE

A <sub>L</sub>	B <sub>L</sub>	C <sub>L</sub>	D <sub>L</sub>	E <sub>L</sub>	F <sub>L</sub>	G <sub>L</sub>	H <sub>L</sub>	I <sub>L</sub>	J <sub>L</sub>	...
• • •										

Fig. 16E

CENSUS VECTORS  
RIGHT IMAGE

A <sub>R</sub>	B <sub>R</sub>	C <sub>R</sub>	D <sub>R</sub>	E <sub>R</sub>	F <sub>R</sub>	G <sub>R</sub>	H <sub>R</sub>	I <sub>R</sub>	J <sub>R</sub>
• • •									

Fig. 16F

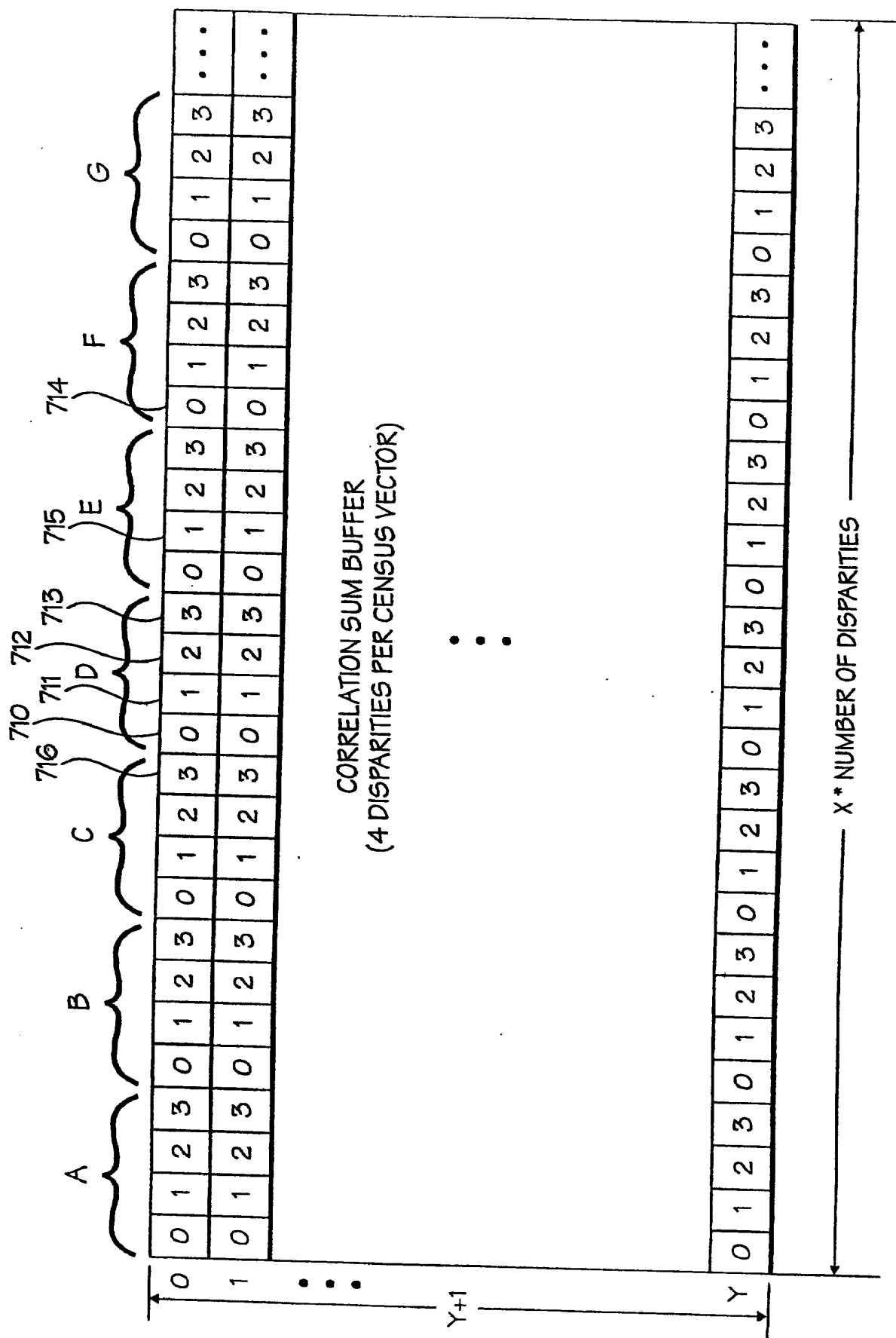
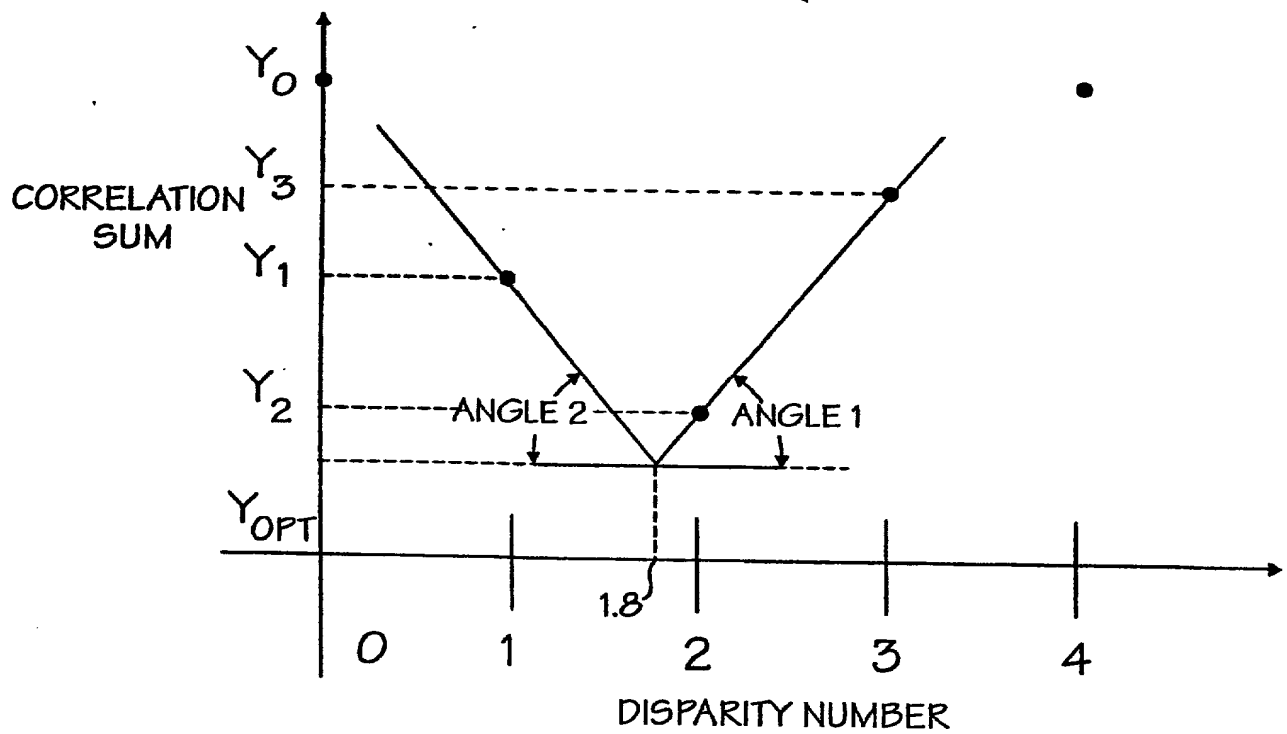
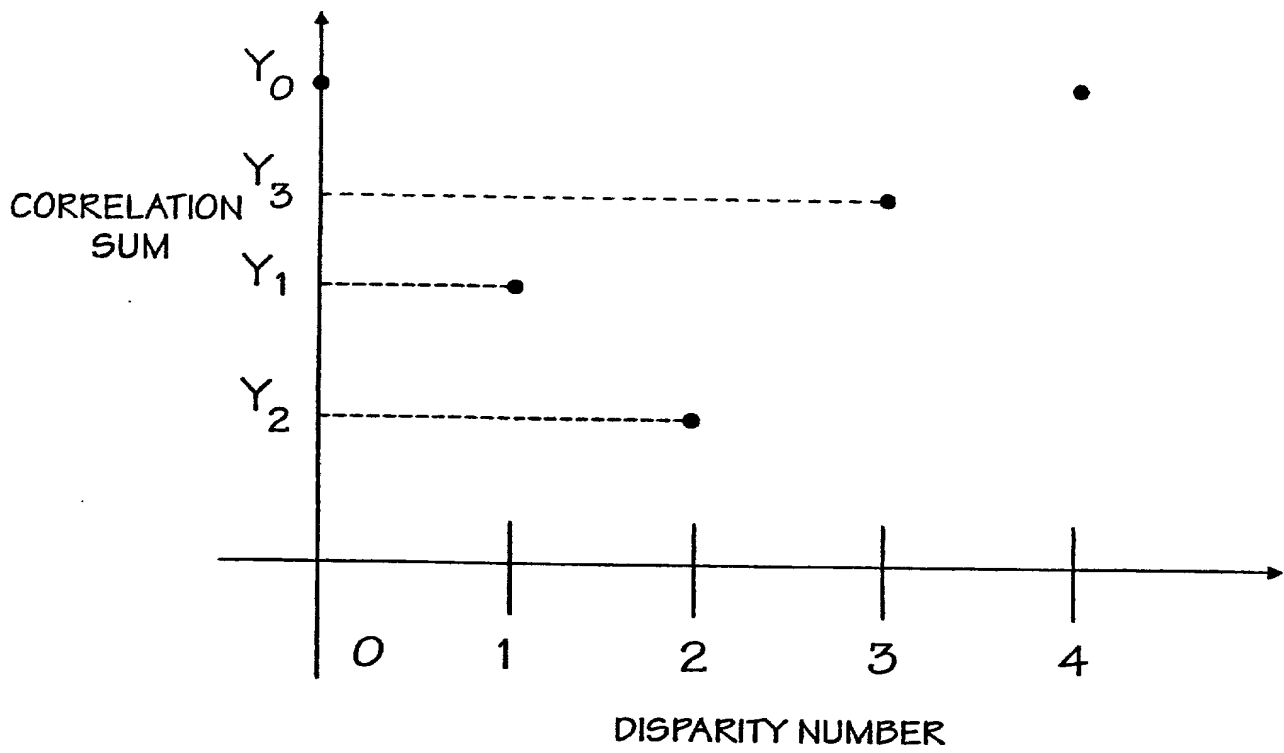


Fig. 16G



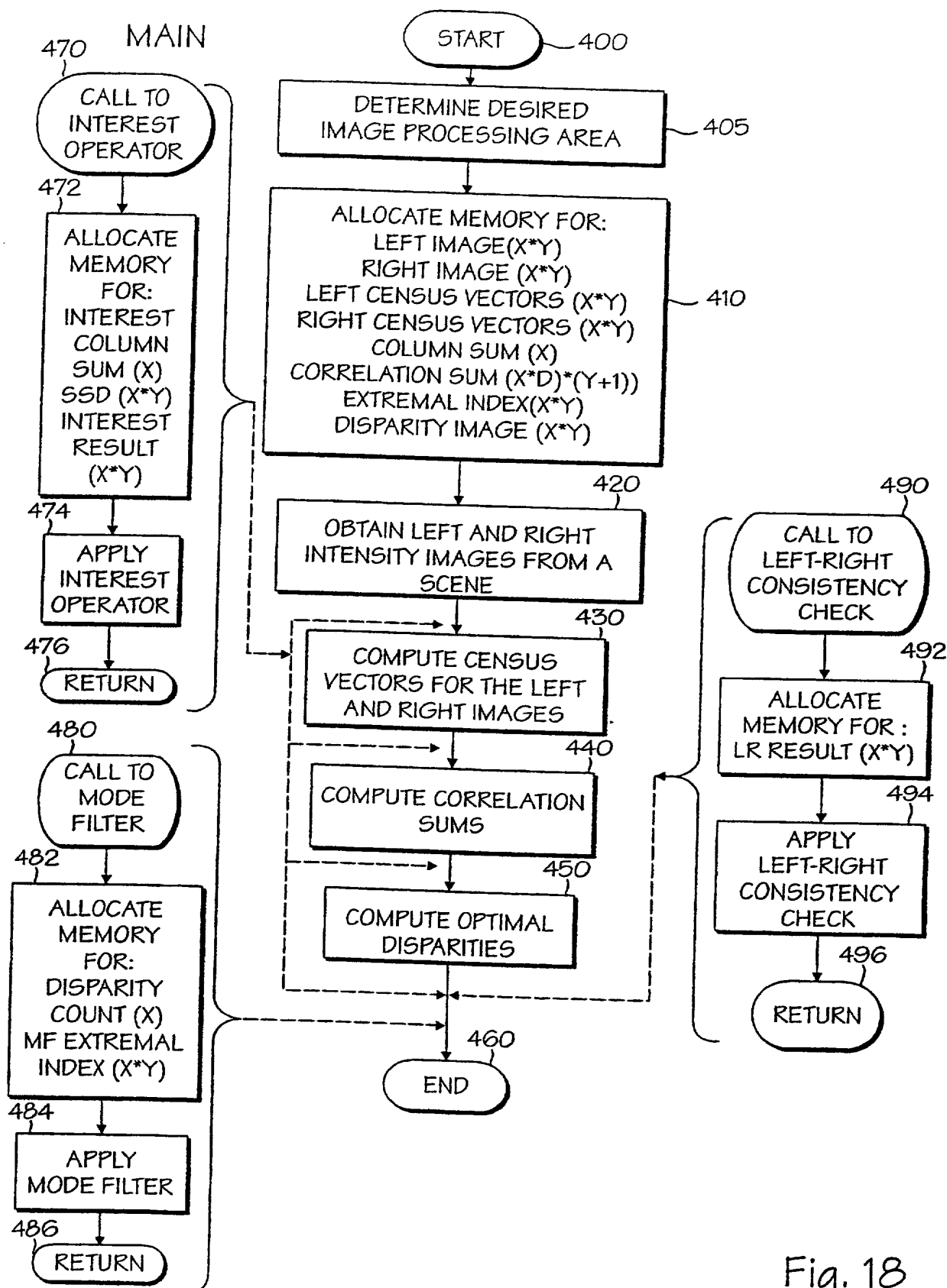


Fig. 18

# CENSUS TRANSFORM & CENSUS VECTORS

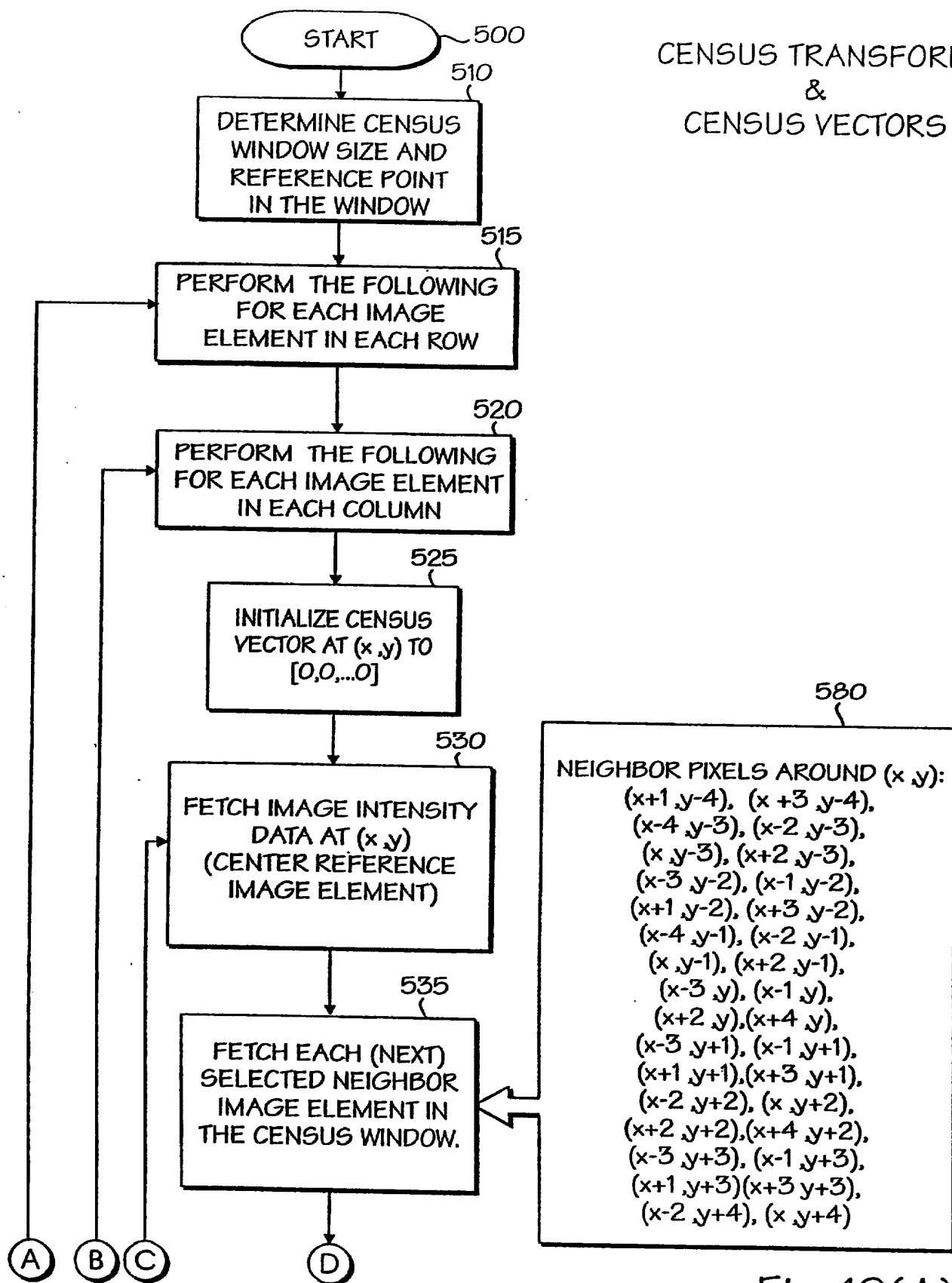


Fig. 19(A)



# CENSUS TRANSFORM & CENSUS VECTORS

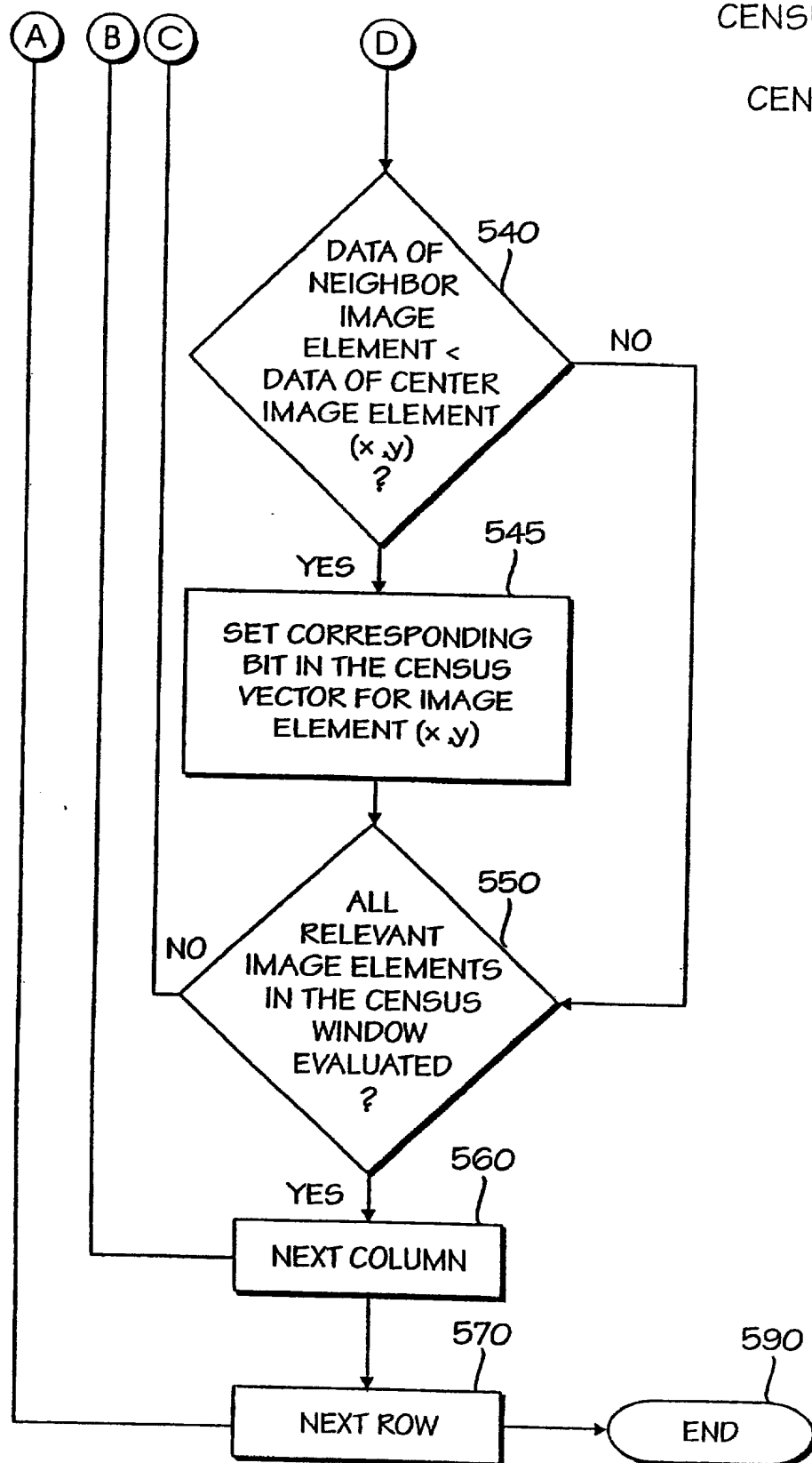


Fig. 19(B)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION

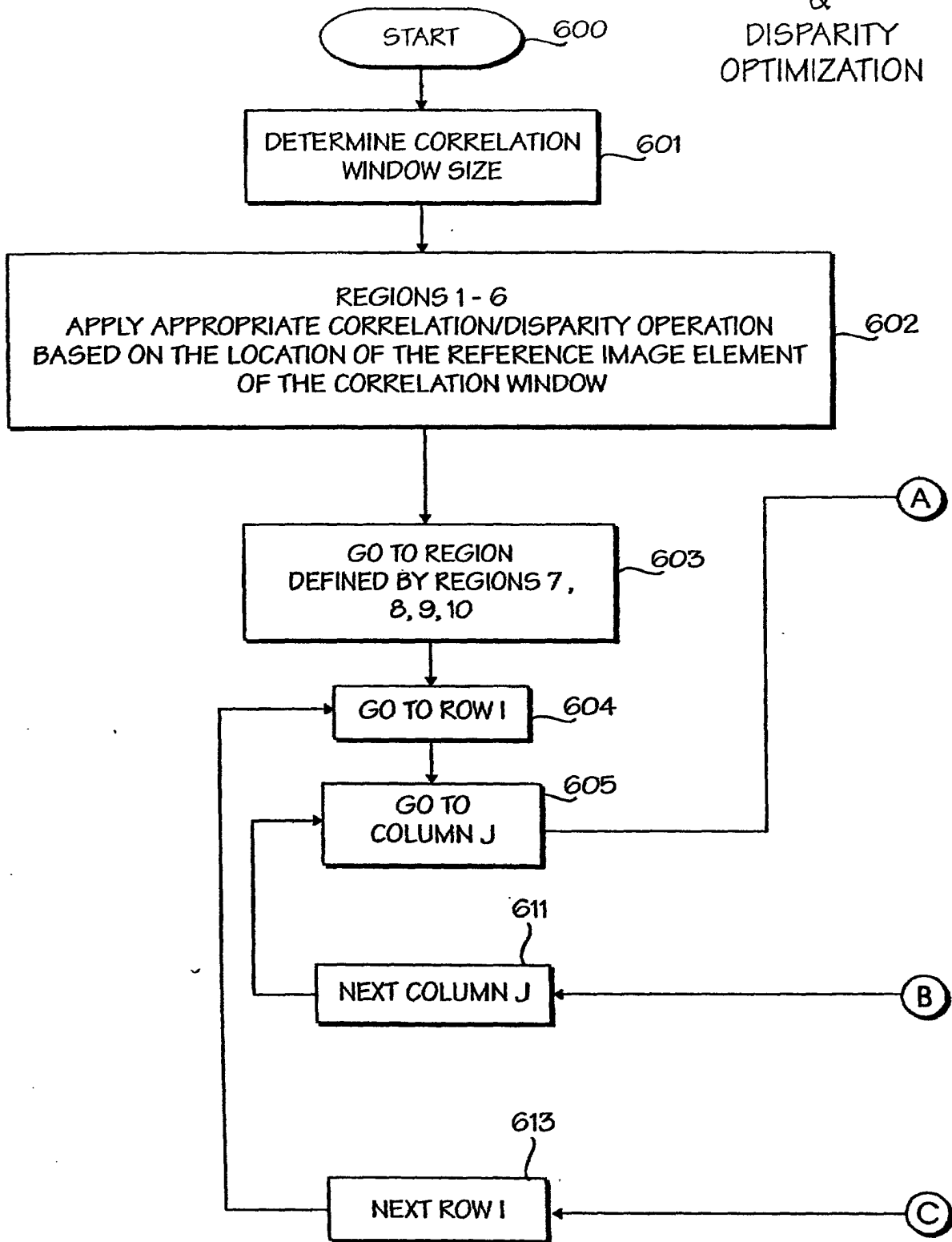


Fig. 20(A)

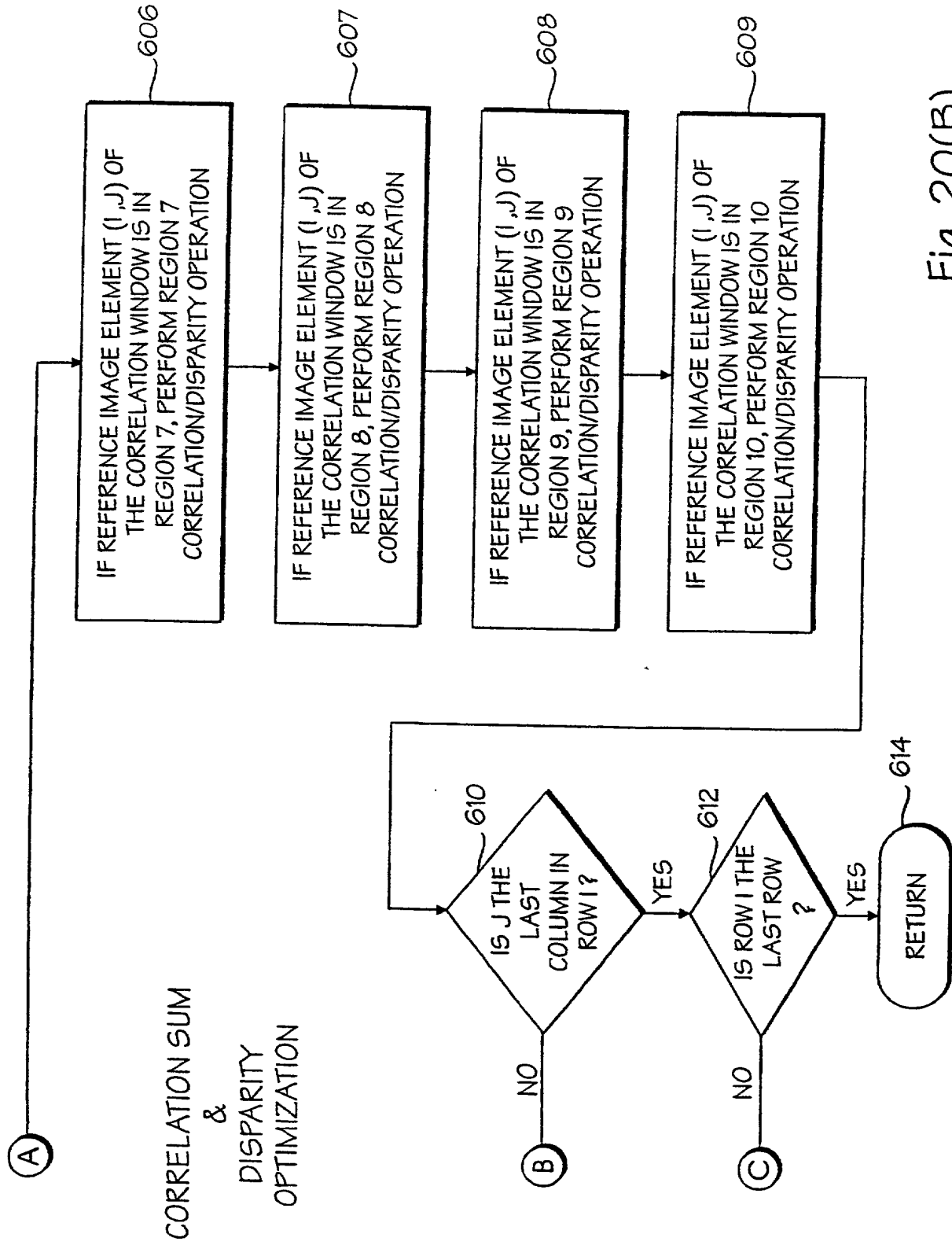


Fig. 20(B)

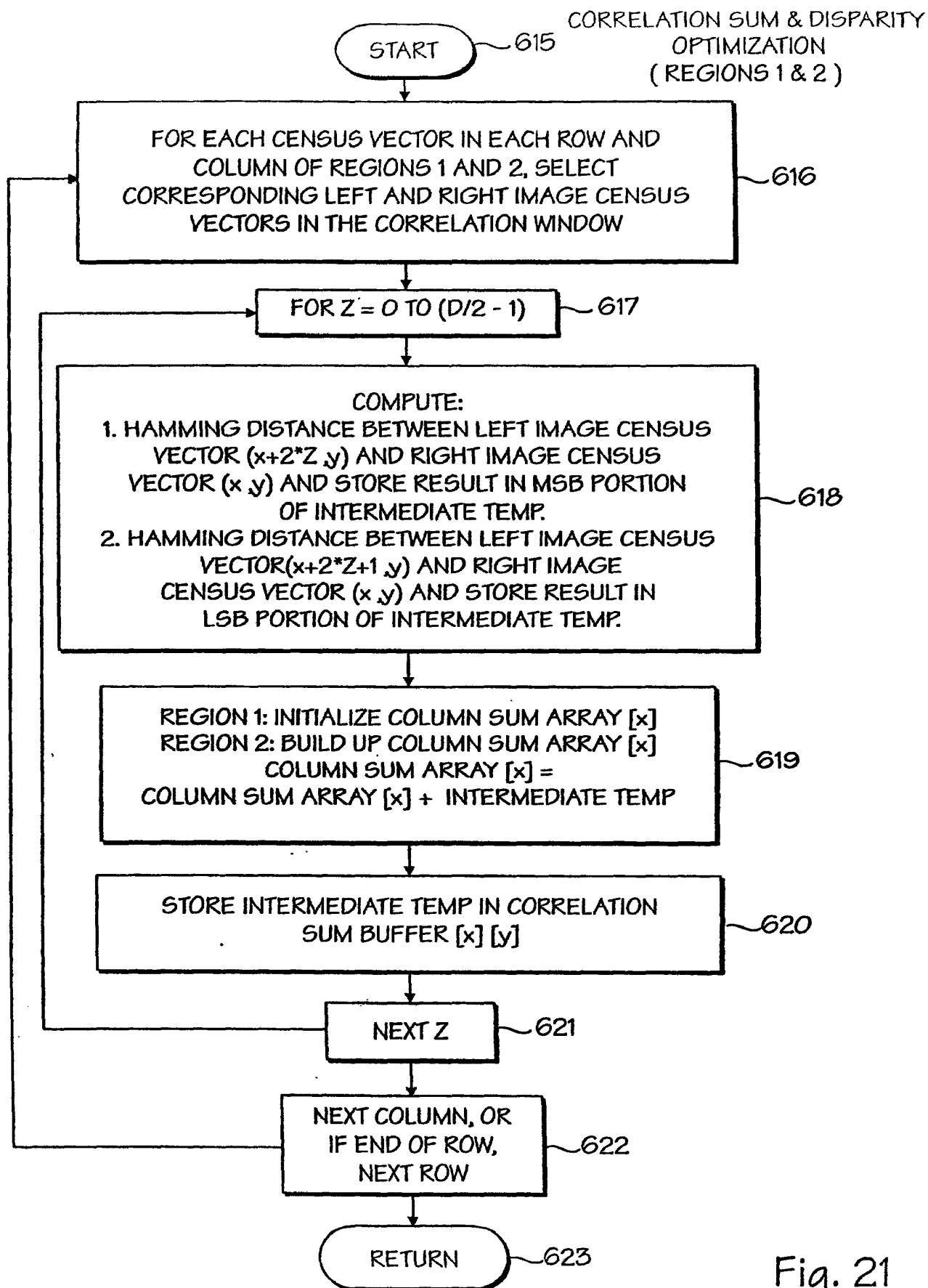


Fig. 21

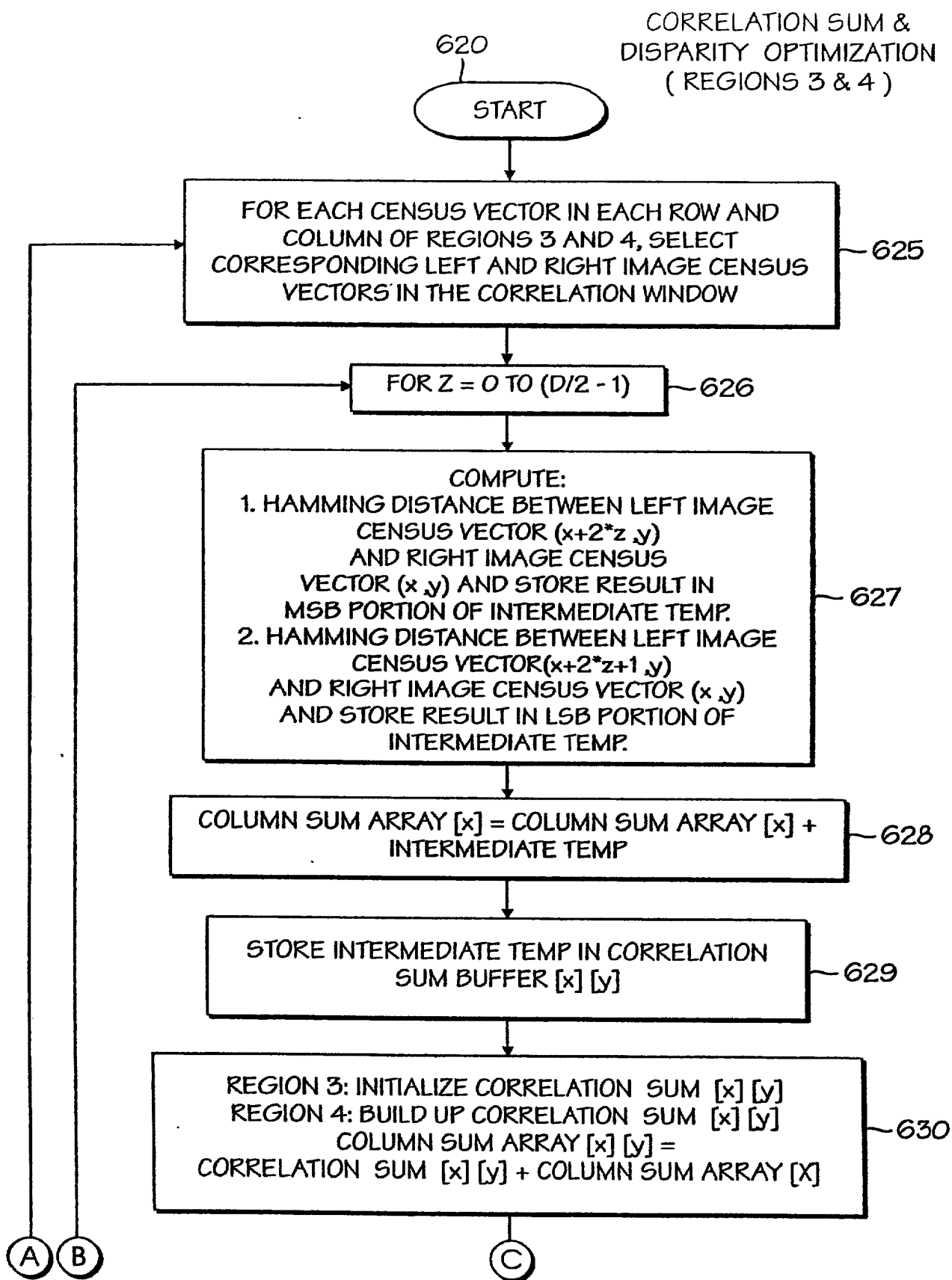


FIG. 22(A)

CORRELATION SUM &  
DISPARITY OPTIMIZATION  
( REGIONS 3 & 4 )

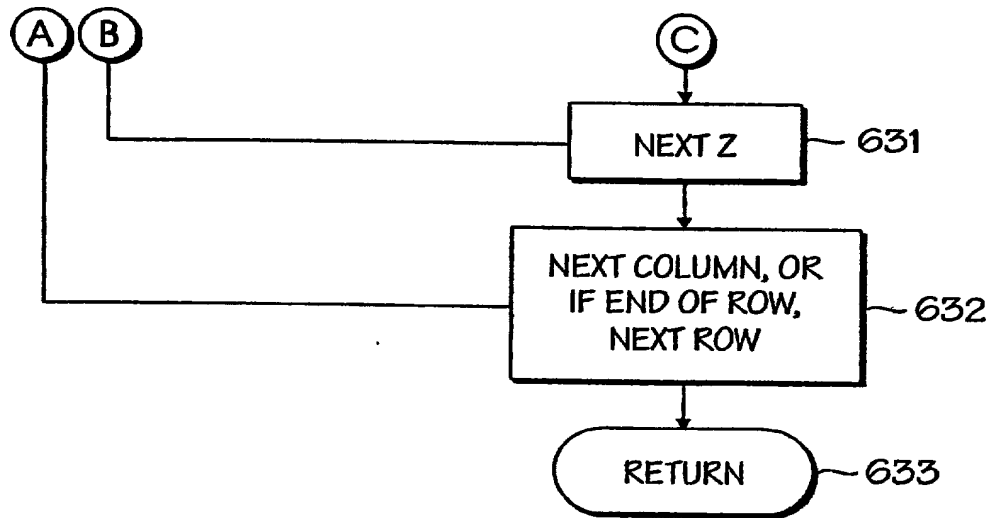


FIG. 22(B)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
( REGION 5 )

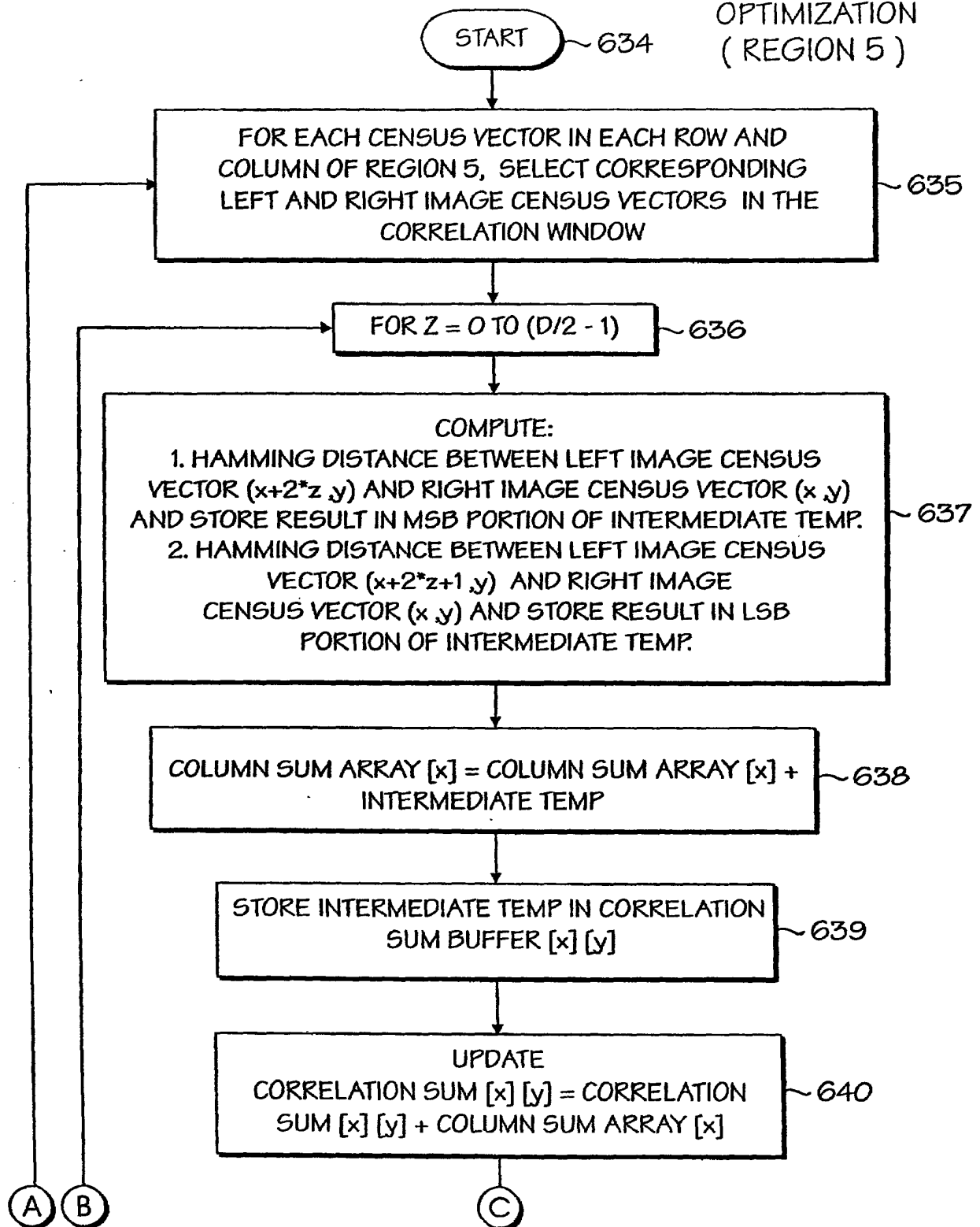


FIG. 23(A)

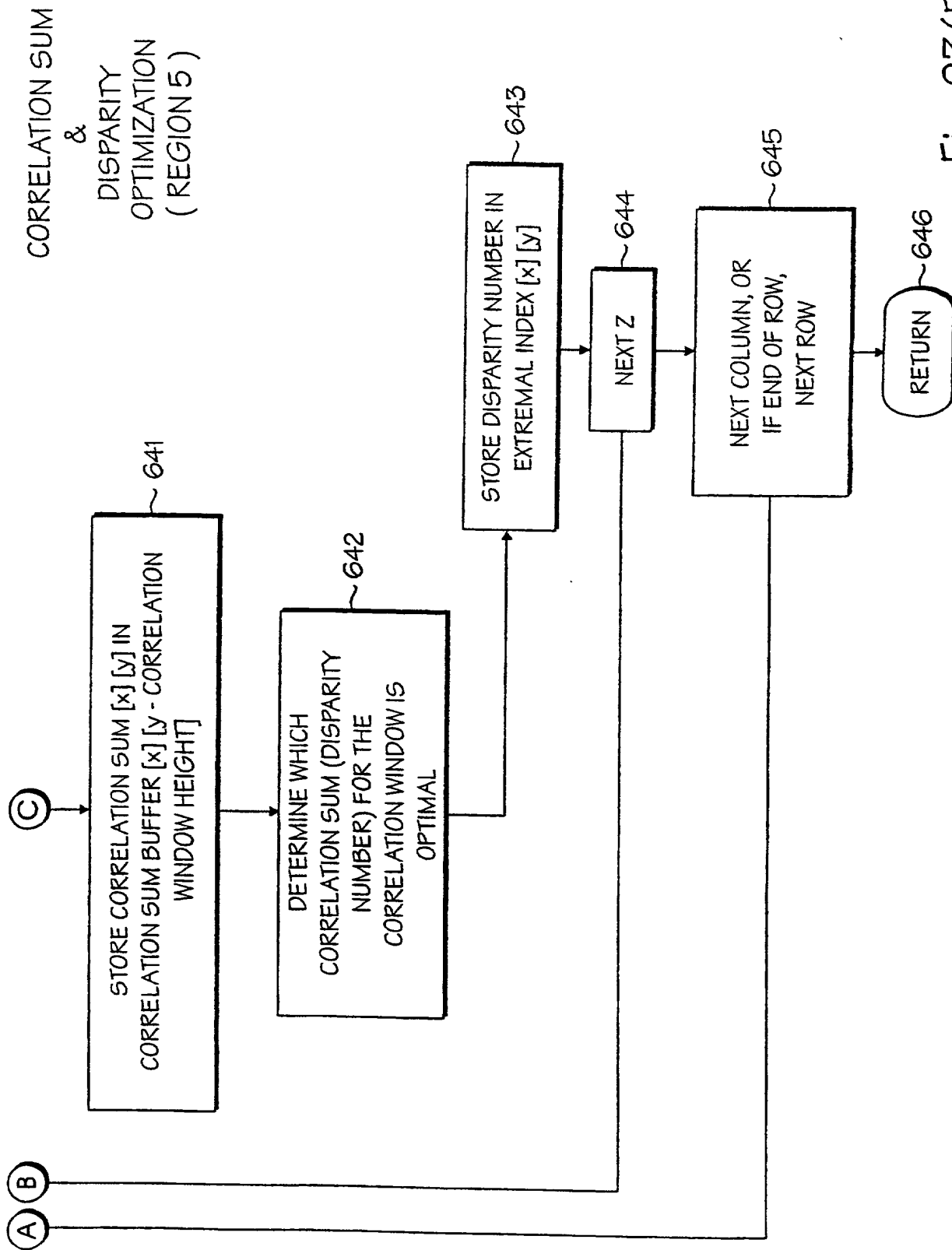


Fig. 23(B)



CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
( REGION 6 )

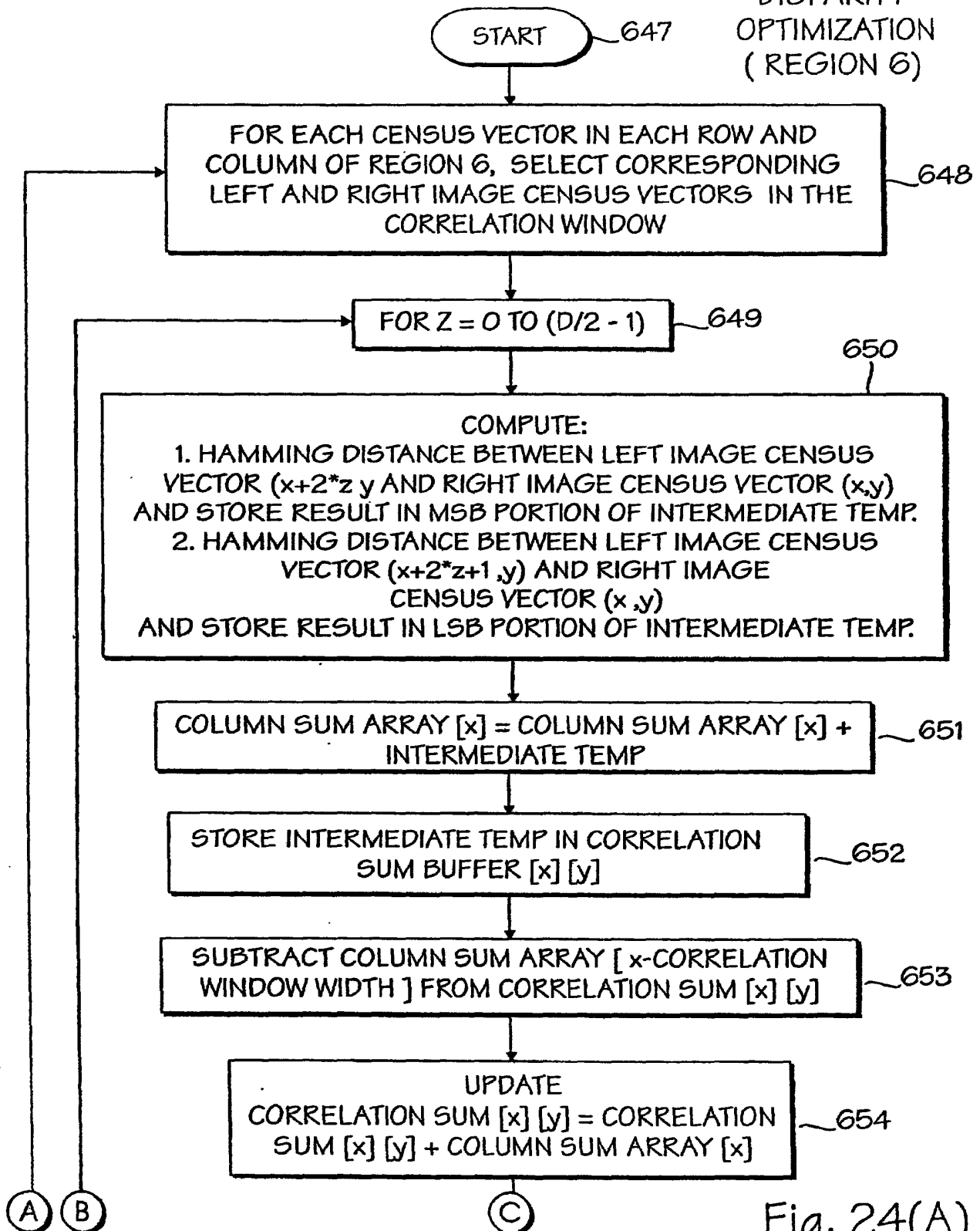


Fig. 24(A)

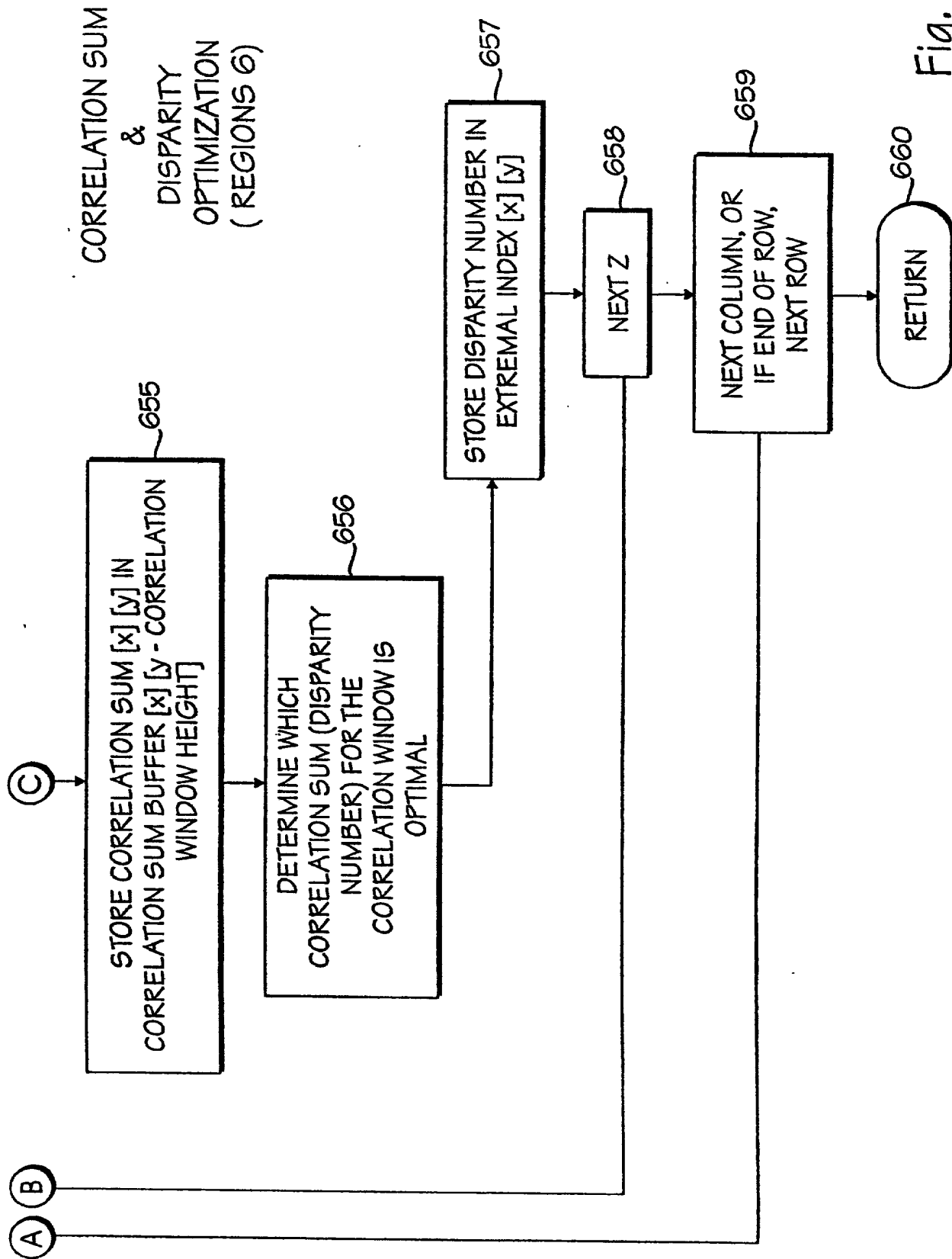


Fig. 24(B)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
(REGIONS 7 & 8)

START

661

FOR EACH CENSUS VECTOR IN EACH ROW AND  
COLUMN OF REGIONS 7 AND 8, SELECT  
CORRESPONDING LEFT AND RIGHT IMAGE CENSUS  
VECTORS IN THE CORRELATION WINDOW

662

FOR  $Z = 0$  TO  $(D/2 - 1)$

663

SUBTRACT TOP RIGHT CORRELATION SUM ELEMENT  
(CORRELATION SUM BUFFER  $[x]$   $[y$ -CORRELATION WINDOW  
HEIGHT]) FROM COLUMN SUM ARRAY  $[x]$

664

COMPUTE:

1. HAMMING DISTANCE BETWEEN LEFT IMAGE CENSUS  
VECTOR  $(x+2*z, y)$  AND RIGHT IMAGE CENSUS  
VECTOR  $(x, y)$  AND STORE RESULT IN MSB  
PORTION OF INTERMEDIATE TEMP.
2. HAMMING DISTANCE BETWEEN LEFT IMAGE CENSUS  
VECTOR  $(x+2*z+1, y)$  AND RIGHT IMAGE CENSUS  
VECTOR  $(x, y)$  AND STORE RESULT IN  
LSB PORTION OF INTERMEDIATE TEMP.

665

COLUMN SUM ARRAY  $[x] =$  COLUMN SUM ARRAY  $[x] +$   
INTERMEDIATE TEMP

666

A B

C

Fig. 25(A)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
(REGIONS 7 & 8 )

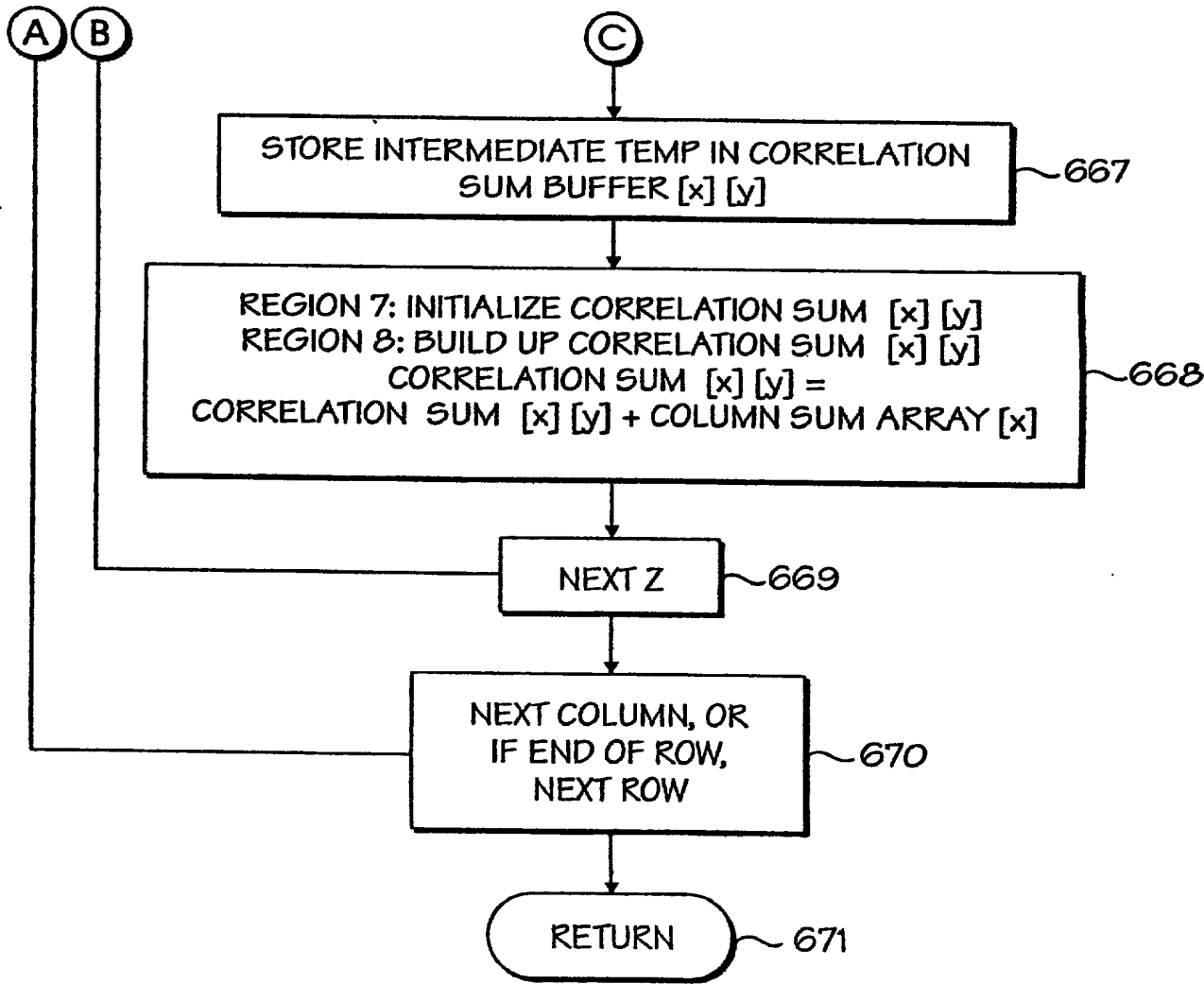


Fig. 25(B)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
(REGION 9)

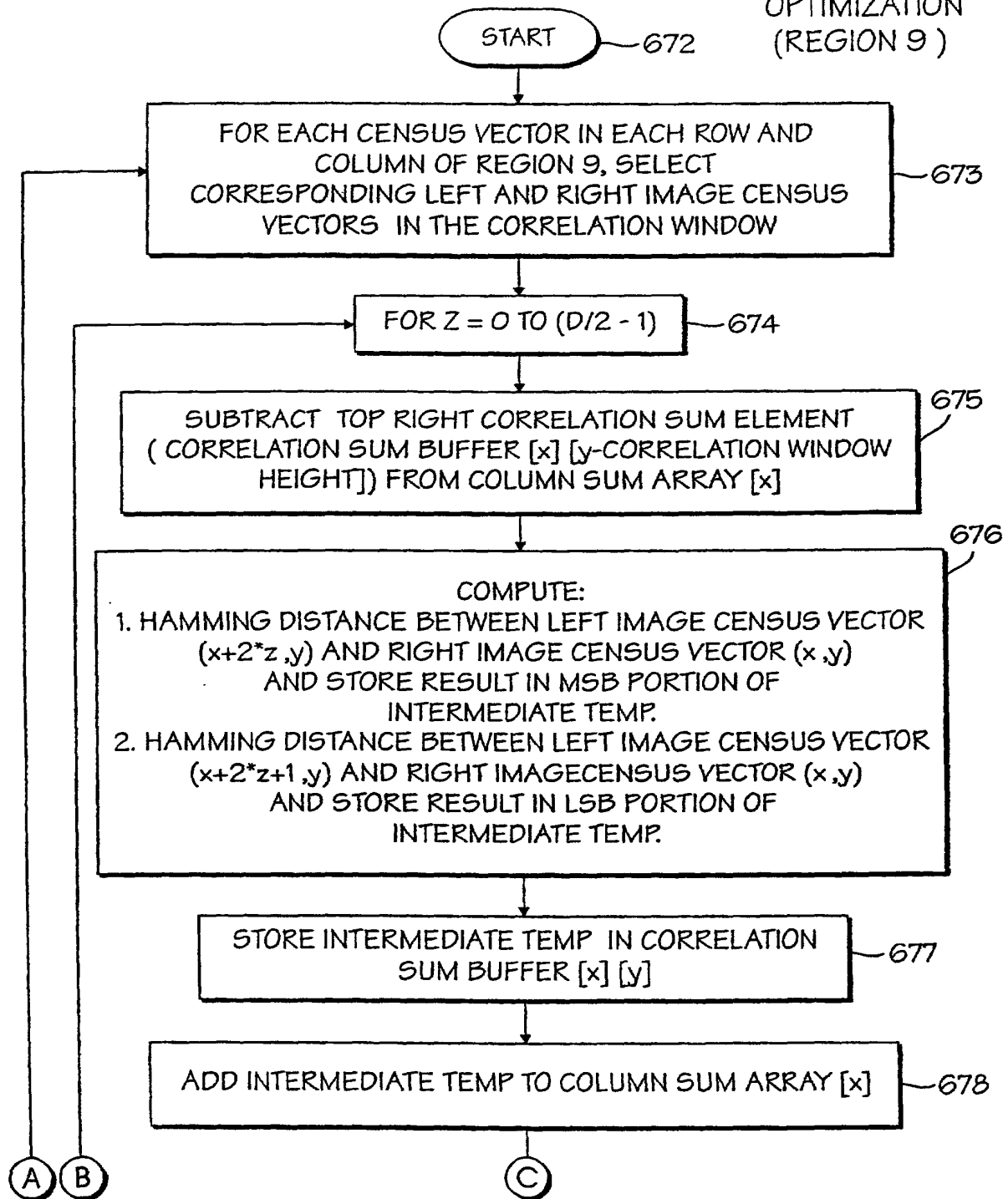


FIG. 26(A)

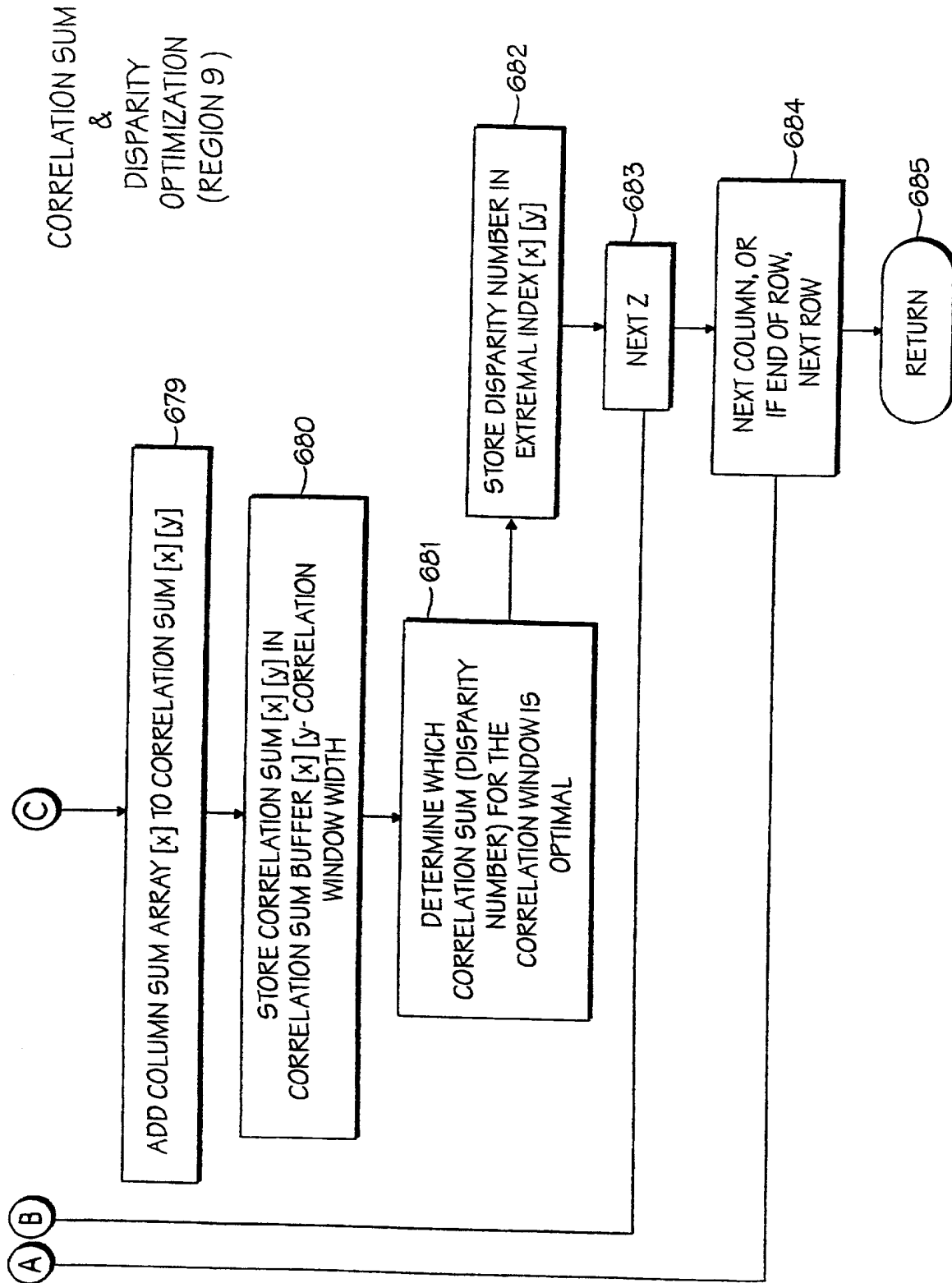


FIG. 26(B)

CORRELATION SUM  
&  
DISPARITY OPTIMIZATION  
(REGION 10)

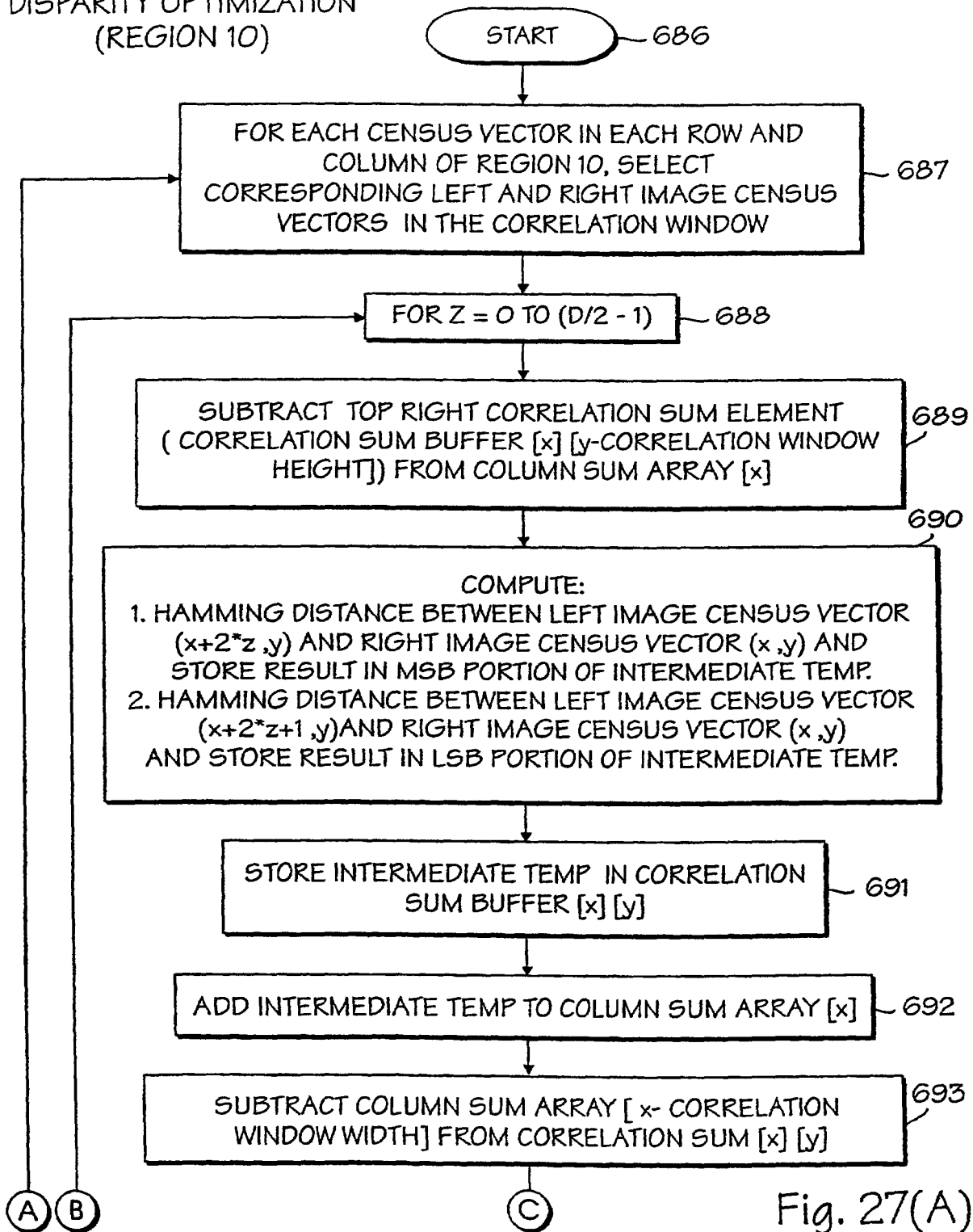


Fig. 27(A)

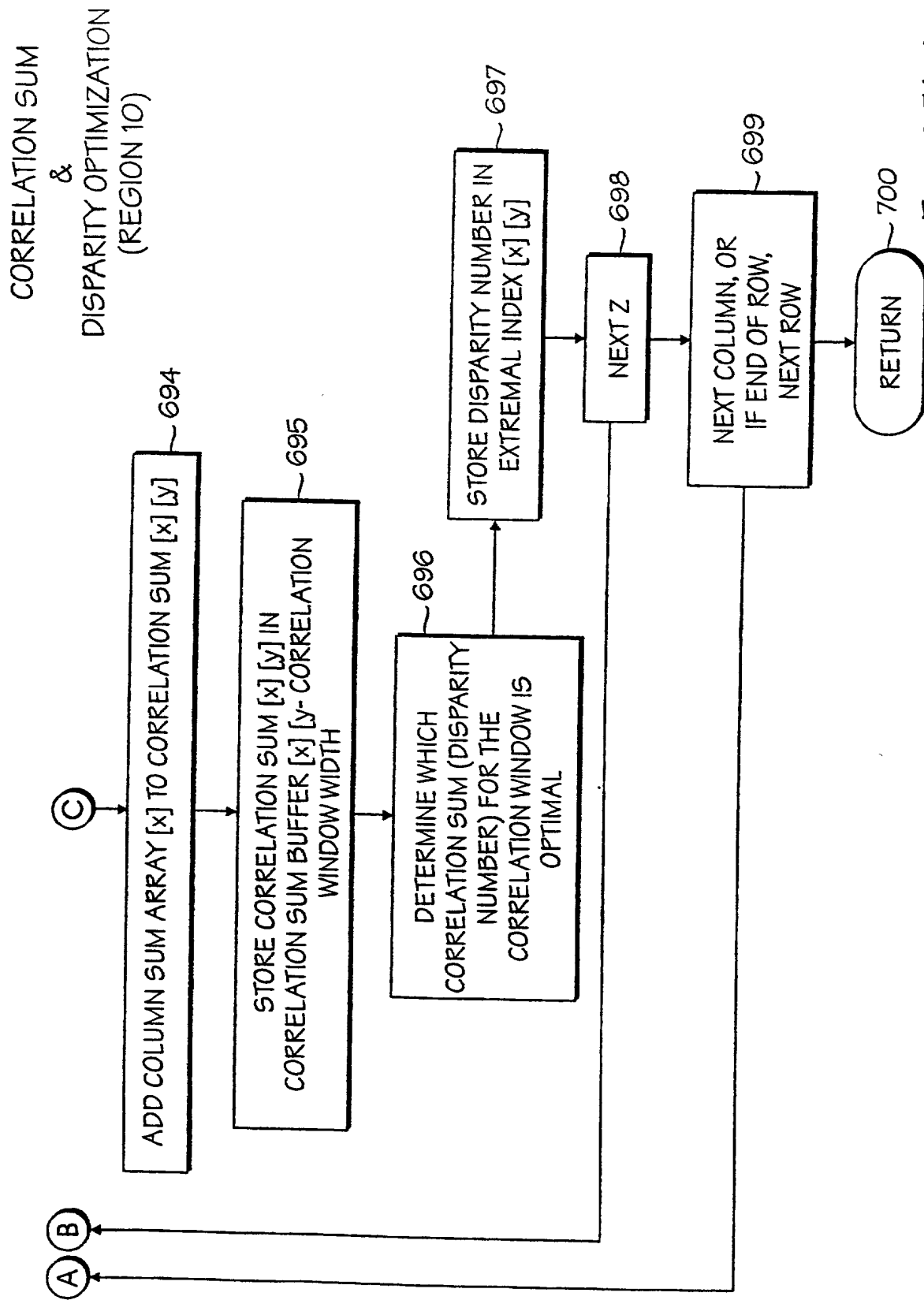


Fig. 27(B)



1002032.1 233000T

INTEREST  
OPERATION

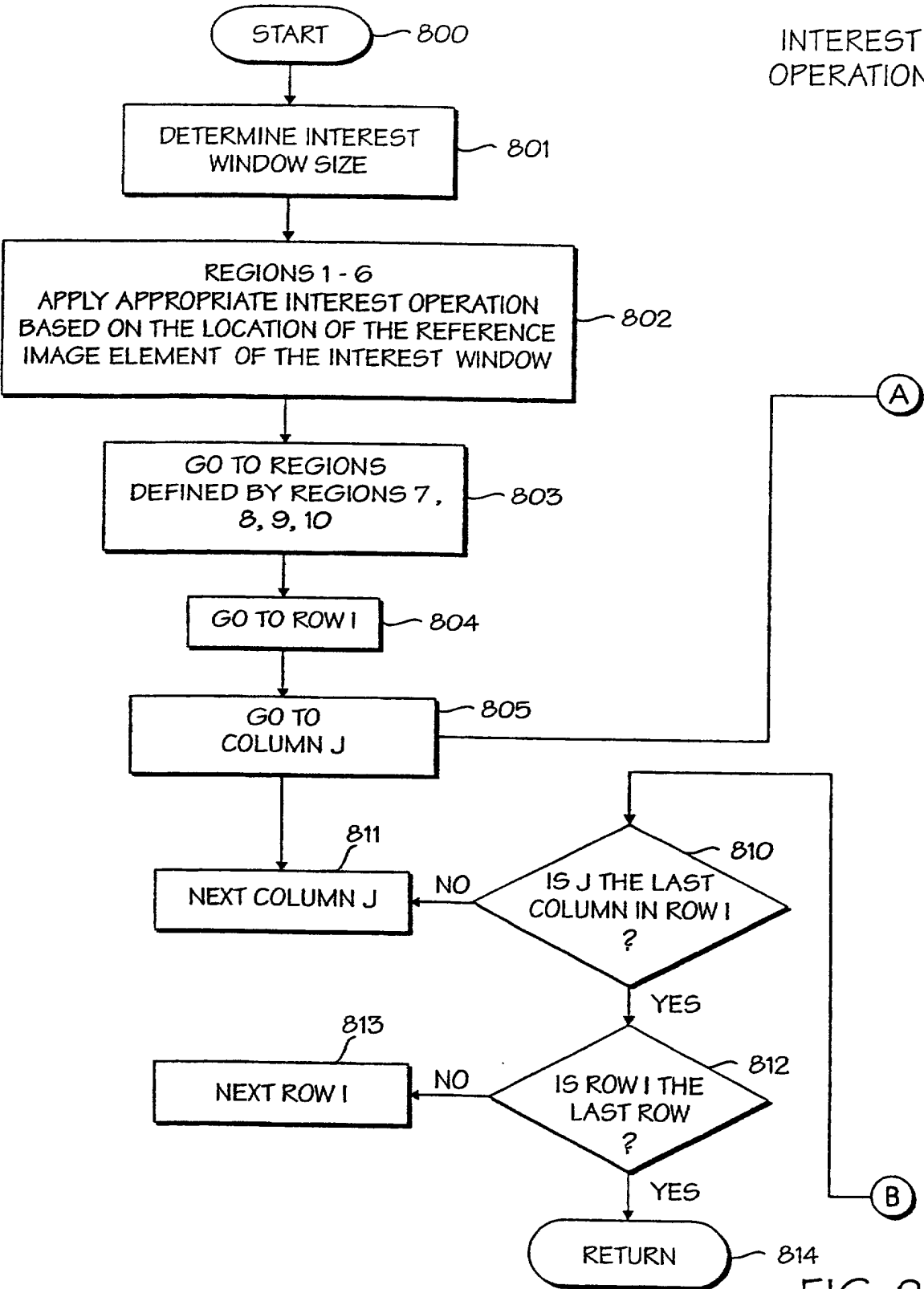


FIG. 28(A)

# INTEREST OPERATION

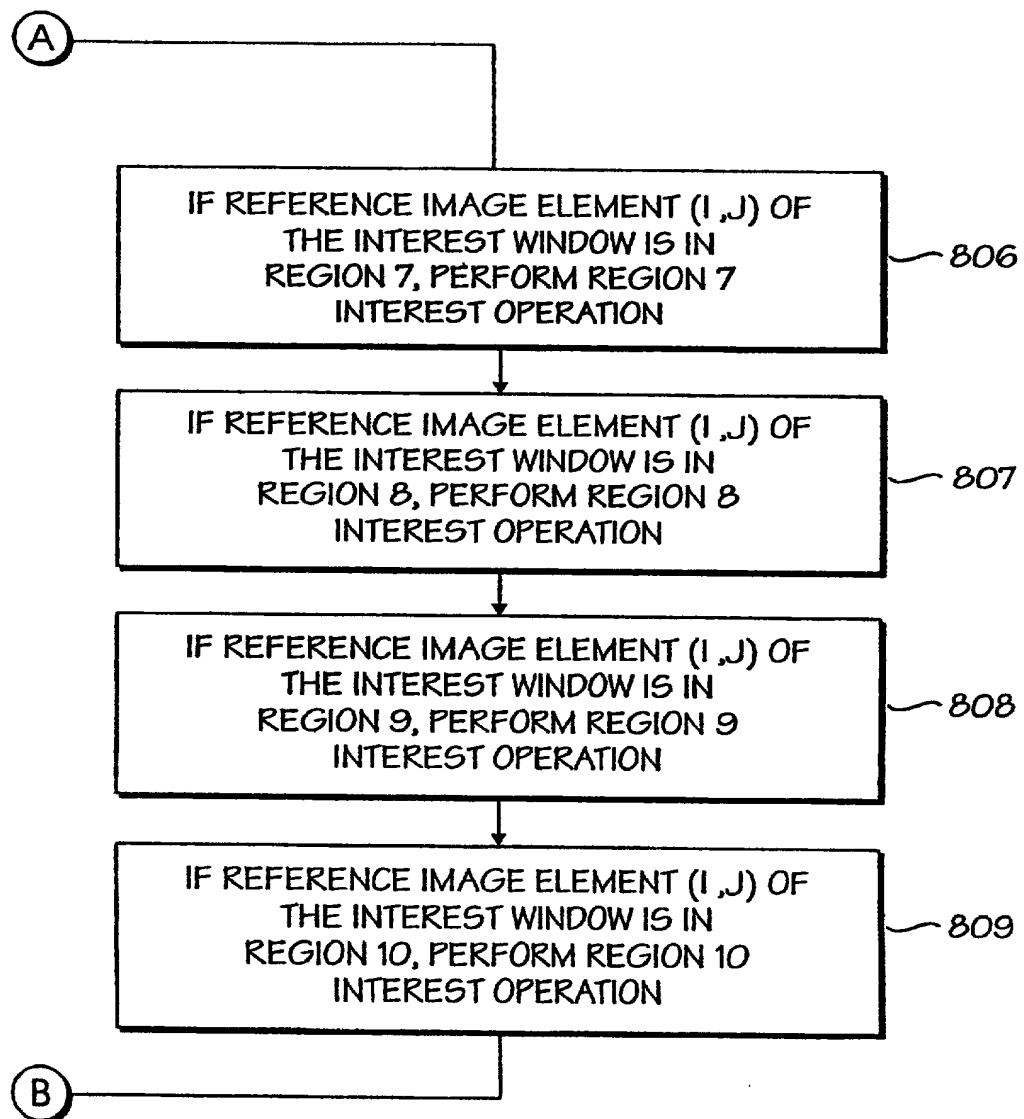


FIG. 28(B)

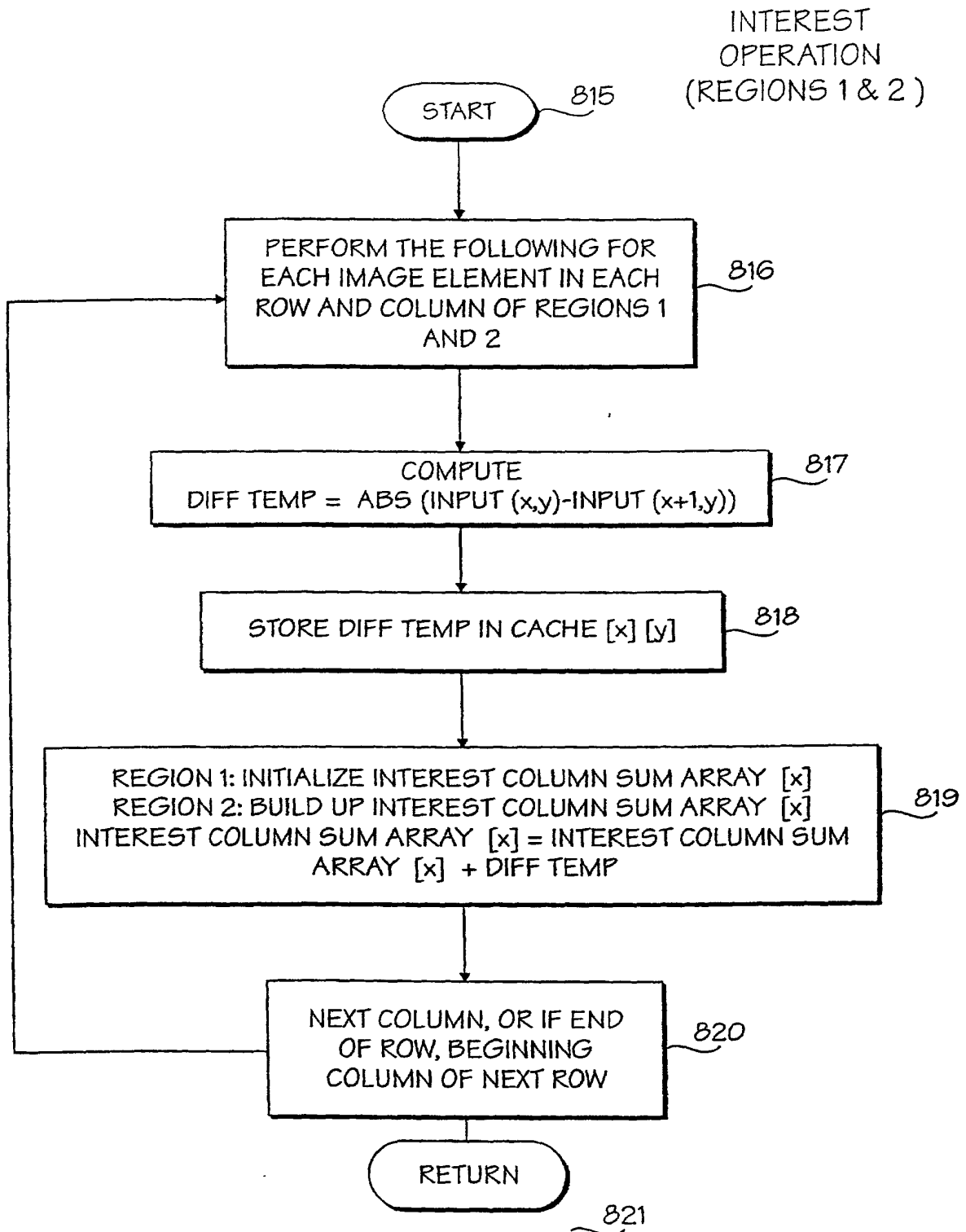


FIG. 29

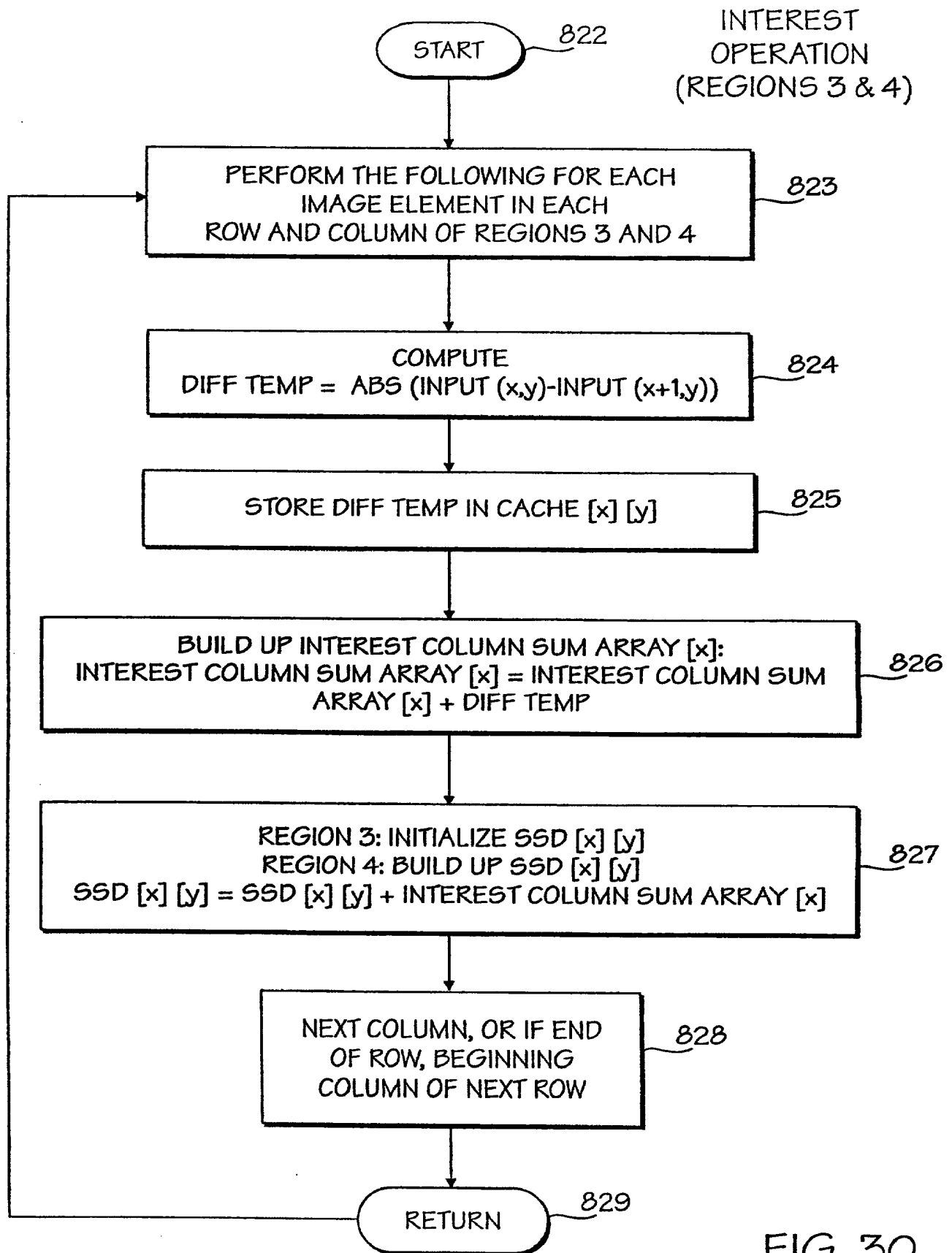


FIG. 30

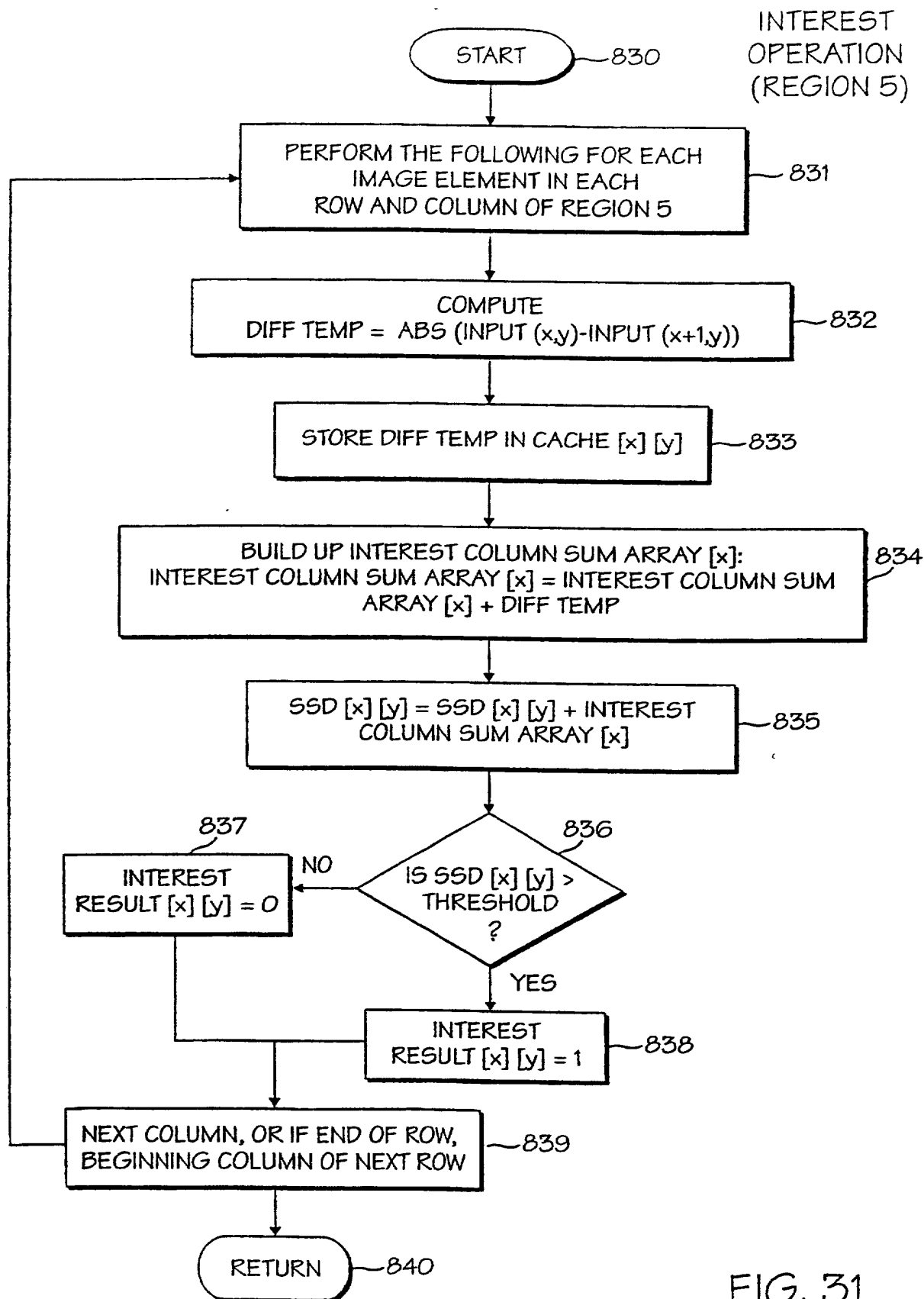


FIG. 31

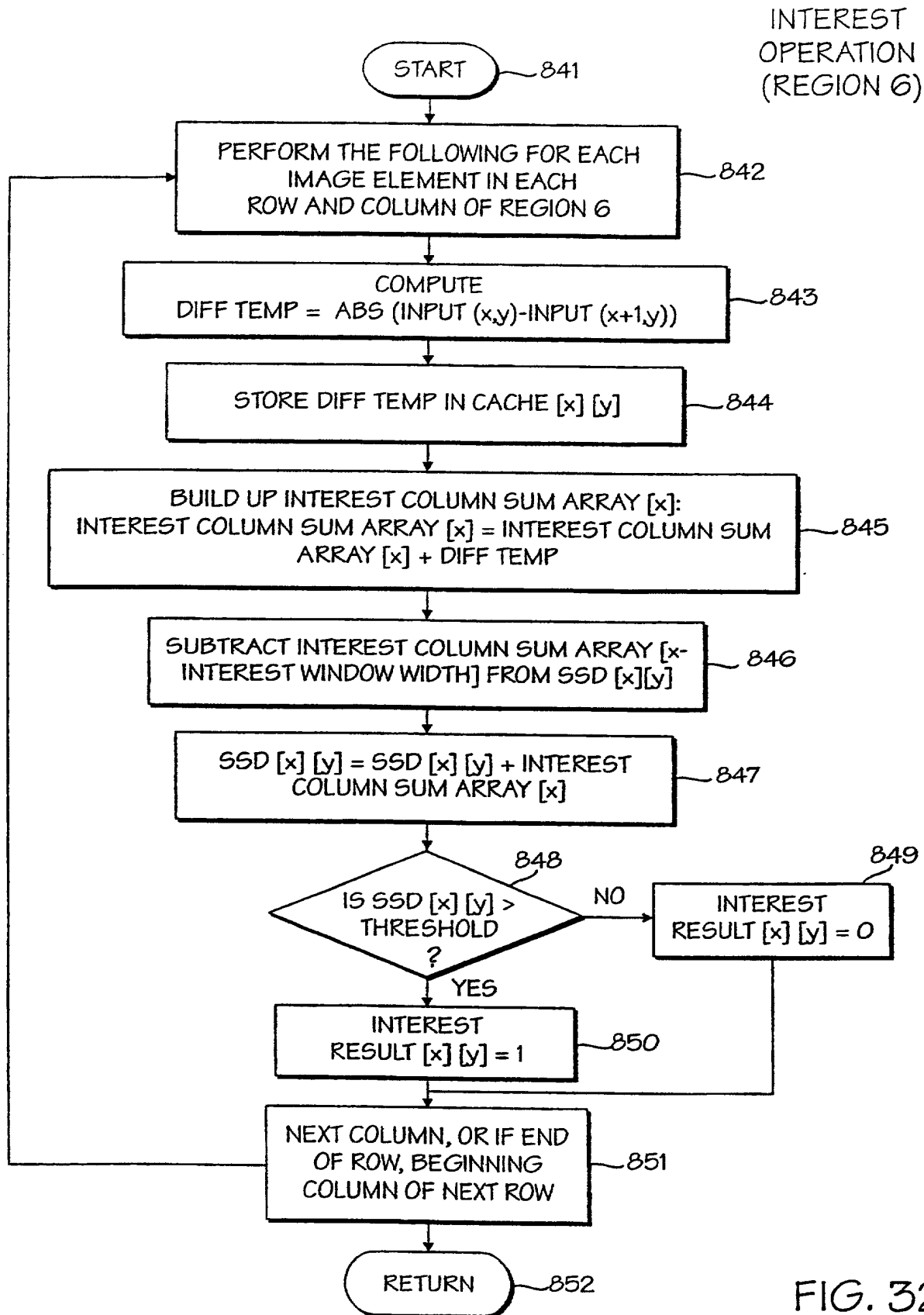


FIG. 32

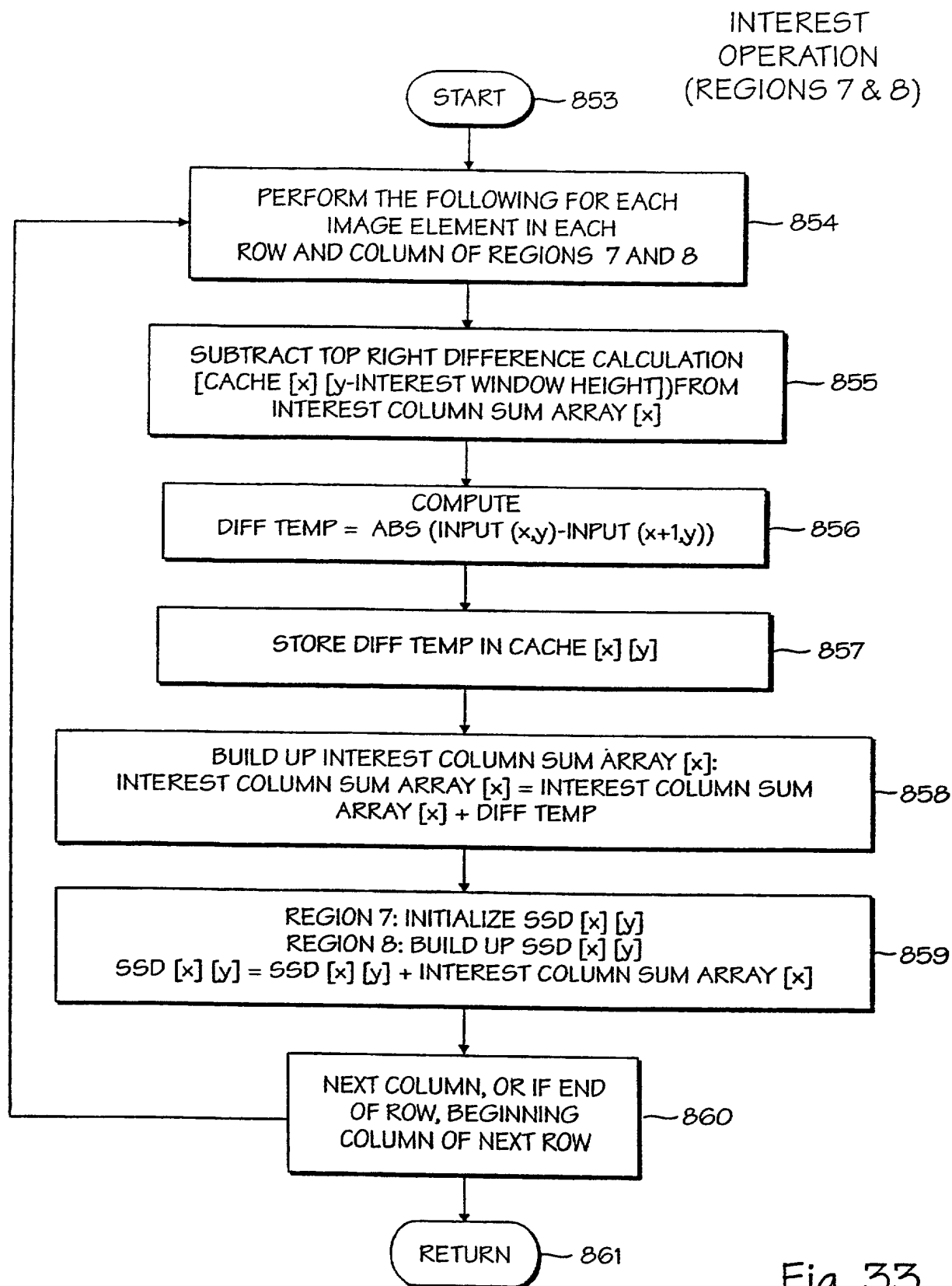


Fig. 33

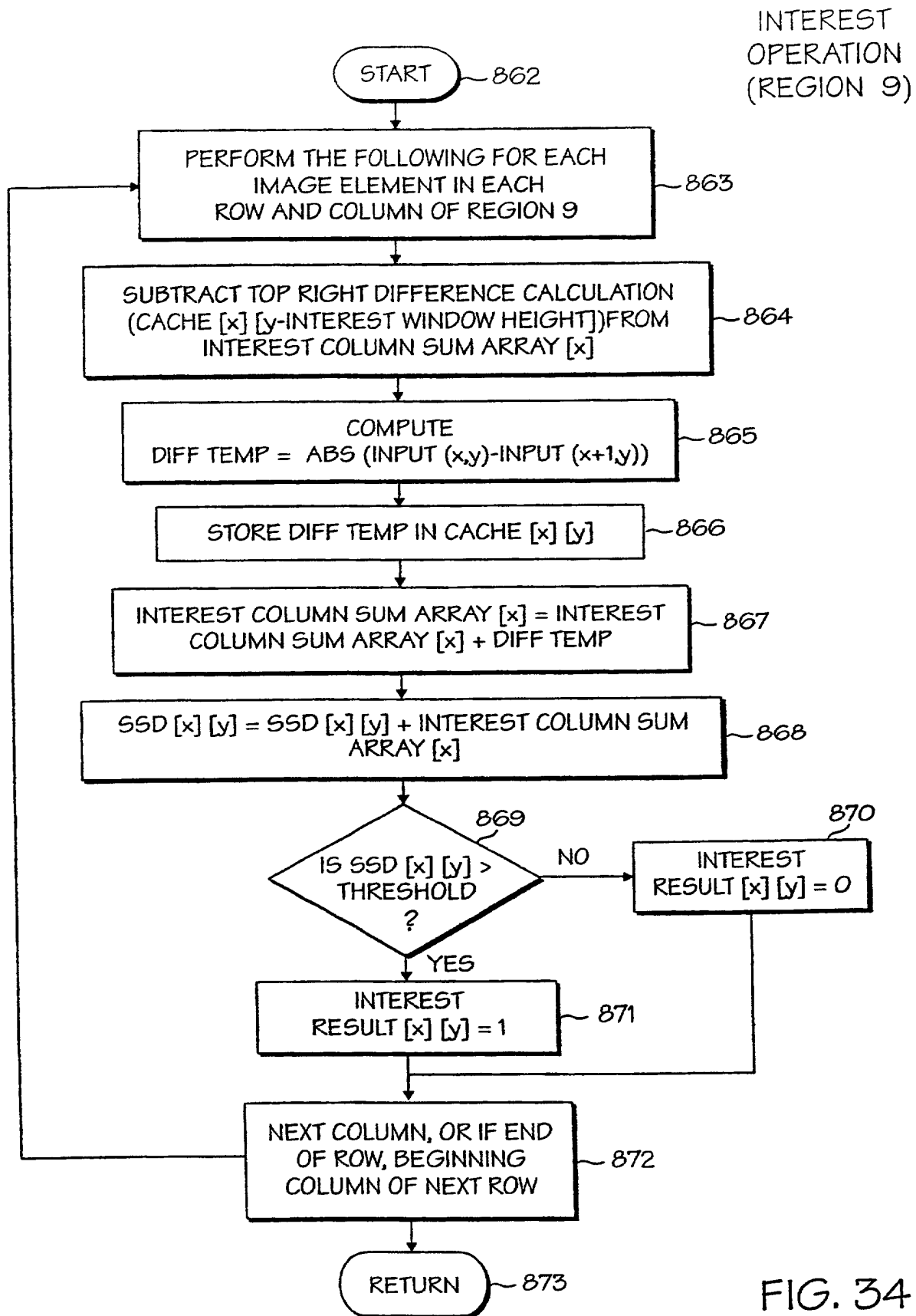


FIG. 34



INTEREST  
OPERATION  
(REGION 10)

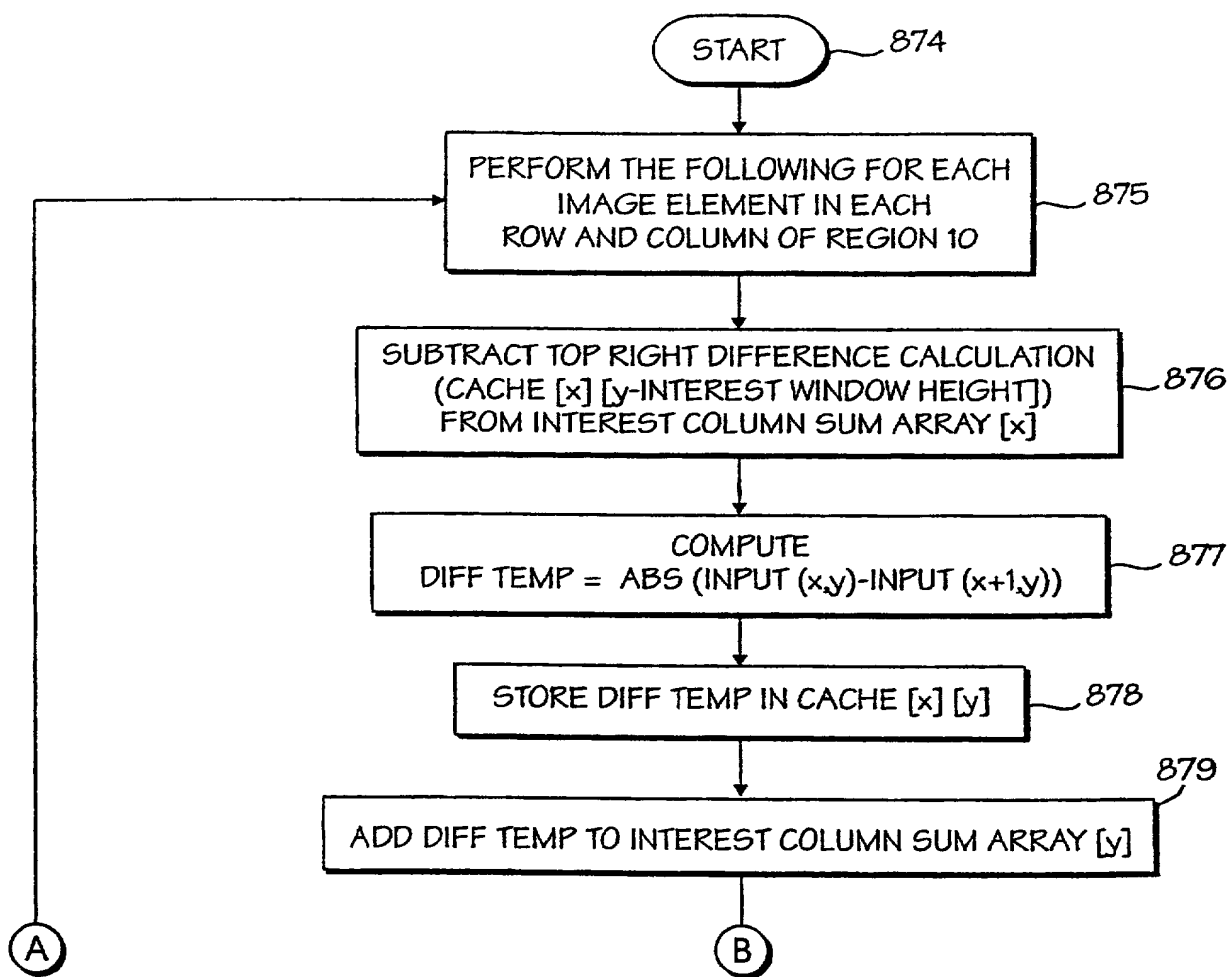


Fig. 35(A)

INTEREST  
OPERATION  
(REGION 10)

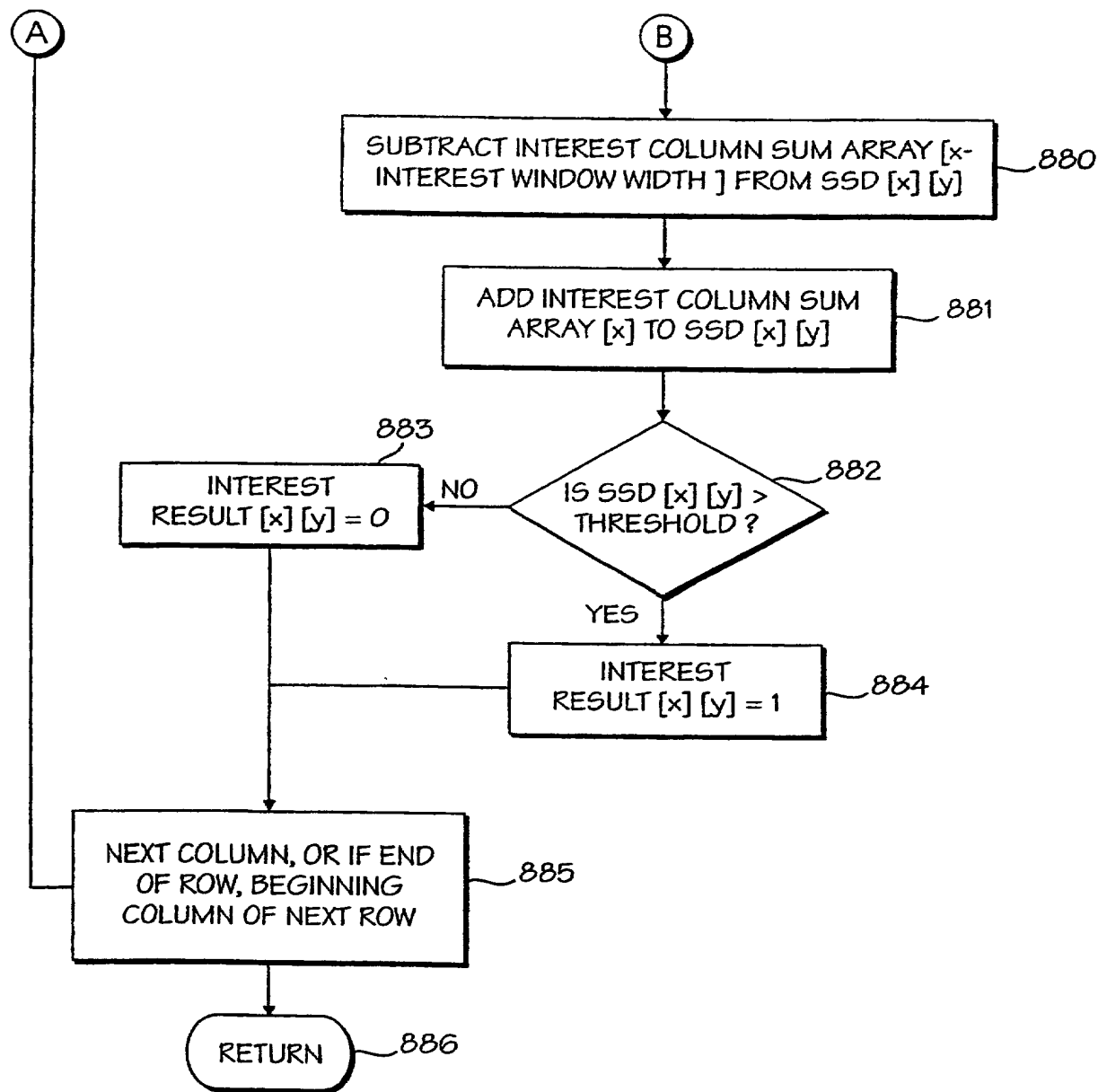
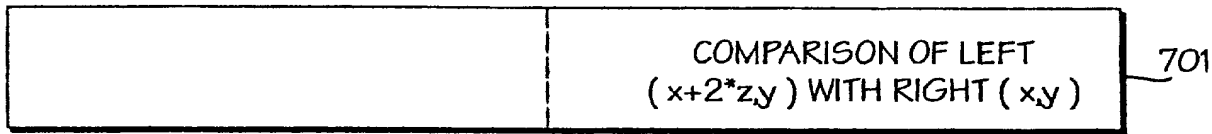


Fig. 35(B)

FIG. 36

DATA PACKING

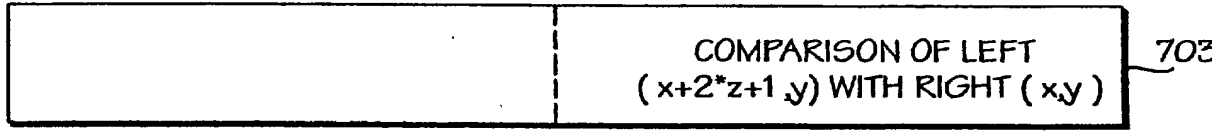
INTERMEDIATE TEMP 1



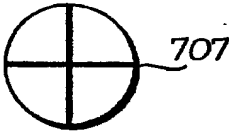
INTERMEDIATE TEMP 2



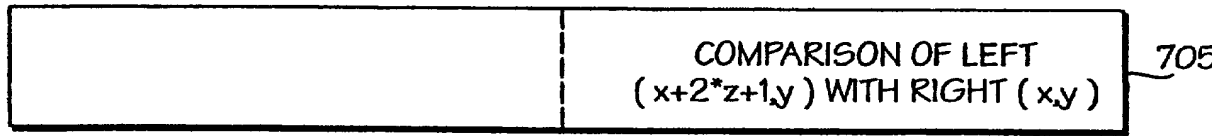
INTERMEDIATE TEMP 1



INTERMEDIATE TEMP 2



INTERMEDIATE TEMP 1



INTERMEDIATE TEMP 1

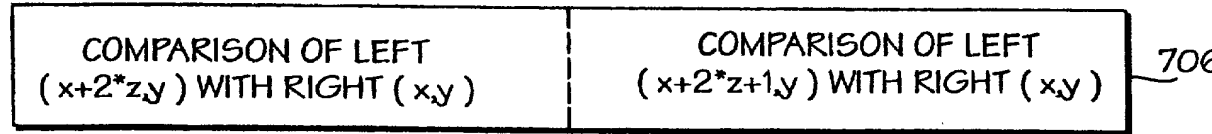


FIG. 36

# LEFT - RIGHT CONSISTENCY CHECK

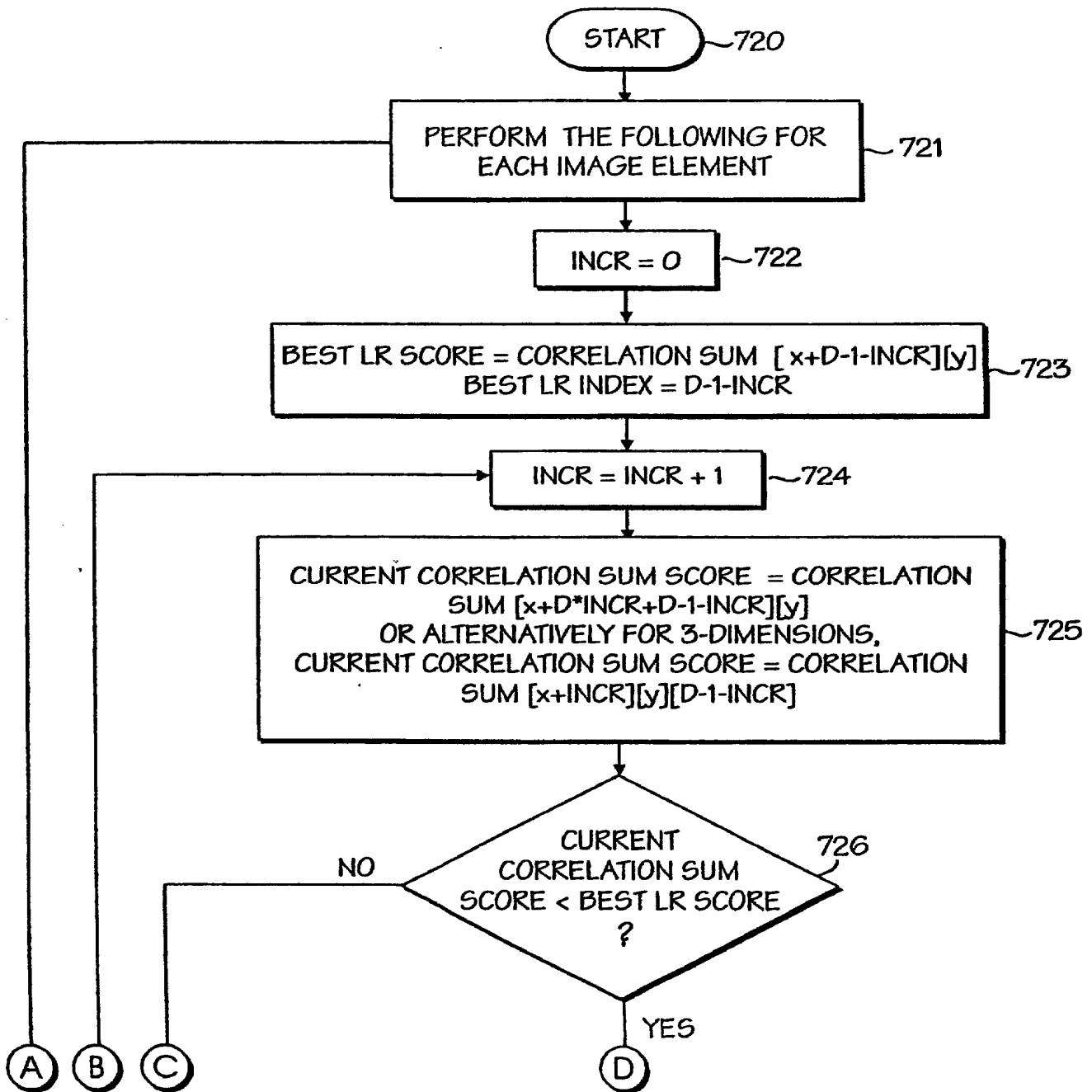


FIG. 37(A)

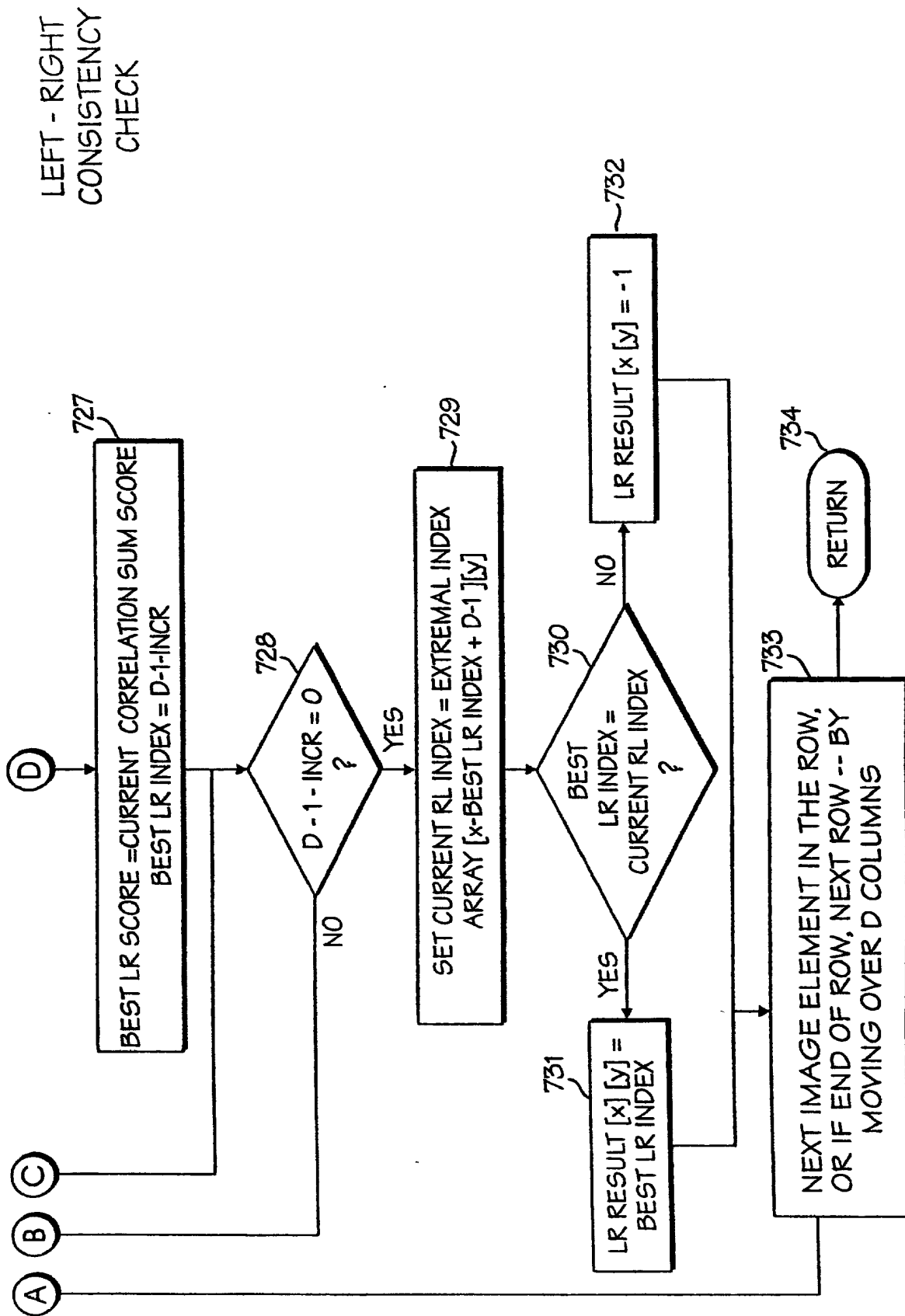
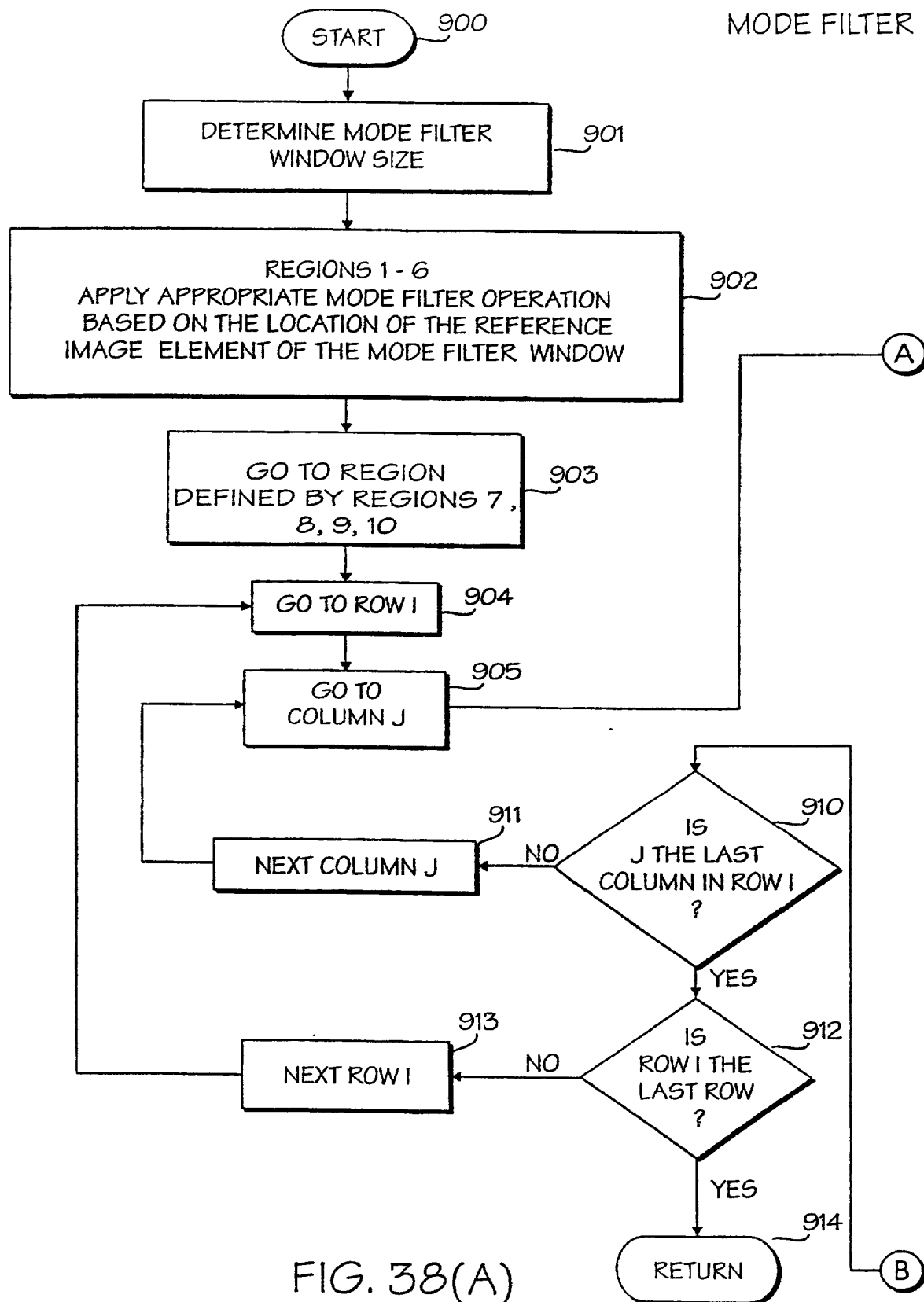


FIG. 37(B)



# MODE FILTER

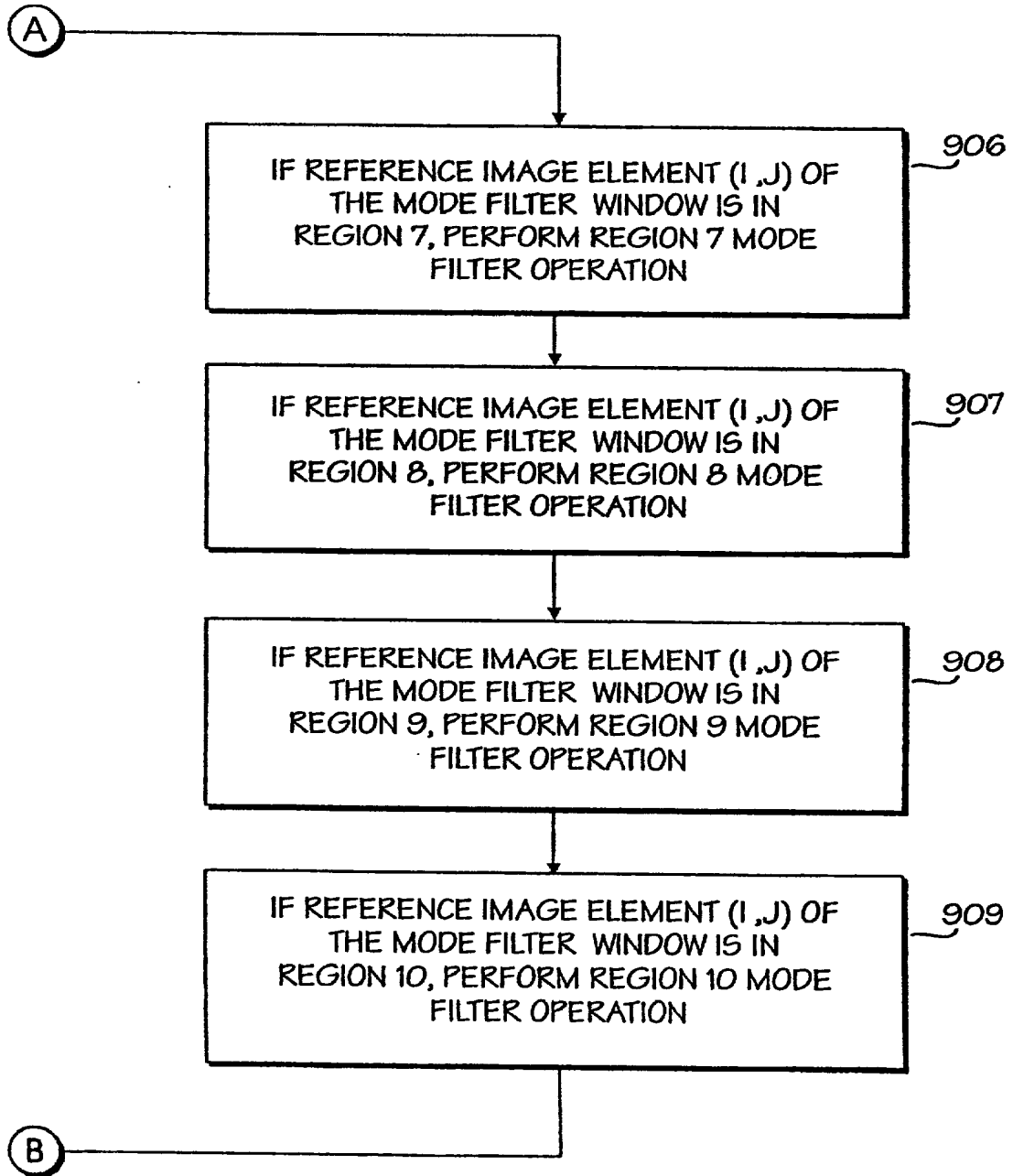


FIG. 38(B)

MODE FILTER  
( REGIONS 1 & 2 )

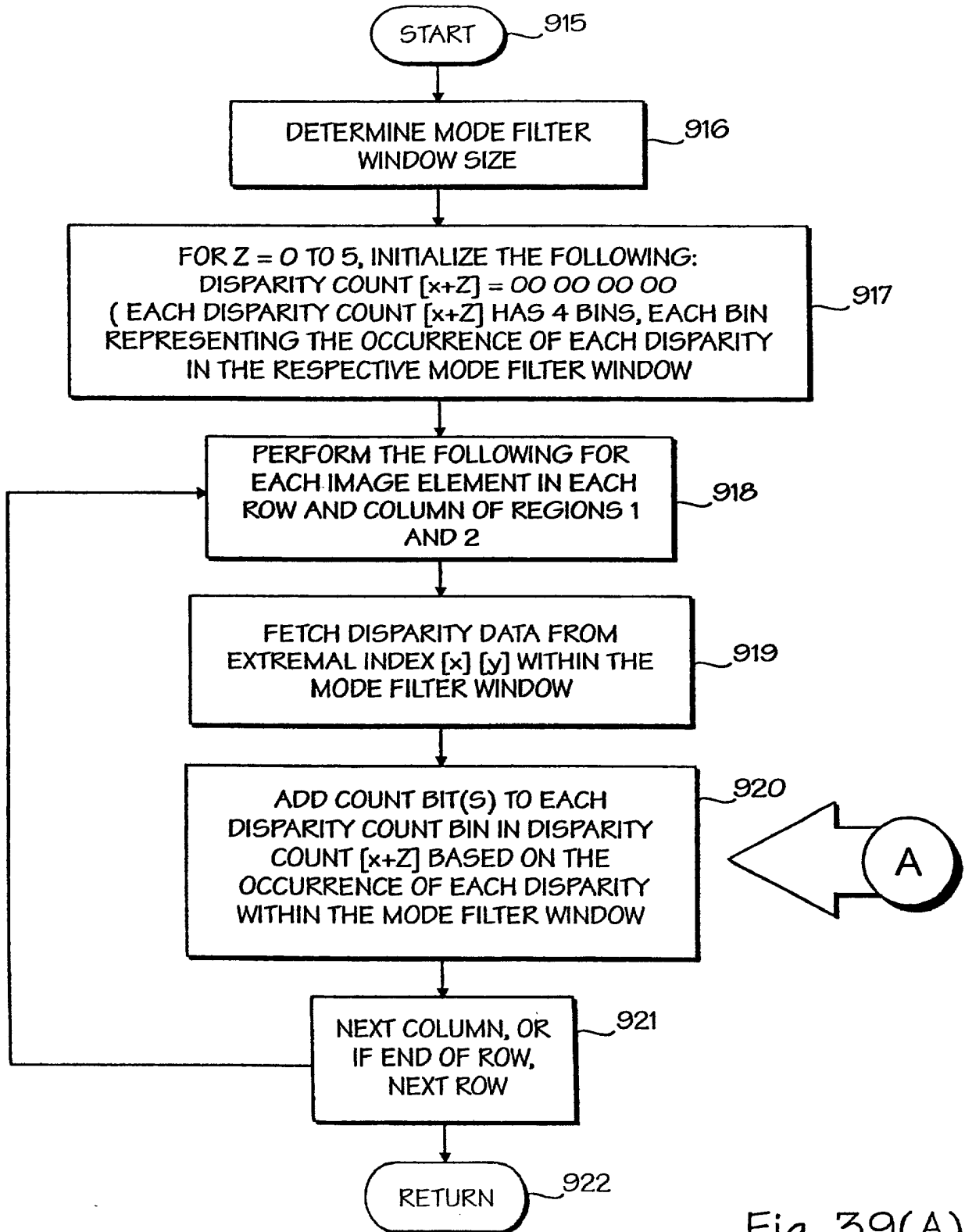


Fig. 39(A)



1002086 121401

MODE FILTER  
( REGIONS 1 & 2 )

A

<u>DISP</u>		<u>DISPARITY COUNT [x+Z]</u>
0	[x]	<span>00</span> 00 00 00
1	[x]	00 <span>00</span> 00 00
2	[x]	00 00 <span>00</span> 00
3	[x]	00 00 00 <span>00</span>
4	[x+1]	<span>00</span> 00 00 00
5	[x+1]	00 <span>00</span> 00 00
6	[x+1]	00 00 <span>00</span> 00
7	[x+1]	00 00 00 <span>00</span>
8	[x+2]	<span>00</span> 00 00 00
9	[x+2]	00 <span>00</span> 00 00
10	[x+2]	00 00 <span>00</span> 00
11	[x+2]	00 00 00 <span>00</span>
12	[x+3]	<span>00</span> 00 00 00
13	[x+3]	00 <span>00</span> 00 00
14	[x+3]	00 00 <span>00</span> 00
15	[x+3]	00 00 00 <span>00</span>
16	[x+4]	<span>00</span> 00 00 00
17	[x+4]	00 <span>00</span> 00 00
18	[x+4]	00 00 <span>00</span> 00
19	[x+4]	00 00 00 <span>00</span>
20	[x+5]	<span>00</span> 00 00 00
21	[x+5]	00 <span>00</span> 00 00
22	[x+5]	00 00 <span>00</span> 00
23	[x+5]	00 00 00 <span>00</span>

923

Fig. 39(B)

MODE FILTER  
( REGIONS 3 & 4)

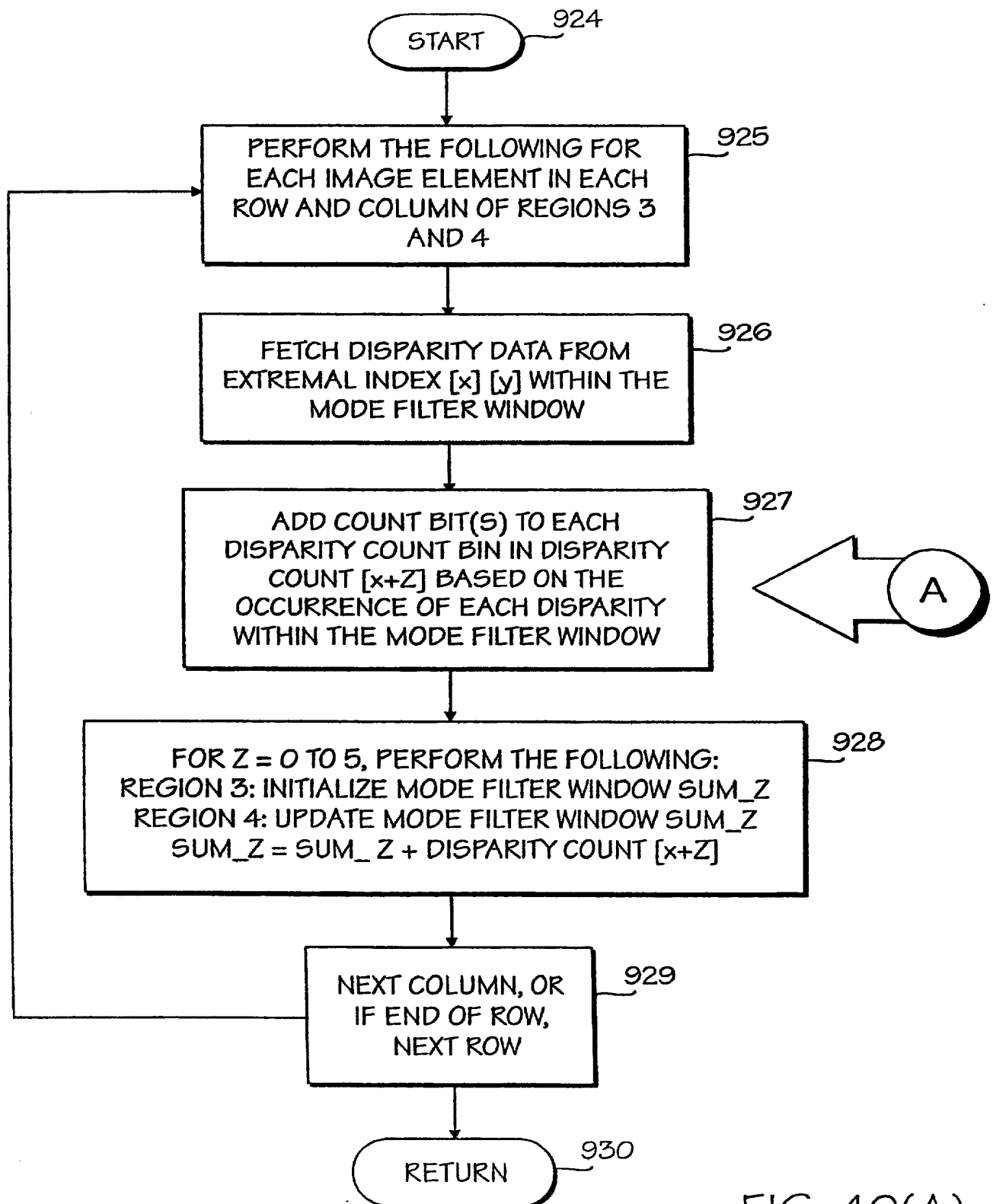


FIG. 40(A)

MODE FILTER  
( REGIONS 3 & 4)

A

<u>DISP</u>		<u>DISPARITY COUNT [x+Z]</u>
0	[x]	<span style="border: 1px solid black;">00</span> 00 00 00
1	[x]	00 <span style="border: 1px solid black;">00</span> 00 00
2	[x]	00 00 <span style="border: 1px solid black;">00</span> 00
3	[x]	00 00 00 <span style="border: 1px solid black;">00</span>
4	[x+1]	<span style="border: 1px solid black;">00</span> 00 00 00
5	[x+1]	00 <span style="border: 1px solid black;">00</span> 00 00
6	[x+1]	00 00 <span style="border: 1px solid black;">00</span> 00
7	[x+1]	00 00 00 <span style="border: 1px solid black;">00</span>
8	[x+2]	<span style="border: 1px solid black;">00</span> 00 00 00
9	[x+2]	00 <span style="border: 1px solid black;">00</span> 00 00
10	[x+2]	00 00 <span style="border: 1px solid black;">00</span> 00
11	[x+2]	00 00 00 <span style="border: 1px solid black;">00</span>
12	[x+3]	<span style="border: 1px solid black;">00</span> 00 00 00
13	[x+3]	00 <span style="border: 1px solid black;">00</span> 00 00
14	[x+3]	00 00 <span style="border: 1px solid black;">00</span> 00
15	[x+3]	00 00 00 <span style="border: 1px solid black;">00</span>
16	[x+4]	<span style="border: 1px solid black;">00</span> 00 00 00
17	[x+4]	00 <span style="border: 1px solid black;">00</span> 00 00
18	[x+4]	00 00 <span style="border: 1px solid black;">00</span> 00
19	[x+4]	00 00 00 <span style="border: 1px solid black;">00</span>
20	[x+5]	<span style="border: 1px solid black;">00</span> 00 00 00
21	[x+5]	00 <span style="border: 1px solid black;">00</span> 00 00
22	[x+5]	00 00 <span style="border: 1px solid black;">00</span> 00
23	[x+5]	00 00 00 <span style="border: 1px solid black;">00</span>

FIG. 40(B)

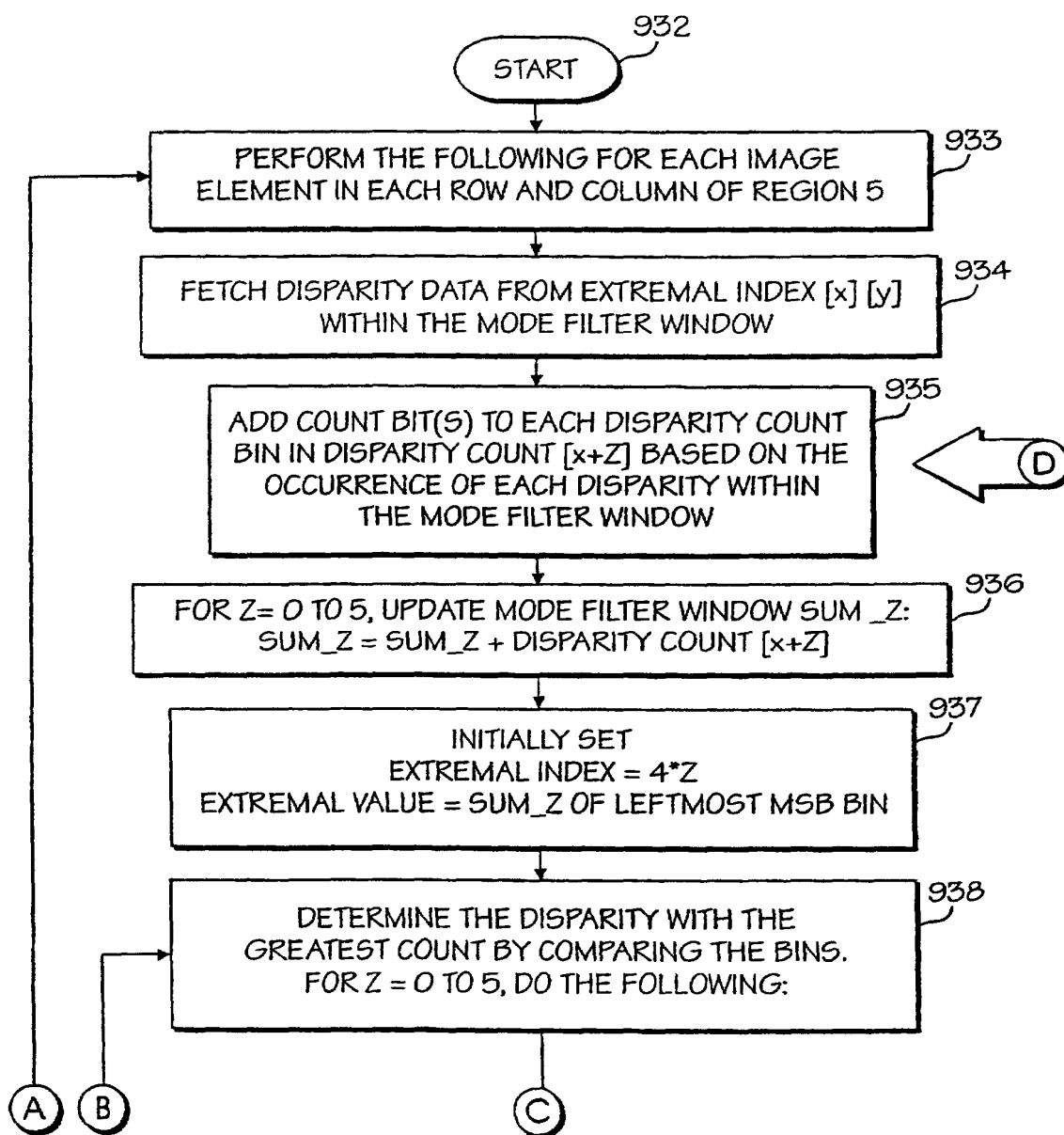


FIG. 41(A)

MODE FILTER  
( REGION 5)

(D)

DISP	DISPARITY COUNT [x+Z]
0	[x] <span style="border: 1px solid black;">00</span> 00 00 00
1	[x]    00 <span style="border: 1px solid black;">00</span> 00 00
2	[x]    00 00 <span style="border: 1px solid black;">00</span> 00
3	[x]    00 00 00 <span style="border: 1px solid black;">00</span>
4	[x+1] <span style="border: 1px solid black;">00</span> 00 00 00
5	[x+1] 00 <span style="border: 1px solid black;">00</span> 00 00
6	[x+1] 00 00 <span style="border: 1px solid black;">00</span> 00
7	[x+1] 00 00 00 <span style="border: 1px solid black;">00</span>
8	[x+2] <span style="border: 1px solid black;">00</span> 00 00 00
9	[x+2] 00 <span style="border: 1px solid black;">00</span> 00 00
10	[x+2] 00 00 <span style="border: 1px solid black;">00</span> 00
11	[x+2] 00 00 00 <span style="border: 1px solid black;">00</span>
12	[x+3] <span style="border: 1px solid black;">00</span> 00 00 00
13	[x+3] 00 <span style="border: 1px solid black;">00</span> 00 00
14	[x+3] 00 00 <span style="border: 1px solid black;">00</span> 00
15	[x+3] 00 00 00 <span style="border: 1px solid black;">00</span>
16	[x+4] <span style="border: 1px solid black;">00</span> 00 00 00
17	[x+4] 00 <span style="border: 1px solid black;">00</span> 00 00
18	[x+4] 00 00 <span style="border: 1px solid black;">00</span> 00
19	[x+4] 00 00 00 <span style="border: 1px solid black;">00</span>
20	[x+5] <span style="border: 1px solid black;">00</span> 00 00 00
21	[x+5] 00 <span style="border: 1px solid black;">00</span> 00 00
22	[x+5] 00 00 <span style="border: 1px solid black;">00</span> 00
23	[x+5] 00 00 00 <span style="border: 1px solid black;">00</span>

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FIG. 41(B)

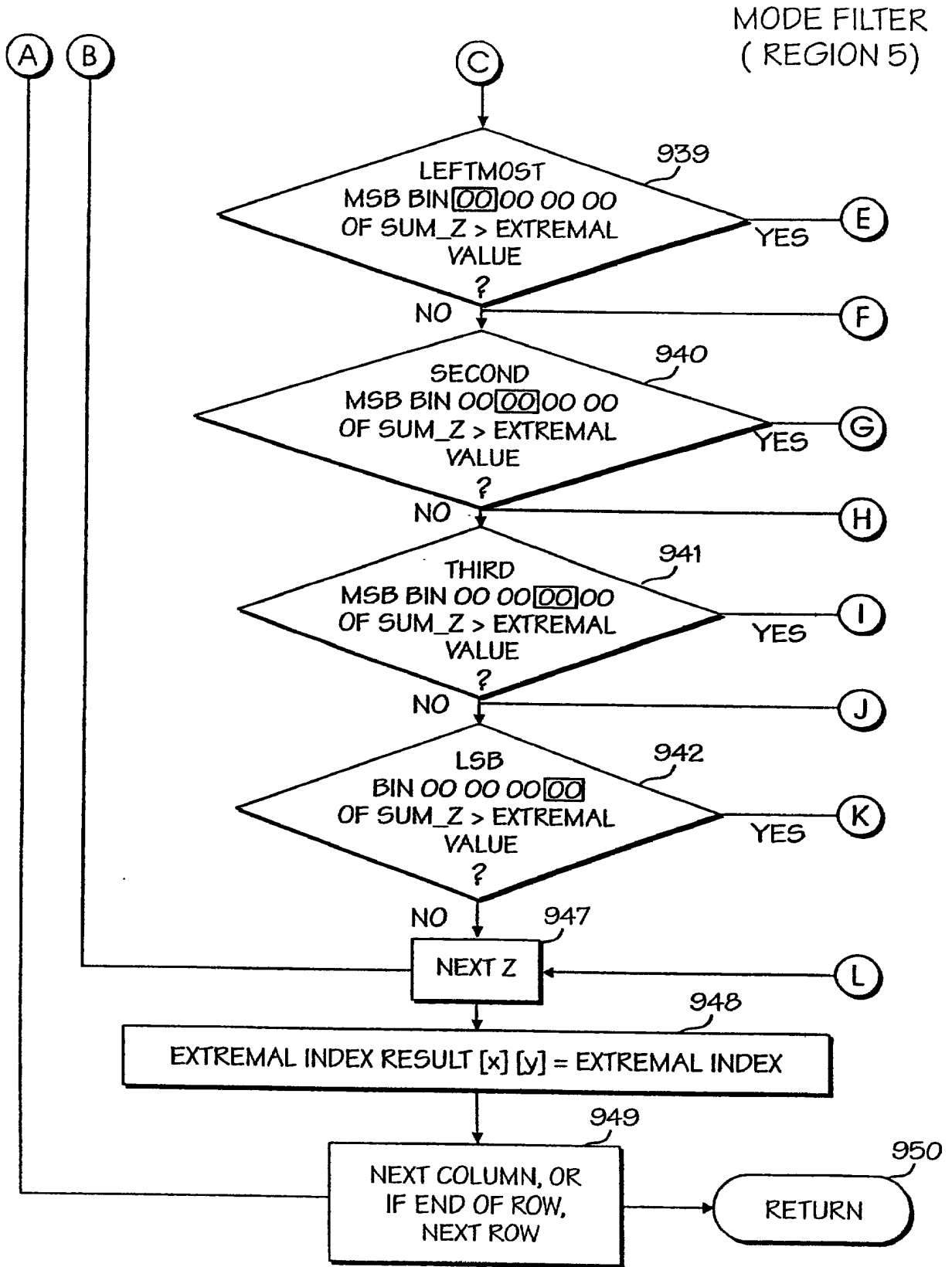


FIG. 41(C)

MODE FILTER  
( REGION 5)

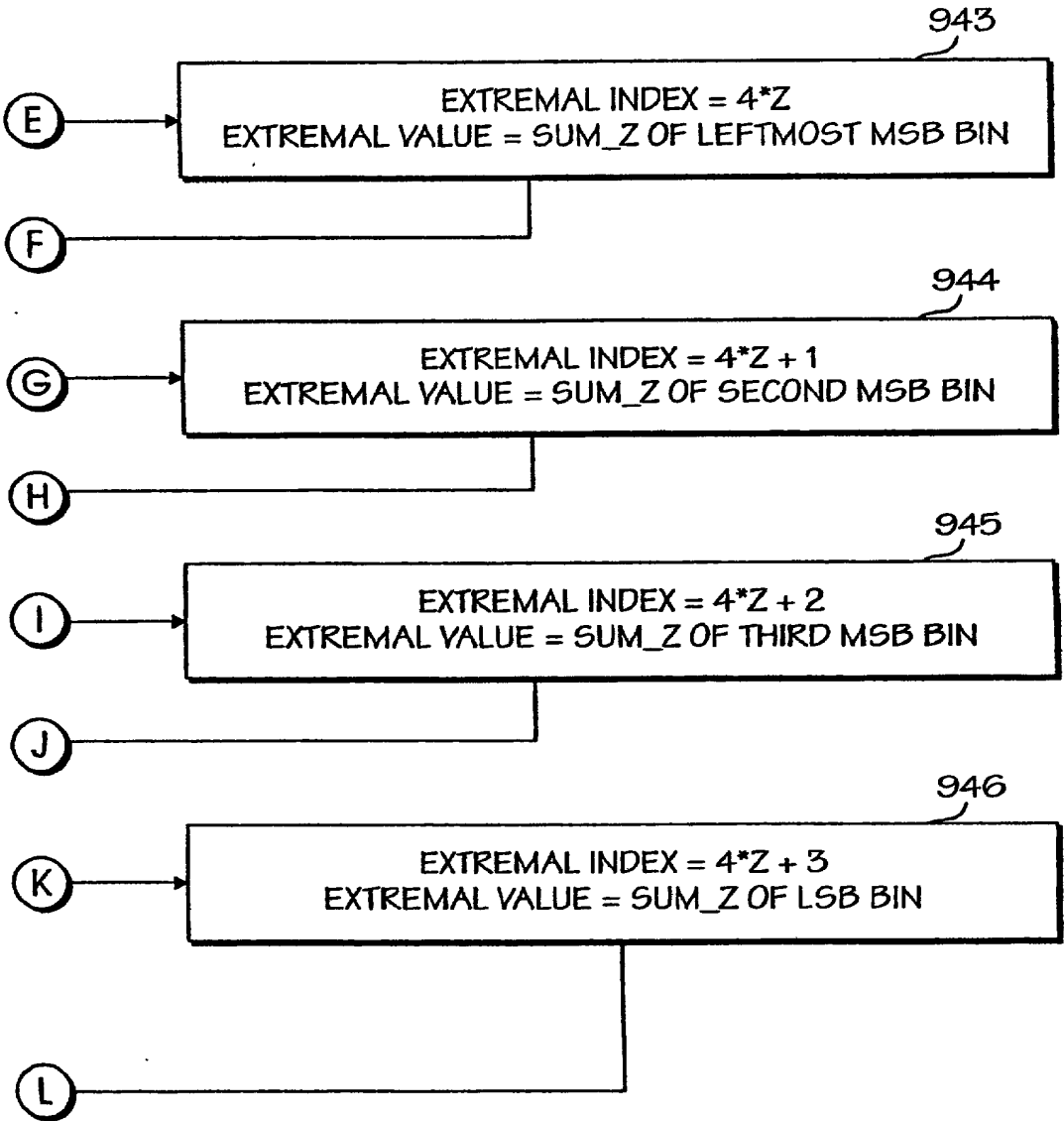


FIG. 41(D)

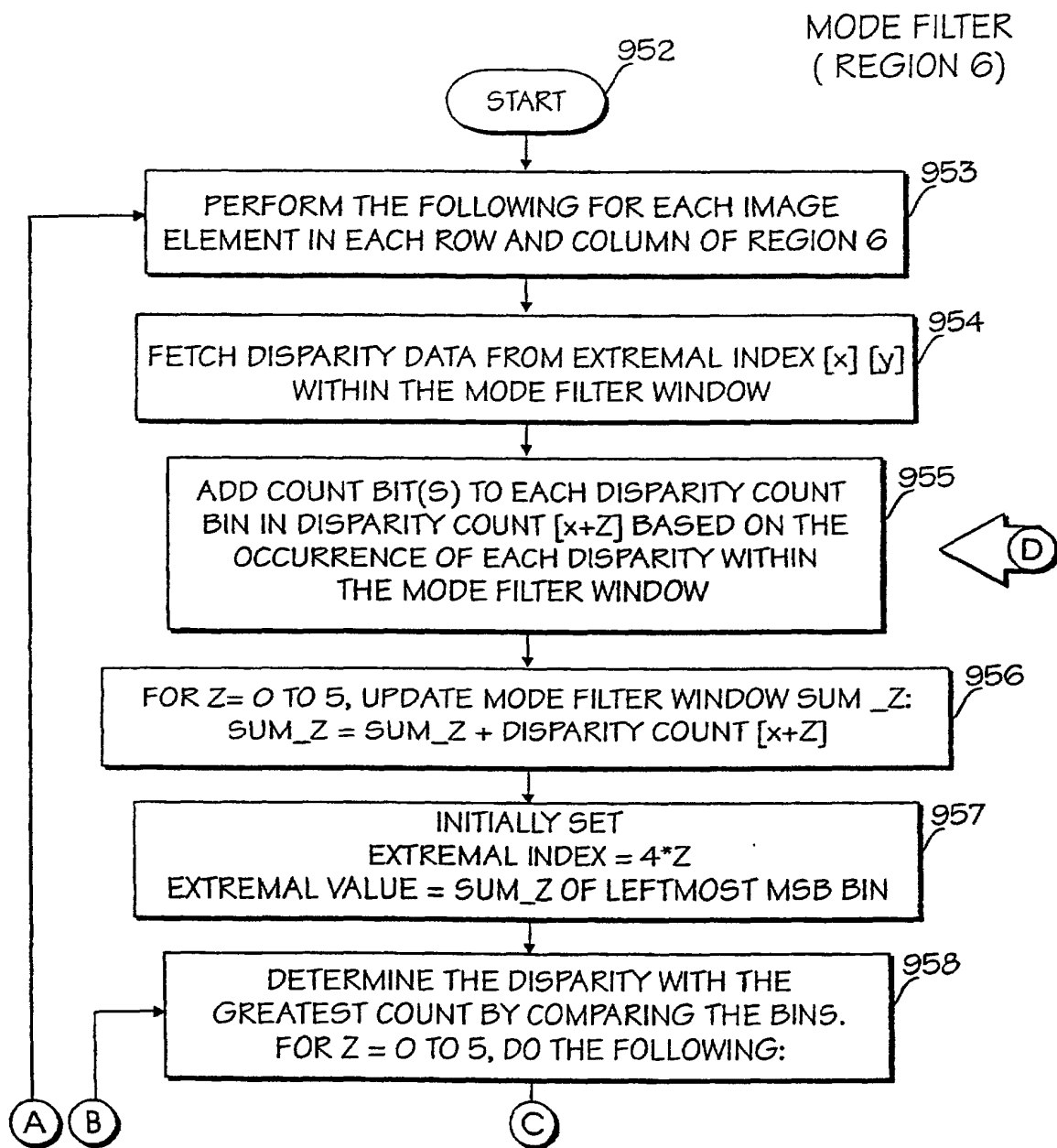


FIG. 42(A)



10020302 1001401

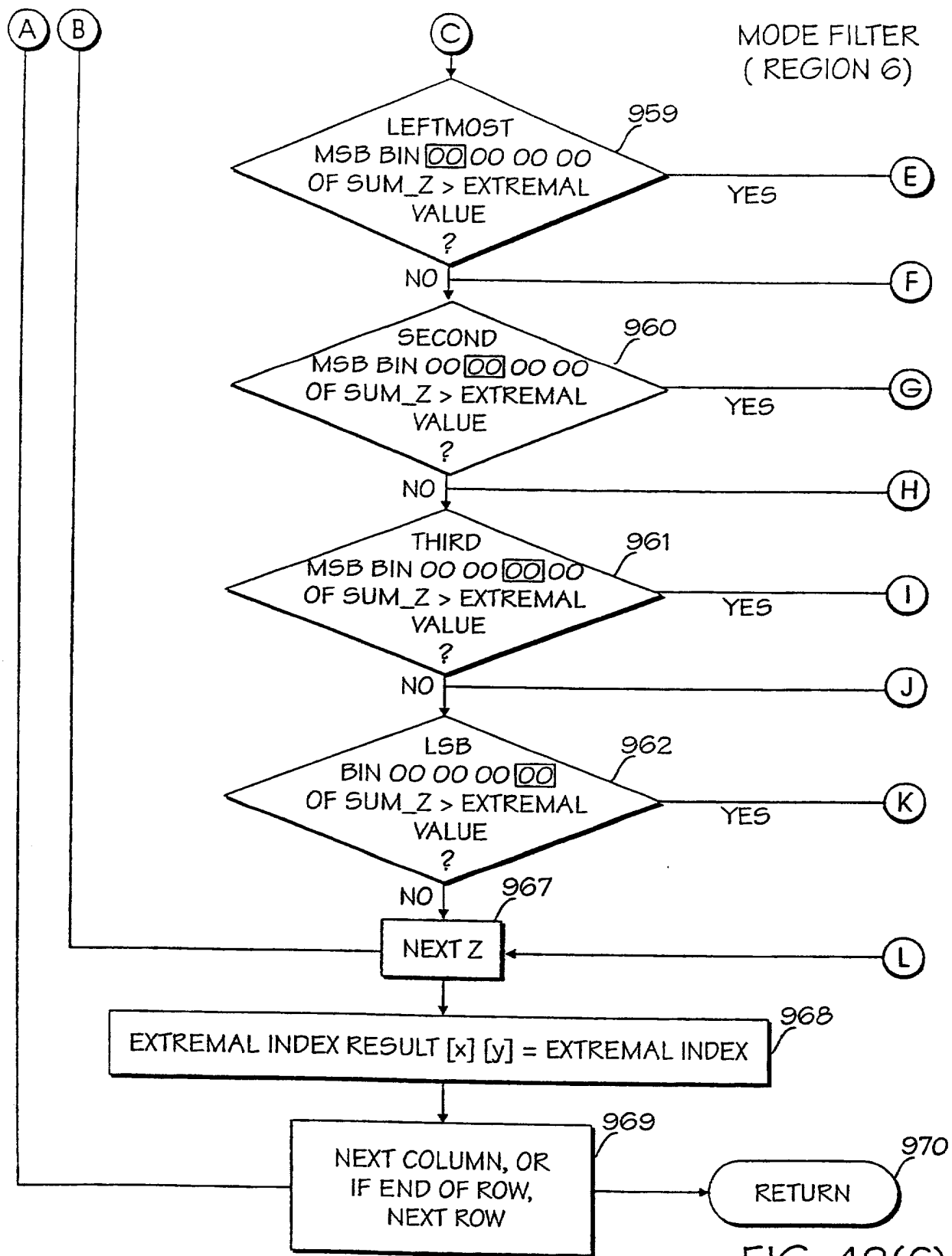
MODE FILTER  
( REGION 6)

DISP	DISPARITY COUNT [x+Z]			
0	[x]	<span>00</span>	00	00 00
1	[x]	00	<span>00</span>	00 00
2	[x]	00	00	<span>00</span> 00
3	[x]	00	00	00 <span>00</span>
4	[x+1]	<span>00</span>	00	00 00
5	[x+1]	00	<span>00</span>	00 00
6	[x+1]	00	00	<span>00</span> 00
7	[x+1]	00	00	00 <span>00</span>
8	[x+2]	<span>00</span>	00	00 00
9	[x+2]	00	<span>00</span>	00 00
10	[x+2]	00	00	<span>00</span> 00
11	[x+2]	00	00	00 <span>00</span>
12	[x+3]	<span>00</span>	00	00 00
13	[x+3]	00	<span>00</span>	00 00
14	[x+3]	00	00	<span>00</span> 00
15	[x+3]	00	00	00 <span>00</span>
16	[x+4]	<span>00</span>	00	00 00
17	[x+4]	00	<span>00</span>	00 00
18	[x+4]	00	00	<span>00</span> 00
19	[x+4]	00	00	00 <span>00</span>
20	[x+5]	<span>00</span>	00	00 00
21	[x+5]	00	<span>00</span>	00 00
22	[x+5]	00	00	<span>00</span> 00
23	[x+5]	00	00	00 <span>00</span>

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D

FIG. 42(B)



MODE FILTER  
( REGION 6 )

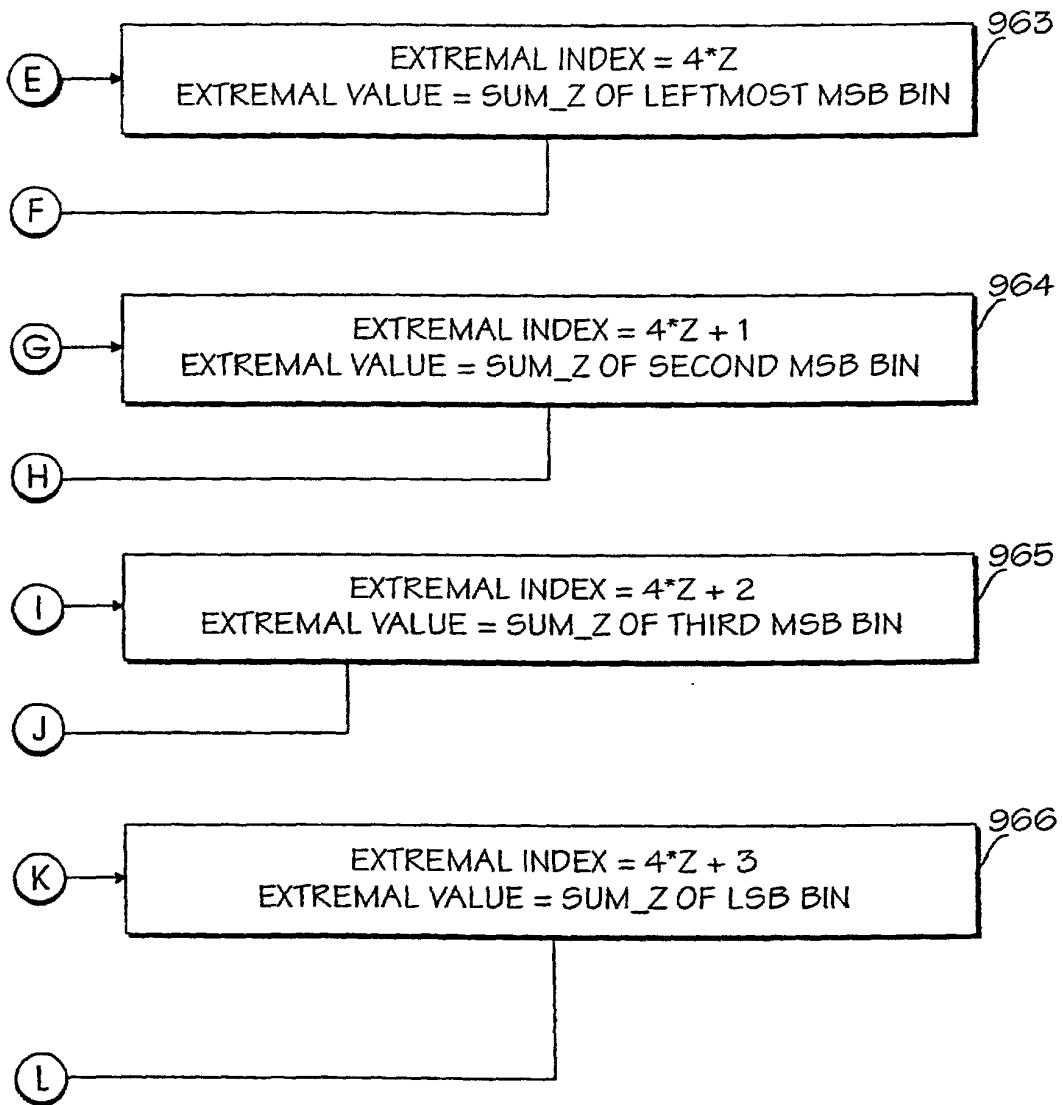


FIG. 42(D)

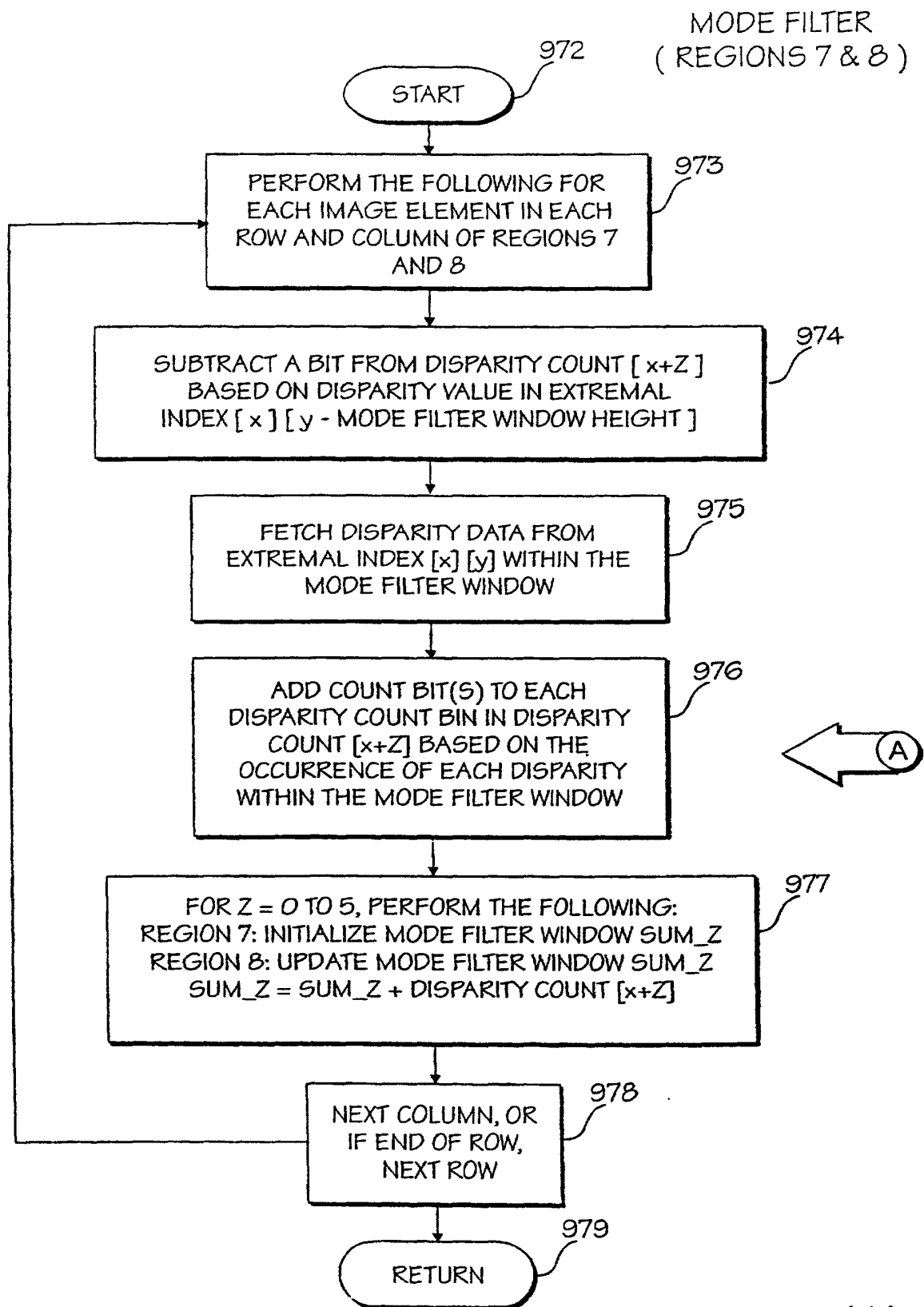


FIG. 43(A)

MODE FILTER  
( REGIONS 7 & 8 )

980

A

<u>DISP</u>		<u>DISPARITY COUNT [x+Z]</u>
0	[x]	<span style="border: 1px solid black;">00</span> 00 00 00
1	[x]	00 <span style="border: 1px solid black;">00</span> 00 00
2	[x]	00 00 <span style="border: 1px solid black;">00</span> 00
3	[x]	00 00 00 <span style="border: 1px solid black;">00</span>
4	[x+1]	<span style="border: 1px solid black;">00</span> 00 00 00
5	[x+1]	00 <span style="border: 1px solid black;">00</span> 00 00
6	[x+1]	00 00 <span style="border: 1px solid black;">00</span> 00
7	[x+1]	00 00 00 <span style="border: 1px solid black;">00</span>
8	[x+2]	<span style="border: 1px solid black;">00</span> 00 00 00
9	[x+2]	00 <span style="border: 1px solid black;">00</span> 00 00
10	[x+2]	00 00 <span style="border: 1px solid black;">00</span> 00
11	[x+2]	00 00 00 <span style="border: 1px solid black;">00</span>
12	[x+3]	<span style="border: 1px solid black;">00</span> 00 00 00
13	[x+3]	00 <span style="border: 1px solid black;">00</span> 00 00
14	[x+3]	00 00 <span style="border: 1px solid black;">00</span> 00
15	[x+3]	00 00 00 <span style="border: 1px solid black;">00</span>
16	[x+4]	<span style="border: 1px solid black;">00</span> 00 00 00
17	[x+4]	00 <span style="border: 1px solid black;">00</span> 00 00
18	[x+4]	00 00 <span style="border: 1px solid black;">00</span> 00
19	[x+4]	00 00 00 <span style="border: 1px solid black;">00</span>
20	[x+5]	<span style="border: 1px solid black;">00</span> 00 00 00
21	[x+5]	00 <span style="border: 1px solid black;">00</span> 00 00
22	[x+5]	00 00 <span style="border: 1px solid black;">00</span> 00
23	[x+5]	00 00 00 <span style="border: 1px solid black;">00</span>

FIG. 43(B)

10020333-12404

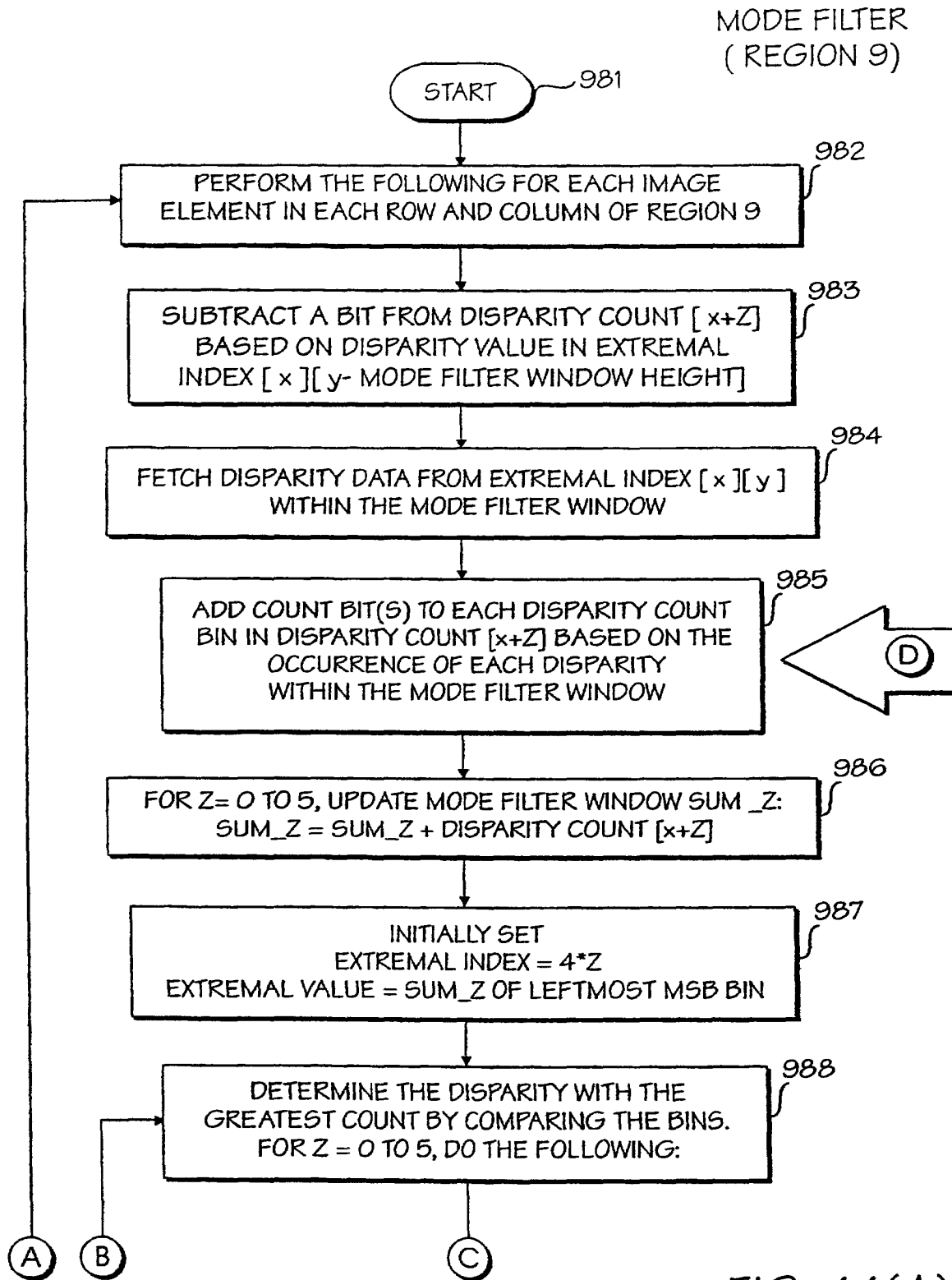


FIG. 44(A)

MODE FILTER  
( REGION 9)

DISP	DISPARITY COUNT [x+Z]	1001
0	[x] 00 00 00 00	
1	[x] 00 00 00 00	
2	[x] 00 00 00 00	
3	[x] 00 00 00 00	
4	[x+1] 00 00 00 00	
5	[x+1] 00 00 00 00	
6	[x+1] 00 00 00 00	
7	[x+1] 00 00 00 00	
8	[x+2] 00 00 00 00	
9	[x+2] 00 00 00 00	
10	[x+2] 00 00 00 00	
11	[x+2] 00 00 00 00	
12	[x+3] 00 00 00 00	
13	[x+3] 00 00 00 00	
14	[x+3] 00 00 00 00	
15	[x+3] 00 00 00 00	
16	[x+4] 00 00 00 00	
17	[x+4] 00 00 00 00	
18	[x+4] 00 00 00 00	
19	[x+4] 00 00 00 00	
20	[x+5] 00 00 00 00	
21	[x+5] 00 00 00 00	
22	[x+5] 00 00 00 00	
23	[x+5] 00 00 00 00	

FIG. 44(B)

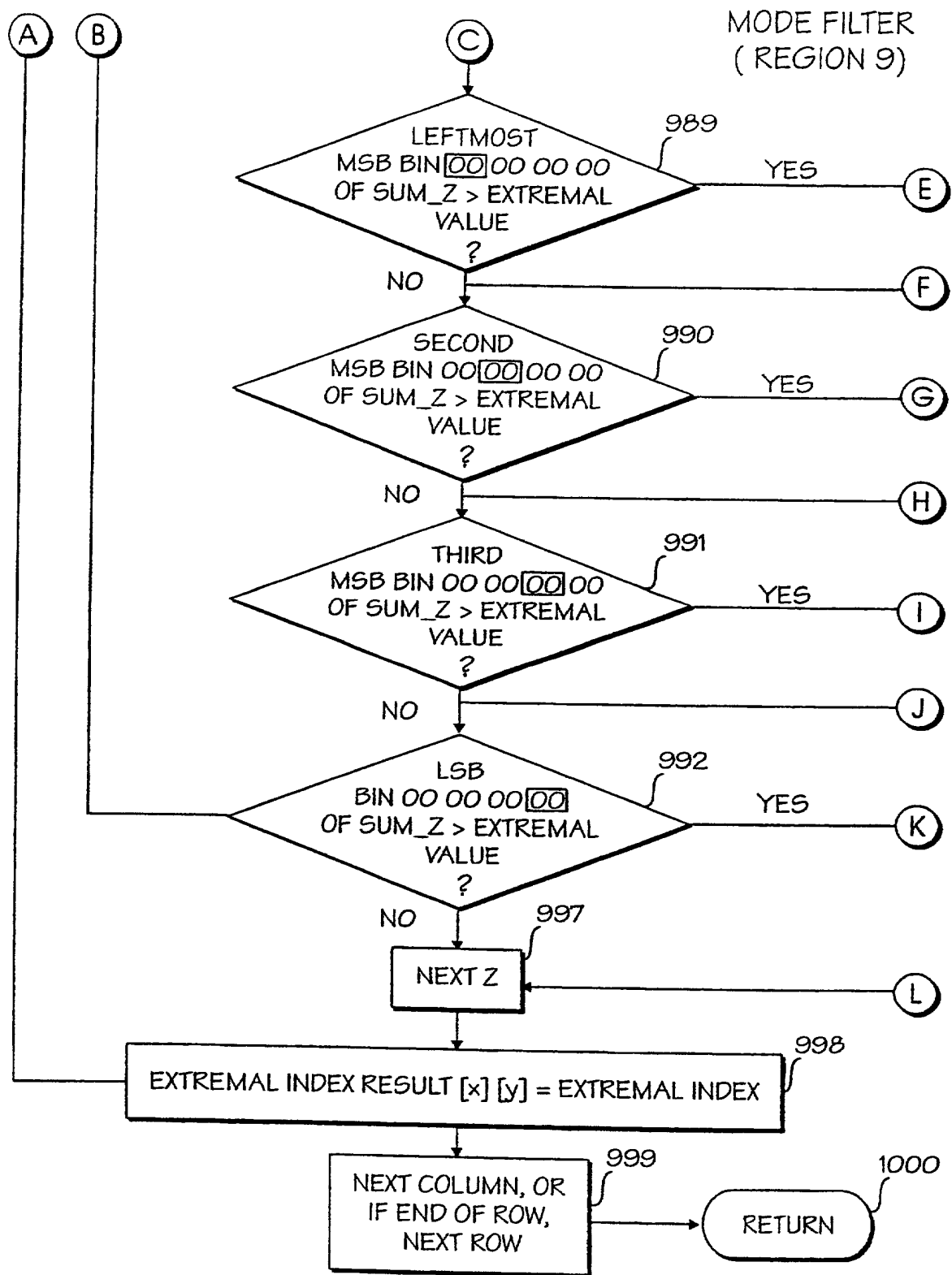


FIG. 44(C)



MODE FILTER  
( REGION 9)

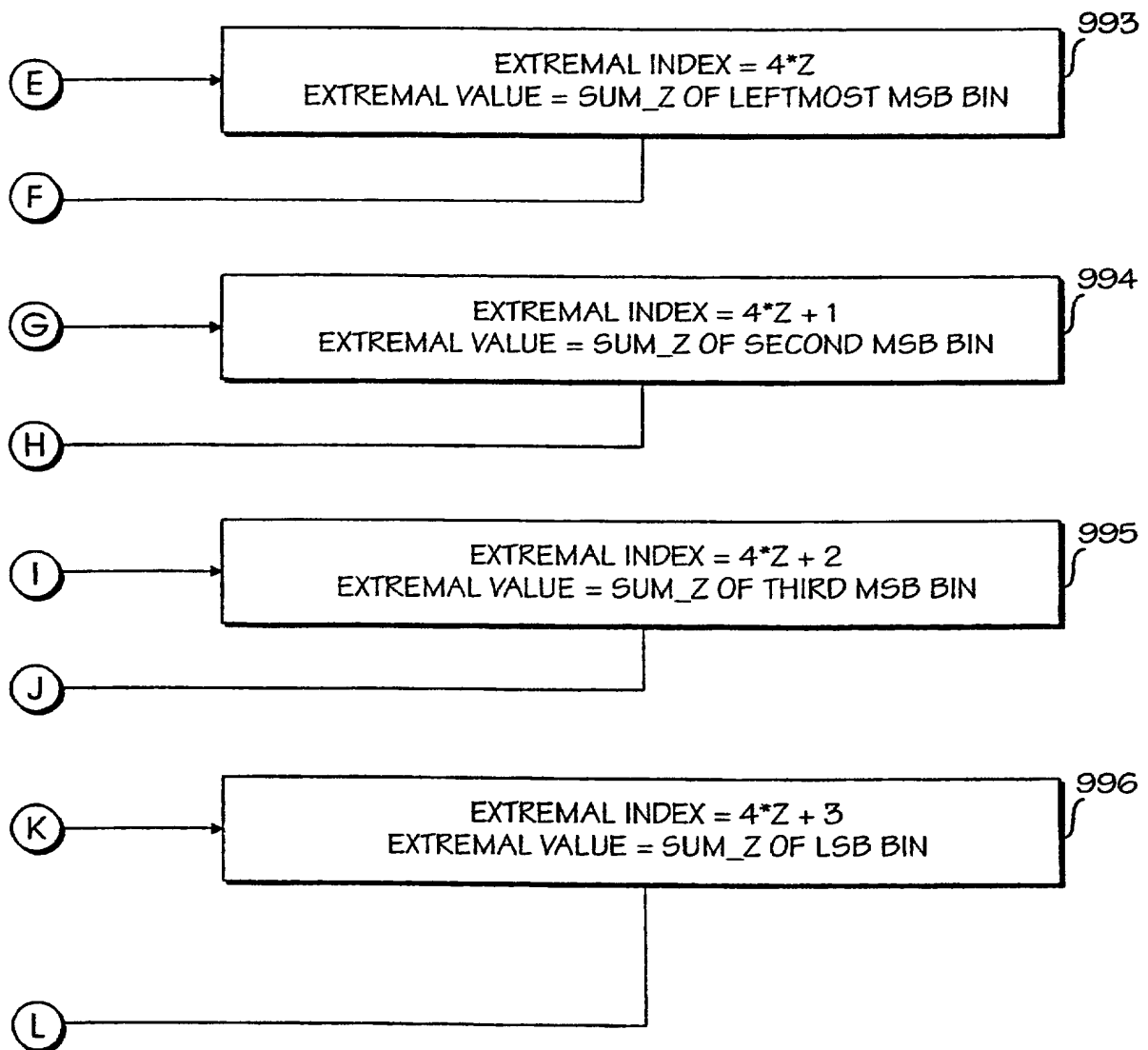


FIG. 44(D)

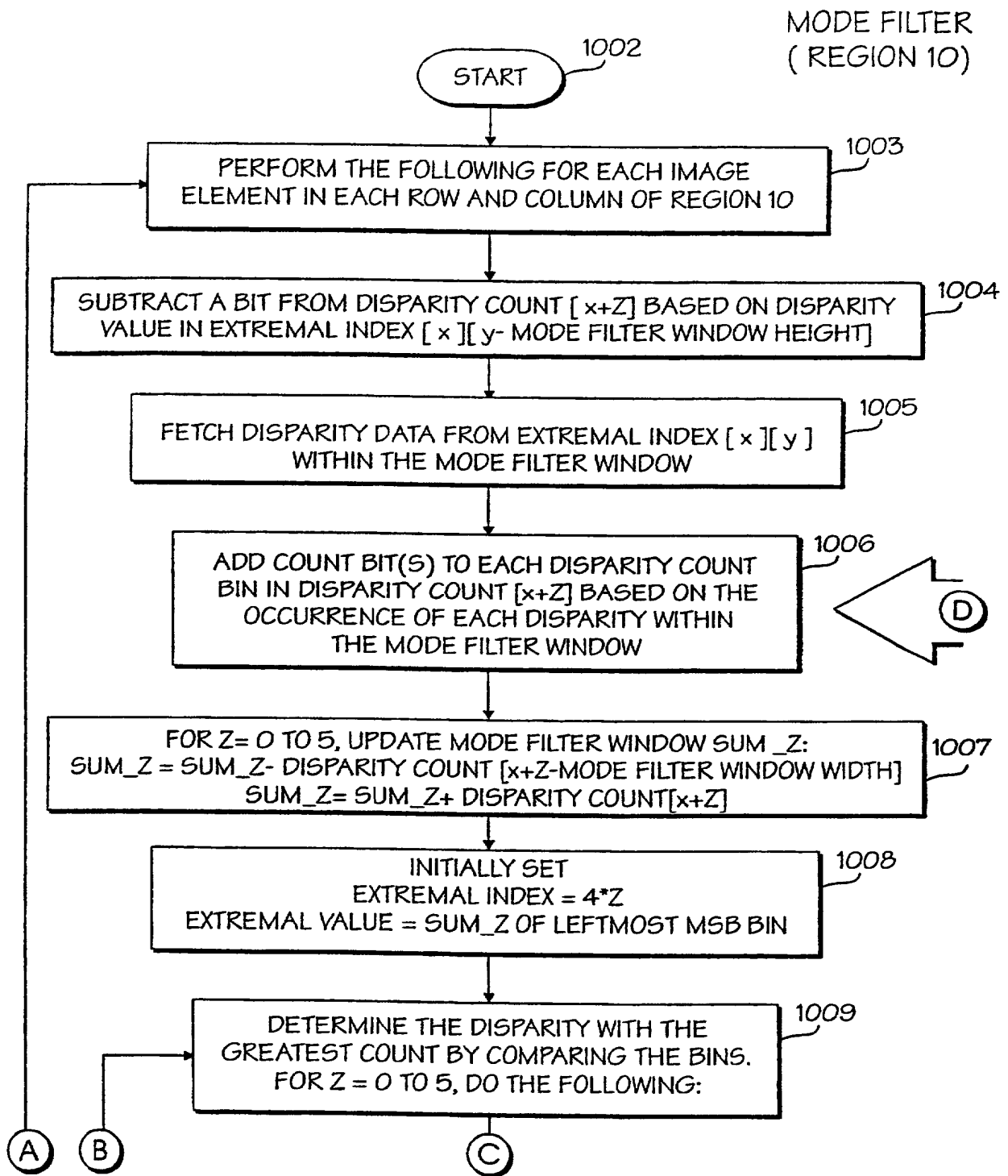


FIG. 45(A)

MODE FILTER  
( REGION 10)

(D)

DISP	DISPARITY COUNT [x+Z]	1022
0	[x] <span style="border: 1px solid black;">00</span> 00 00 00	
1	[x]    00 <span style="border: 1px solid black;">00</span> 00 00	
2	[x]    00 00 <span style="border: 1px solid black;">00</span> 00	
3	[x]    00 00 00 <span style="border: 1px solid black;">00</span>	
4	[x+1] <span style="border: 1px solid black;">00</span> 00 00 00	
5	[x+1] 00 <span style="border: 1px solid black;">00</span> 00 00	
6	[x+1] 00 00 <span style="border: 1px solid black;">00</span> 00	
7	[x+1] 00 00 00 <span style="border: 1px solid black;">00</span>	
8	[x+2] <span style="border: 1px solid black;">00</span> 00 00 00	
9	[x+2] 00 <span style="border: 1px solid black;">00</span> 00 00	
10	[x+2] 00 00 <span style="border: 1px solid black;">00</span> 00	
11	[x+2] 00 00 00 <span style="border: 1px solid black;">00</span>	
12	[x+3] <span style="border: 1px solid black;">00</span> 00 00 00	
13	[x+3] 00 <span style="border: 1px solid black;">00</span> 00 00	
14	[x+3] 00 00 <span style="border: 1px solid black;">00</span> 00	
15	[x+3] 00 00 00 <span style="border: 1px solid black;">00</span>	
16	[x+4] <span style="border: 1px solid black;">00</span> 00 00 00	
17	[x+4] 00 <span style="border: 1px solid black;">00</span> 00 00	
18	[x+4] 00 00 <span style="border: 1px solid black;">00</span> 00	
19	[x+4] 00 00 00 <span style="border: 1px solid black;">00</span>	
20	[x+5] <span style="border: 1px solid black;">00</span> 00 00 00	
21	[x+5] 00 <span style="border: 1px solid black;">00</span> 00 00	
22	[x+5] 00 00 <span style="border: 1px solid black;">00</span> 00	
23	[x+5] 00 00 00 <span style="border: 1px solid black;">00</span>	

FIG. 45(B)

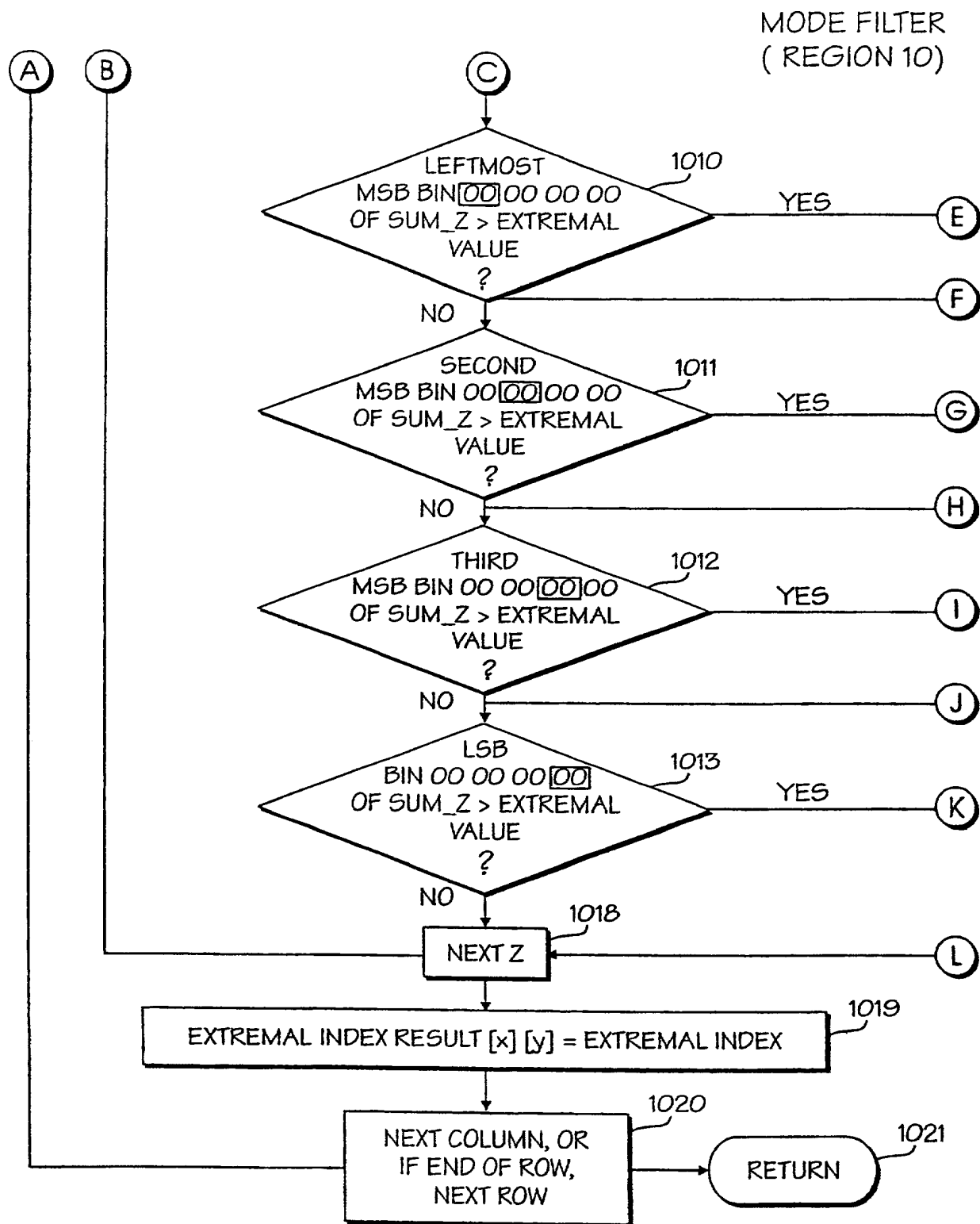


FIG. 45(C)

MODE FILTER  
( REGION 10)

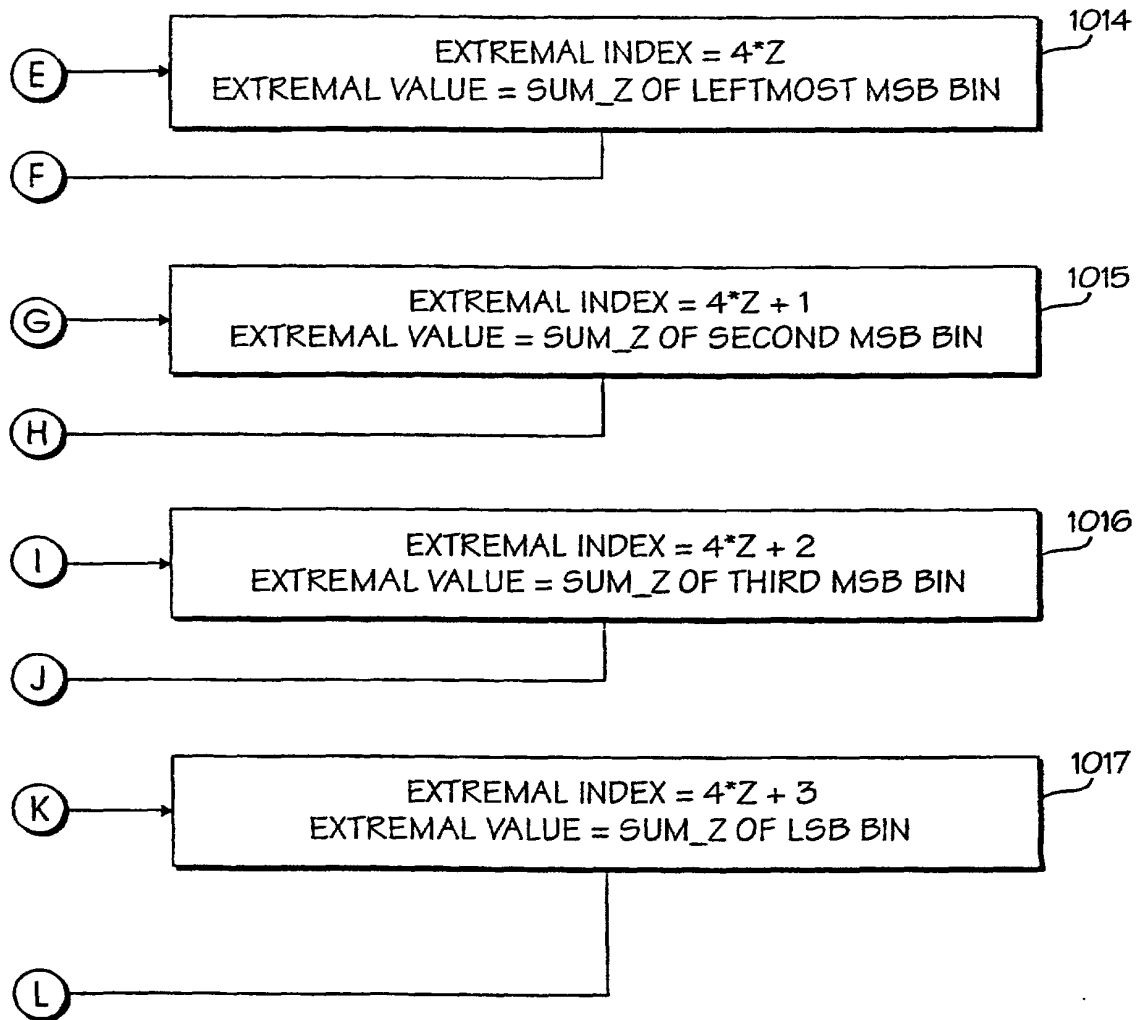


FIG. 45(D)

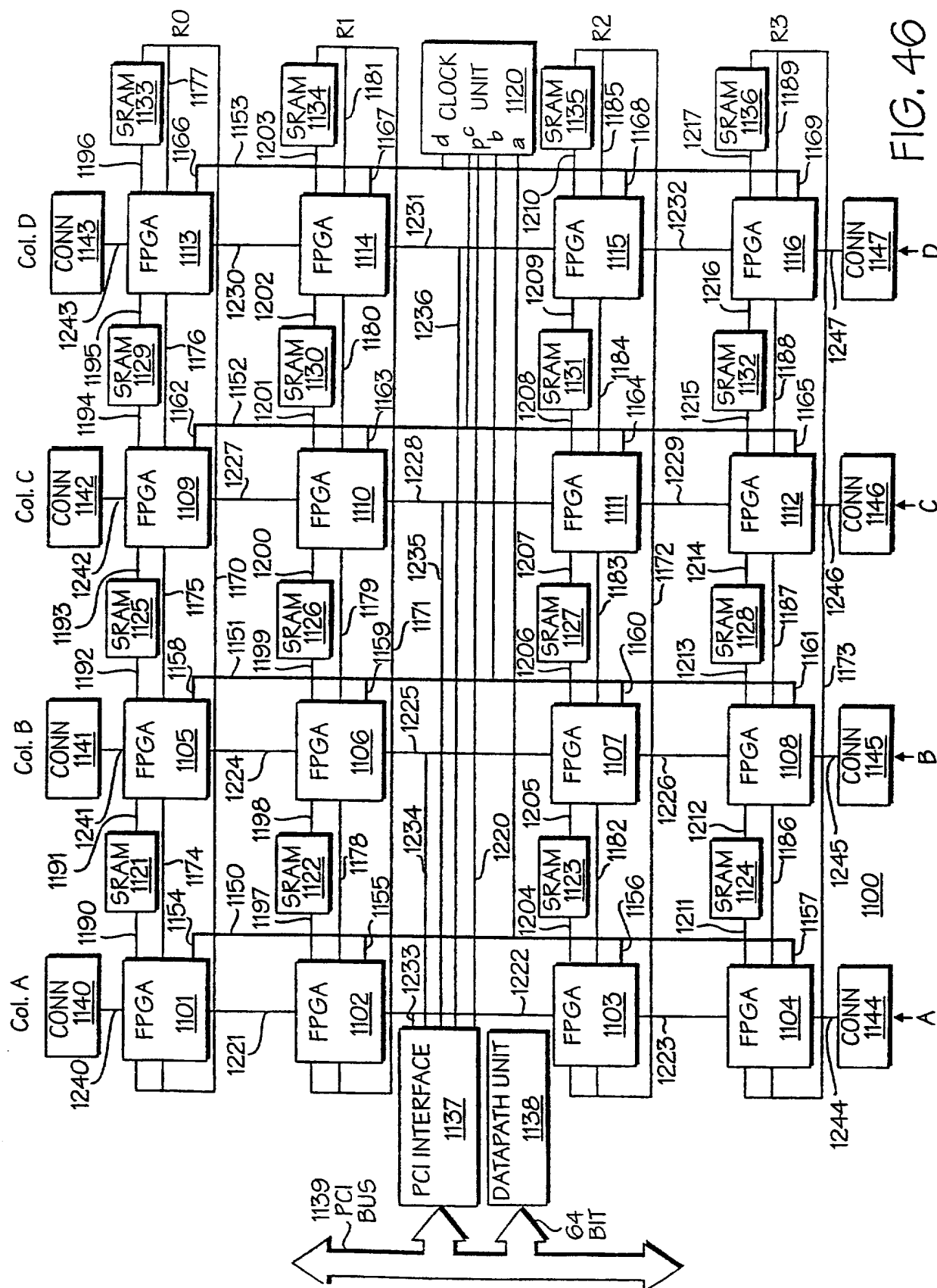


FIG. 46

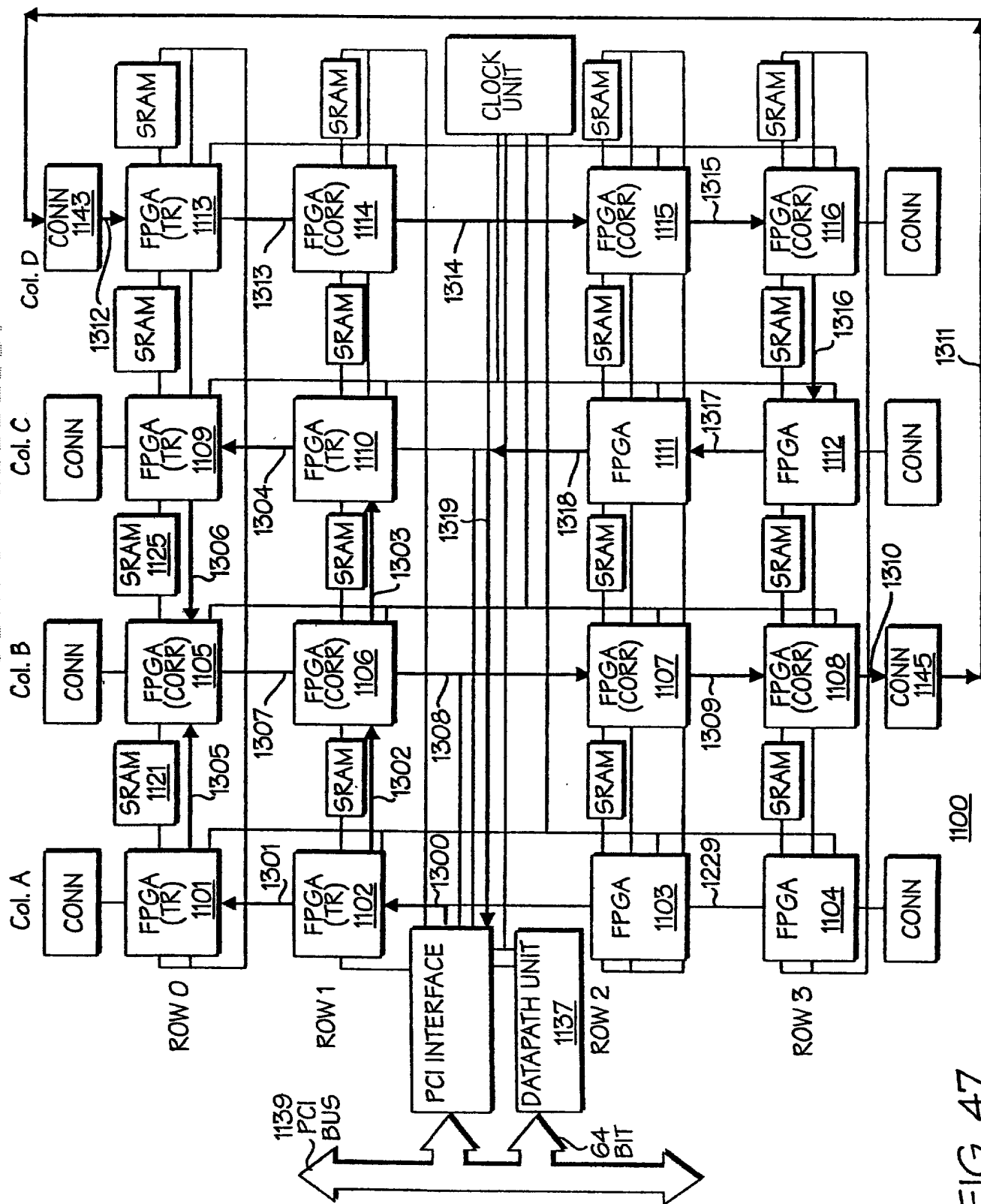


FIG. 47

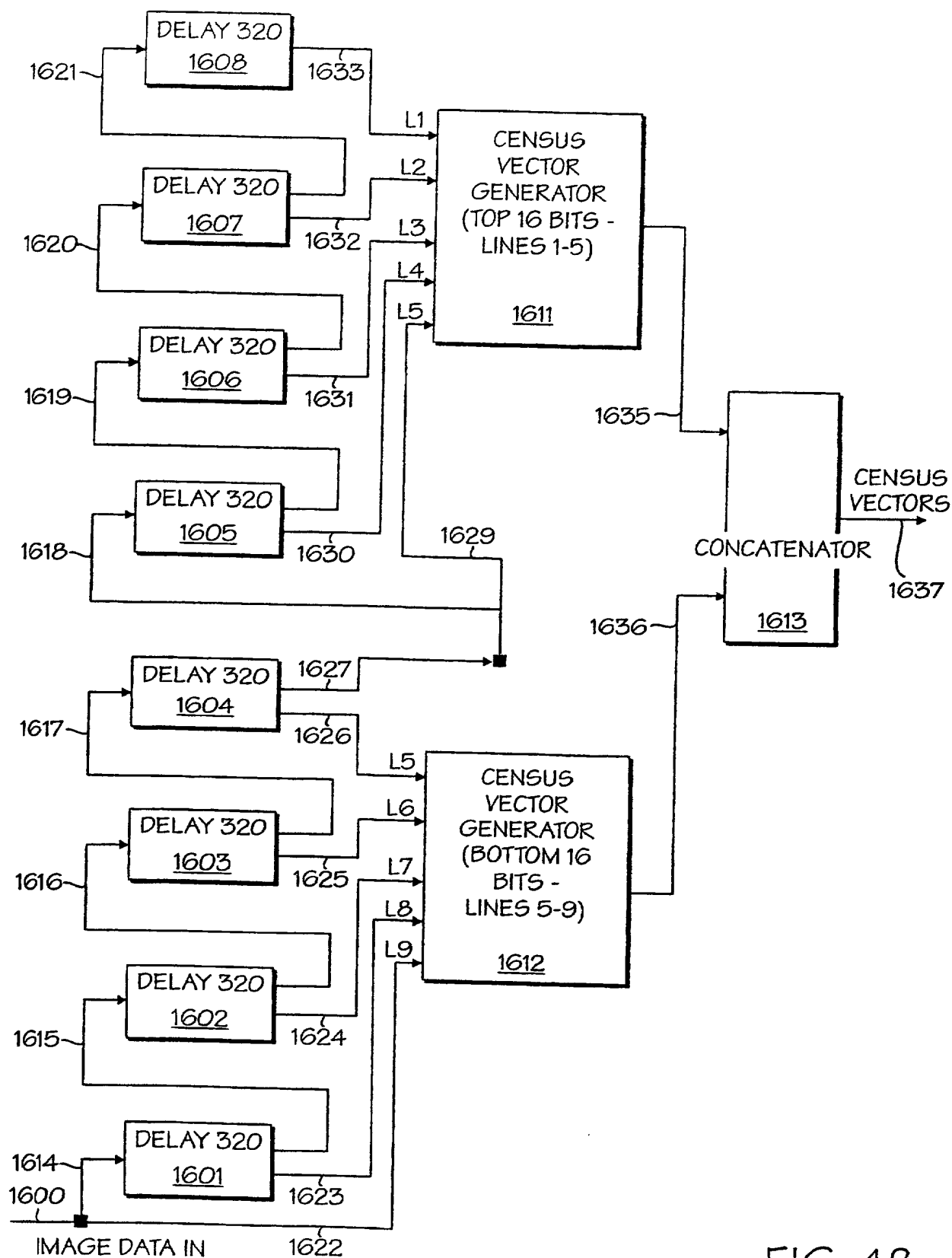


FIG. 48



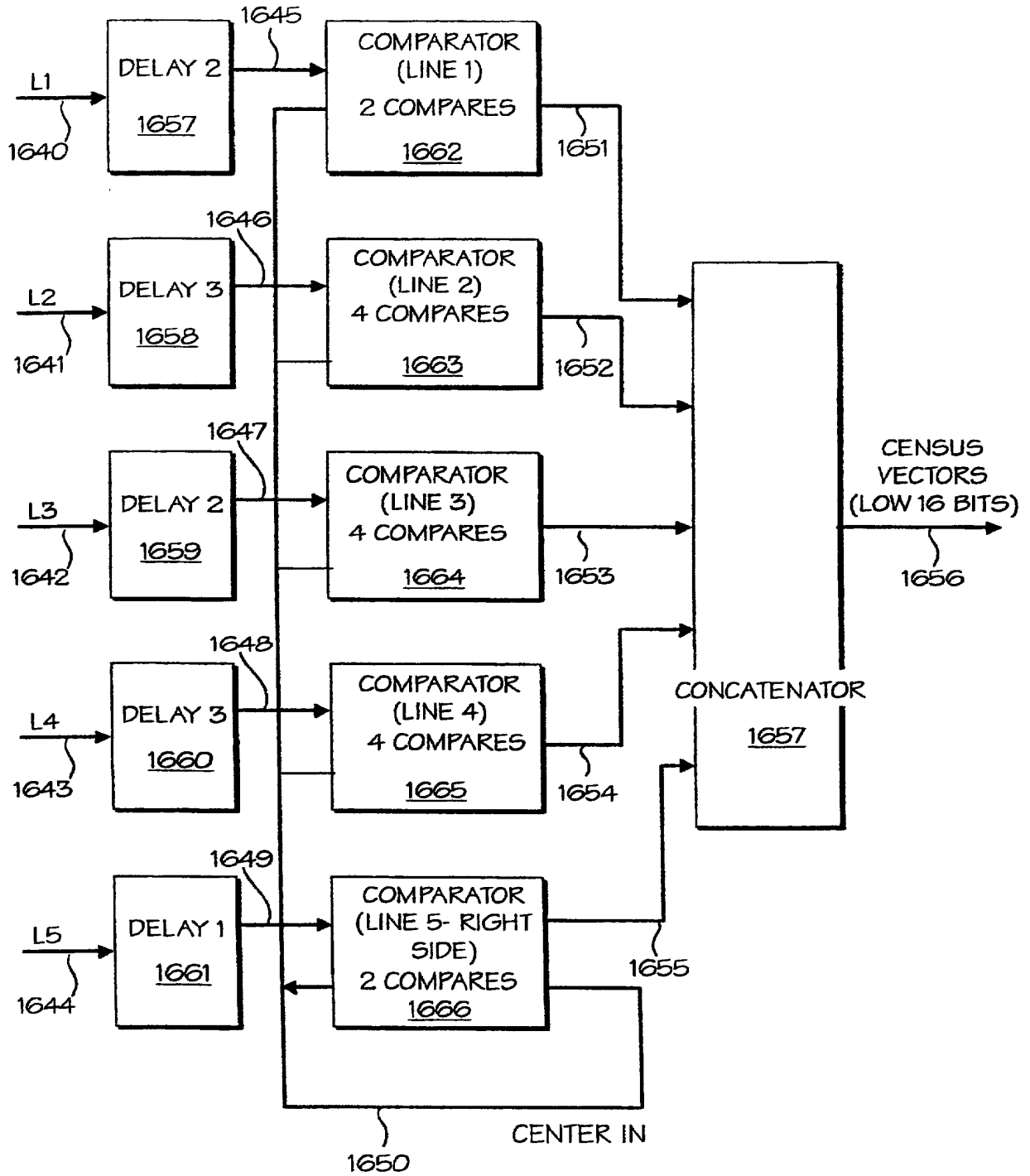


FIG. 49

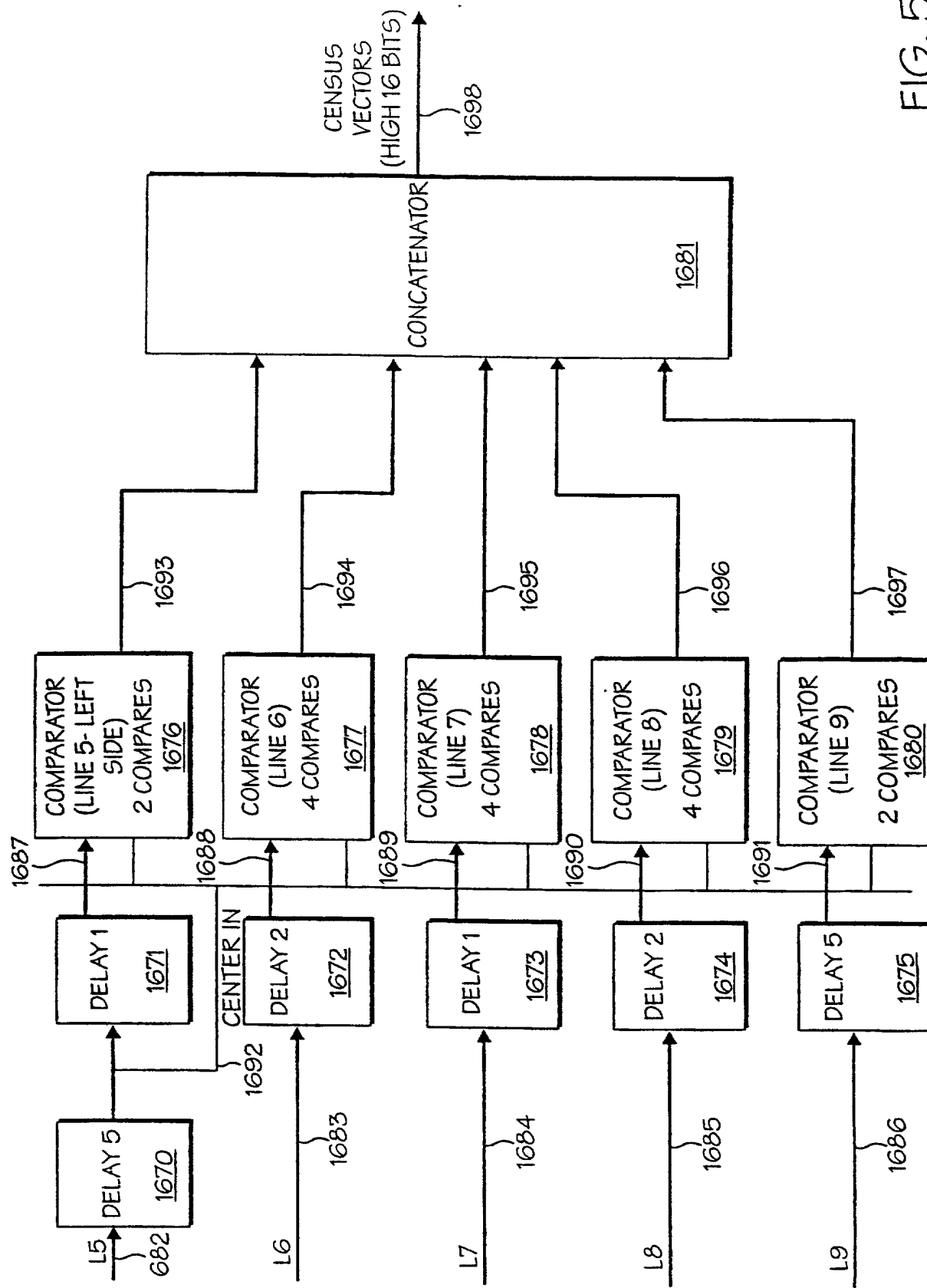


FIG. 50

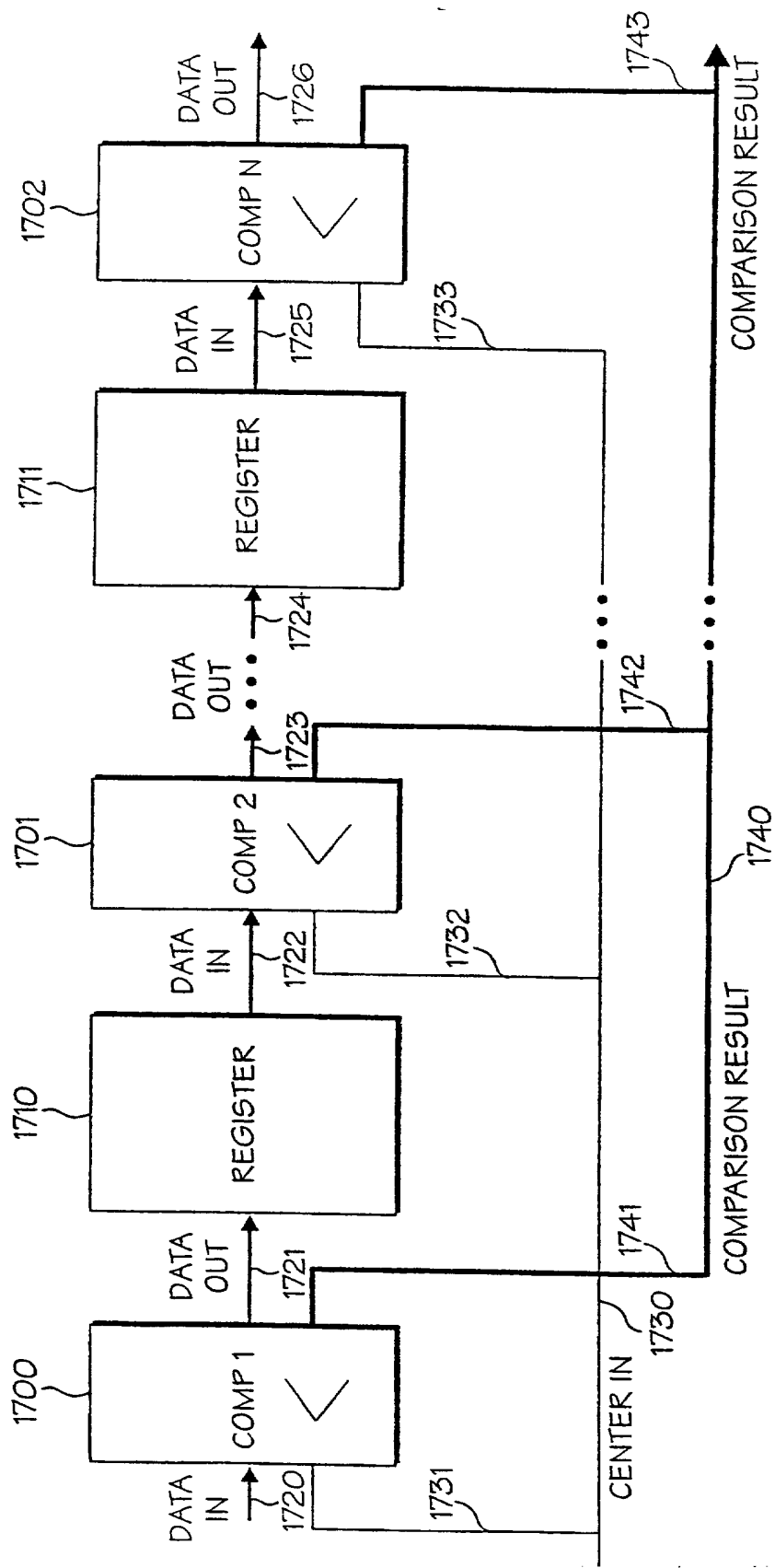


FIG. 51

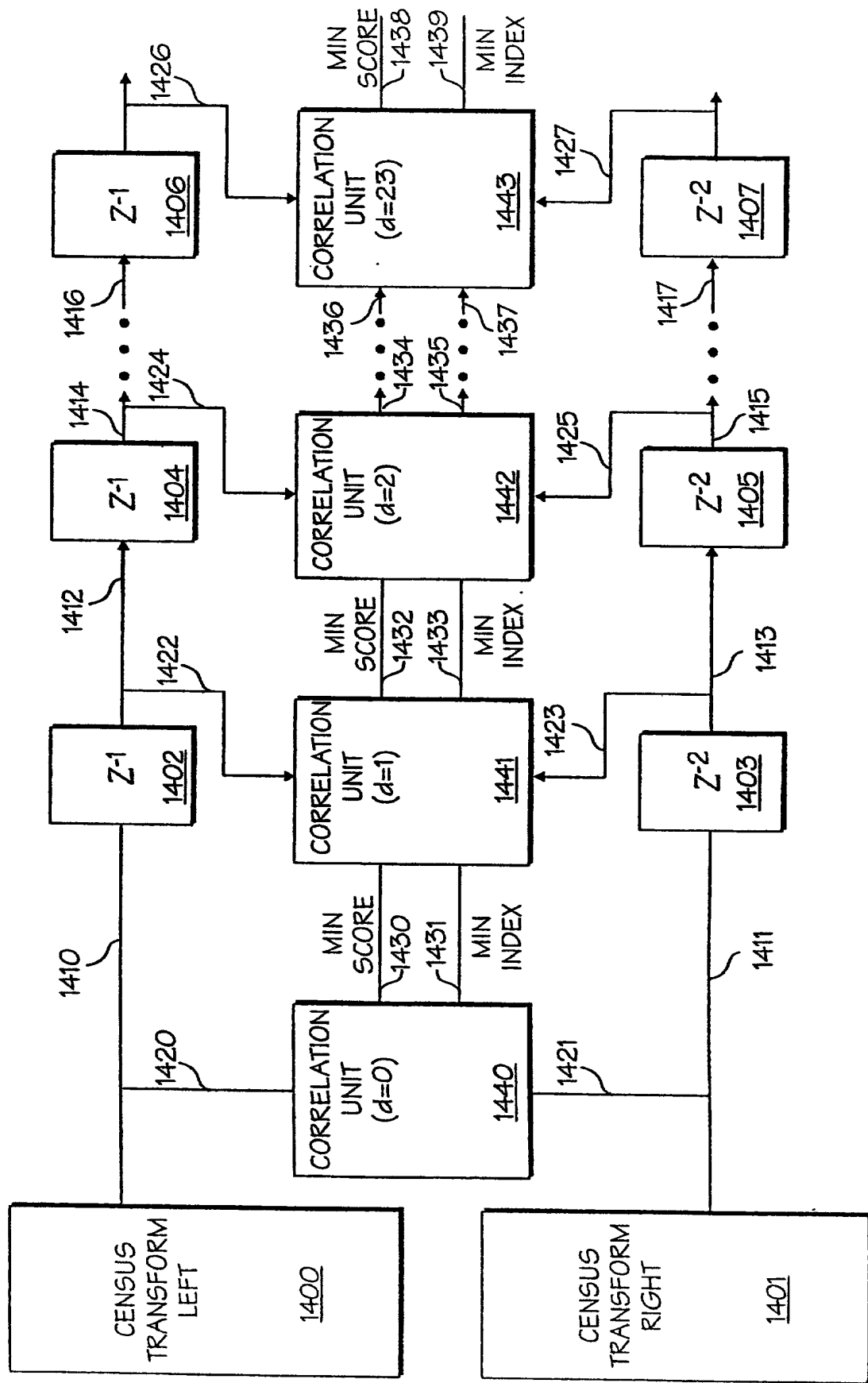


Fig. 52

CENSUS VECTORS  
LEFT IMAGE

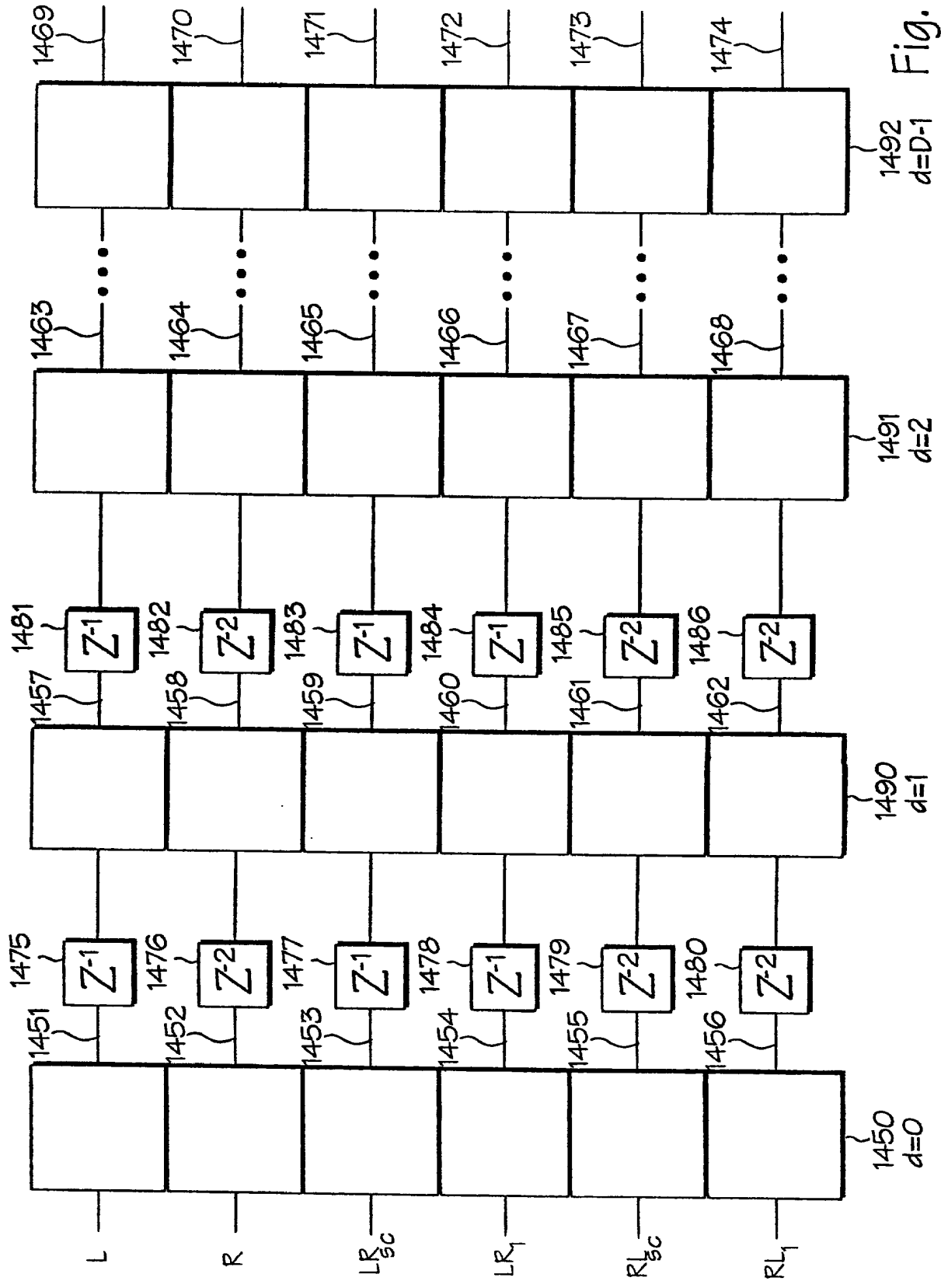
1	2	3	4	5	6	7	8	9	10	...
• • •										

Fig. 53(A)

CENSUS VECTORS  
RIGHT IMAGE

1'	2'	3'	4'	5'	6'	7'	8'	9'	10'	...
• • •										

Fig. 53(B)



# 15 IMAGE ELEMENTS (1 TO 15 FOR L; 1' TO 15' FOR R)

D=5 (d= {0, 1, 2, 3, 4})

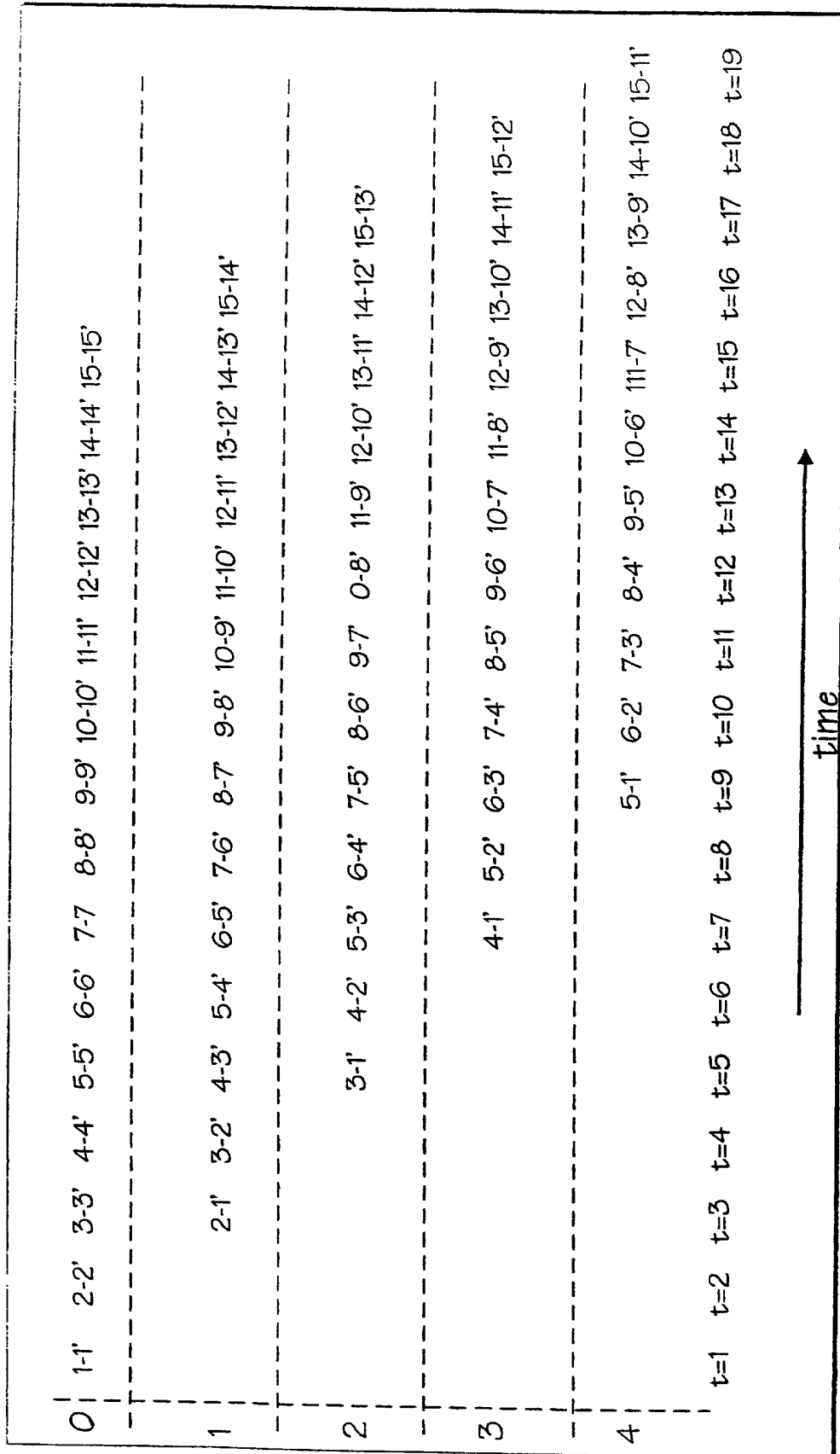


FIG. 55

$LR_l$		$RL_l$	
$LR_l(9)$	5	$RL_l(5')$	
$LR_l(8)$	4	$RL_l(4')$	
$LR_l(7)$	3	$RL_l(3')$	
$LR_l(6)$	2	$RL_l(2')$	
$LR_l(5)$	1	$RL_l(1')$	

Fig. 56(A)

$LR_l$		$RL_l$	
$LR_l(10)$	5	$RL_l(6')$	
$LR_l(9)$	4	$RL_l(5')$	
$LR_l(8)$	3	$RL_l(4')$	
$LR_l(7)$	2	$RL_l(3')$	
$LR_l(6)$	1	$RL_l(2')$	

Fig. 56(B)

$LR_l$		$RL_l$	
$LR_l(11)$	5	$RL_l(7)$	
$LR_l(10)$	4	$RL_l(6')$	
$LR_l(9)$	3	$RL_l(5')$	
$LR_l(8)$	2	$RL_l(4')$	
$LR_l(7)$	1	$RL_l(3')$	

Fig. 56(C)

$LR_l$		$RL_l$
	10	
	9	
	8	
	7	
	6	
	5	
	4	
	3	
	2	
	1	

Fig. 56(D)



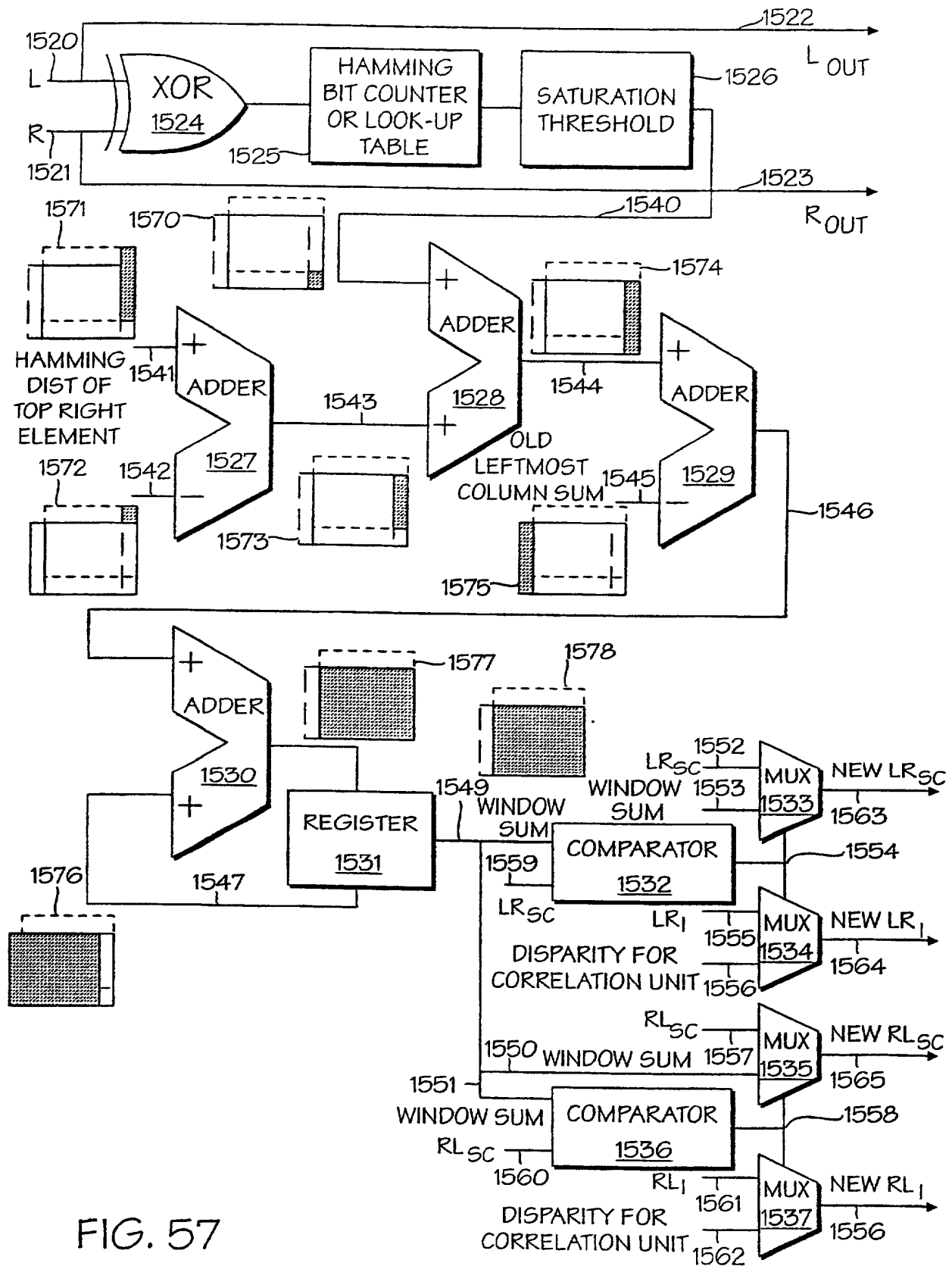


FIG. 57

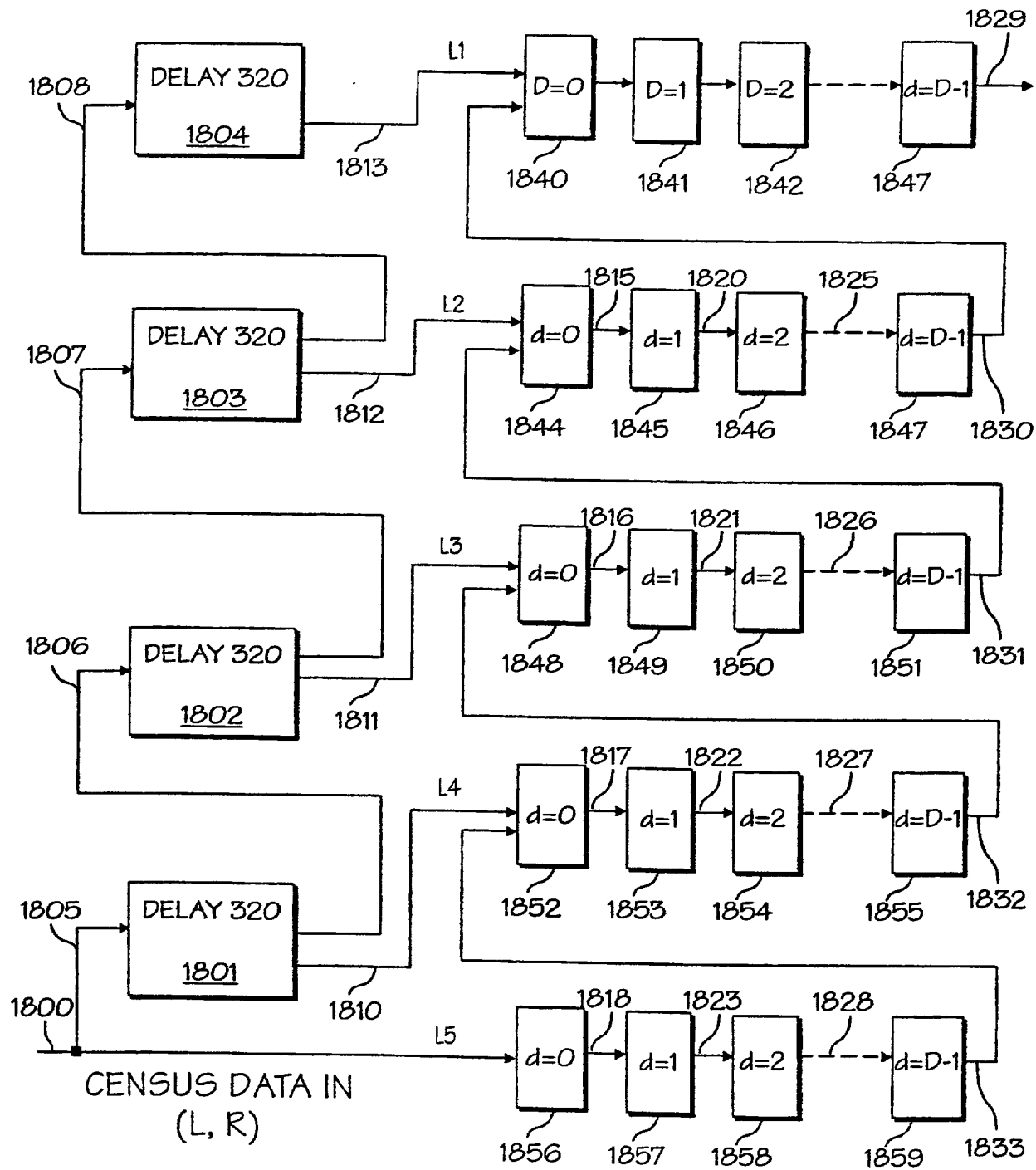


FIG. 58

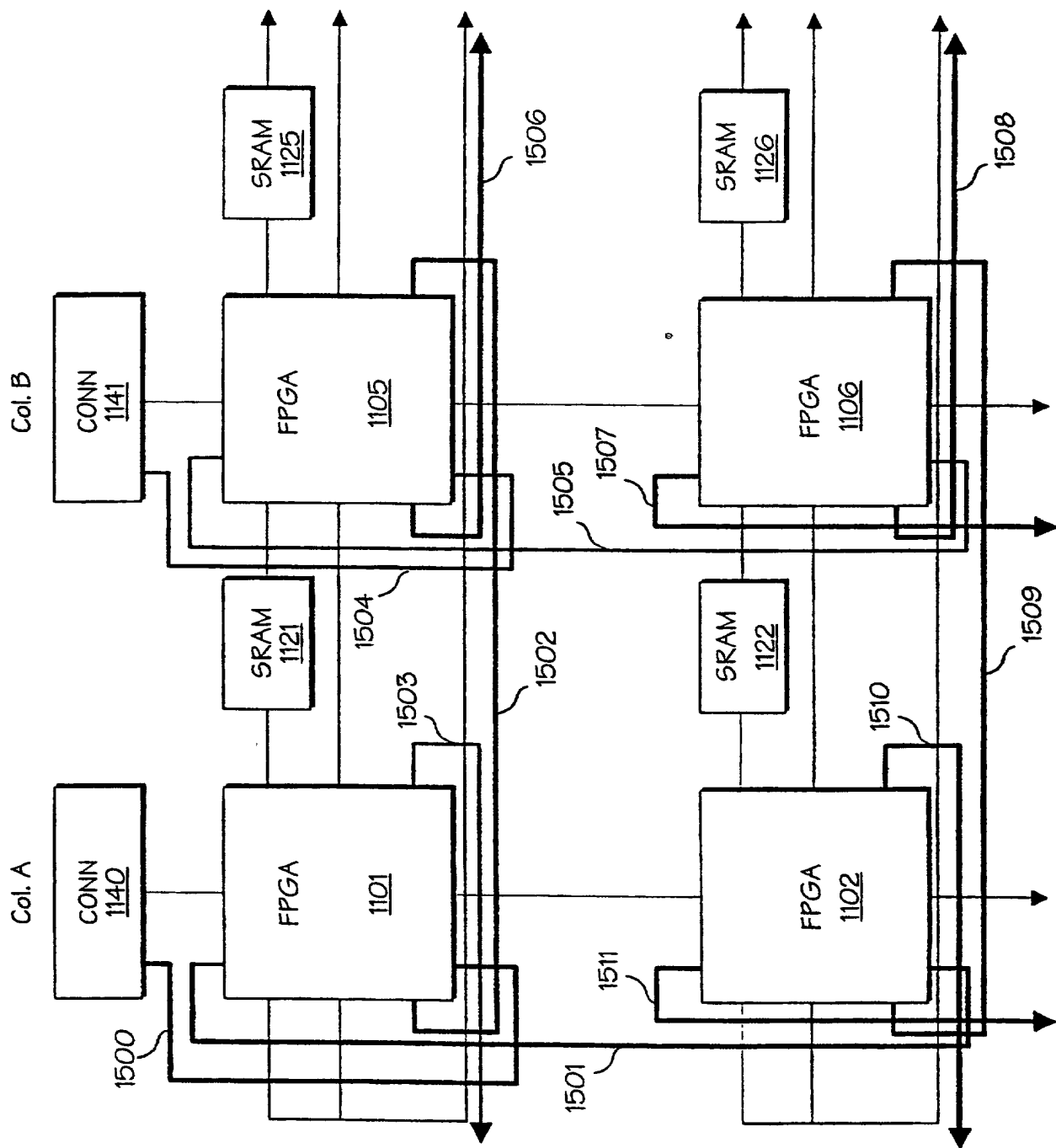


FIG. 59

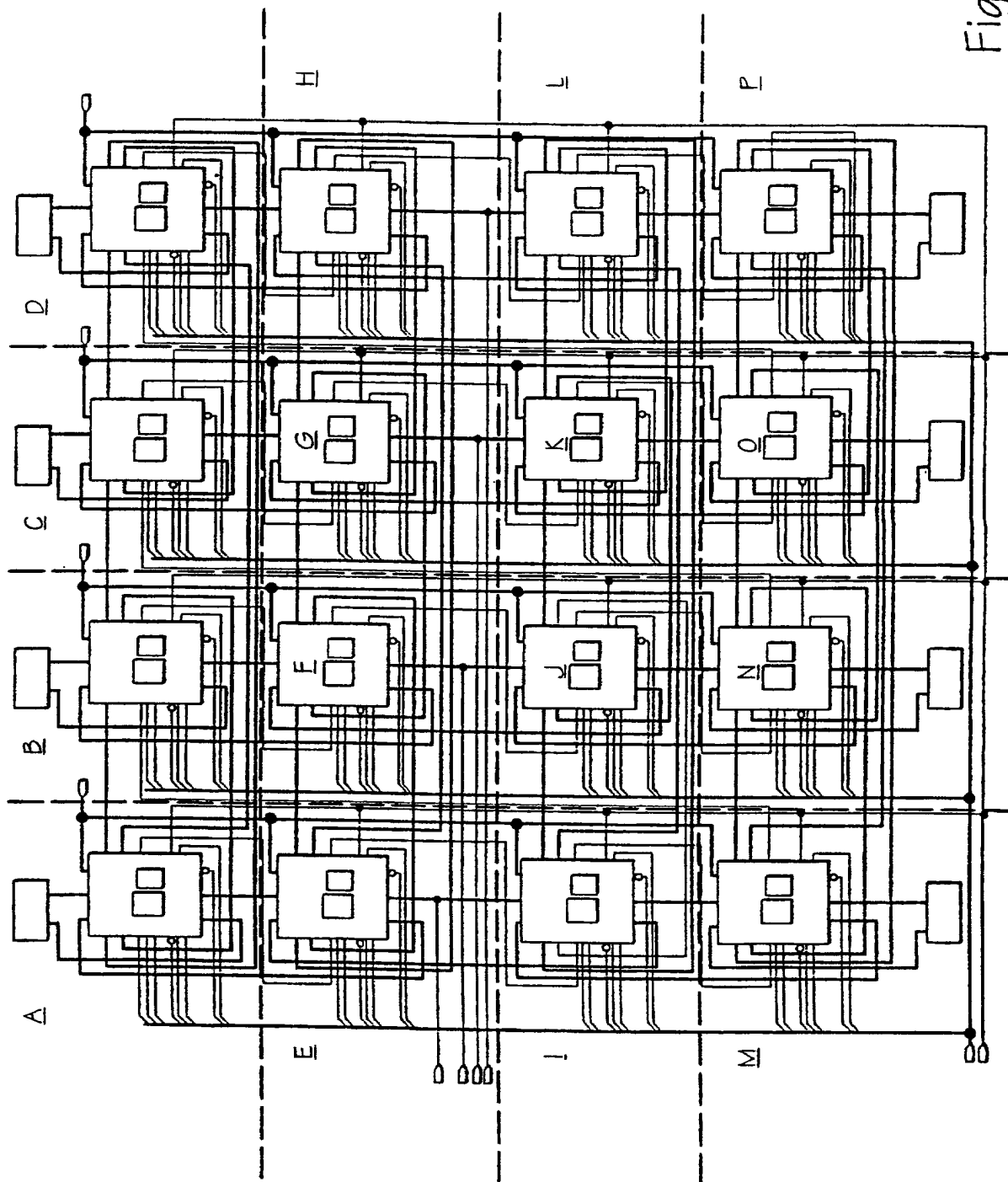
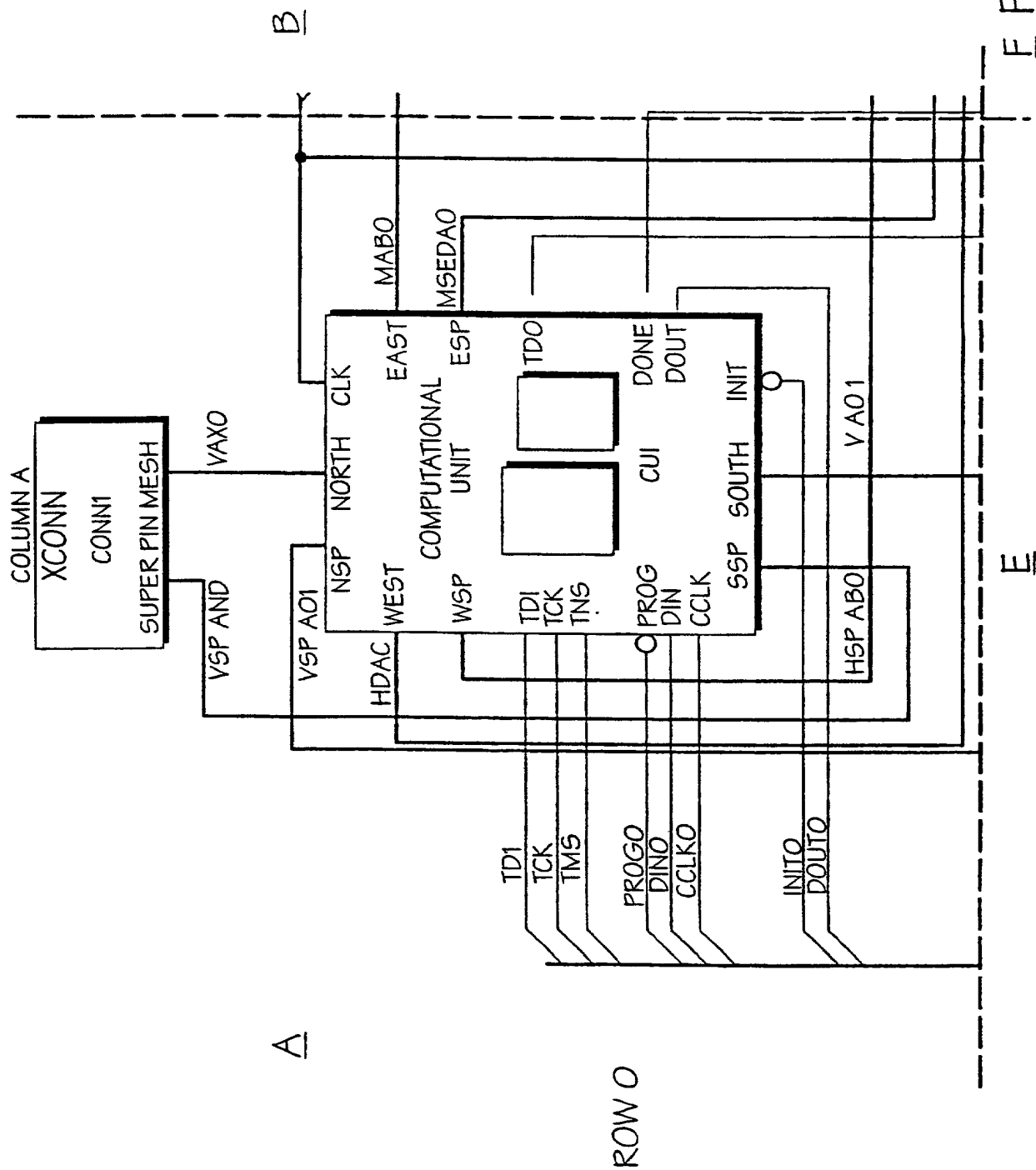


Fig. 60



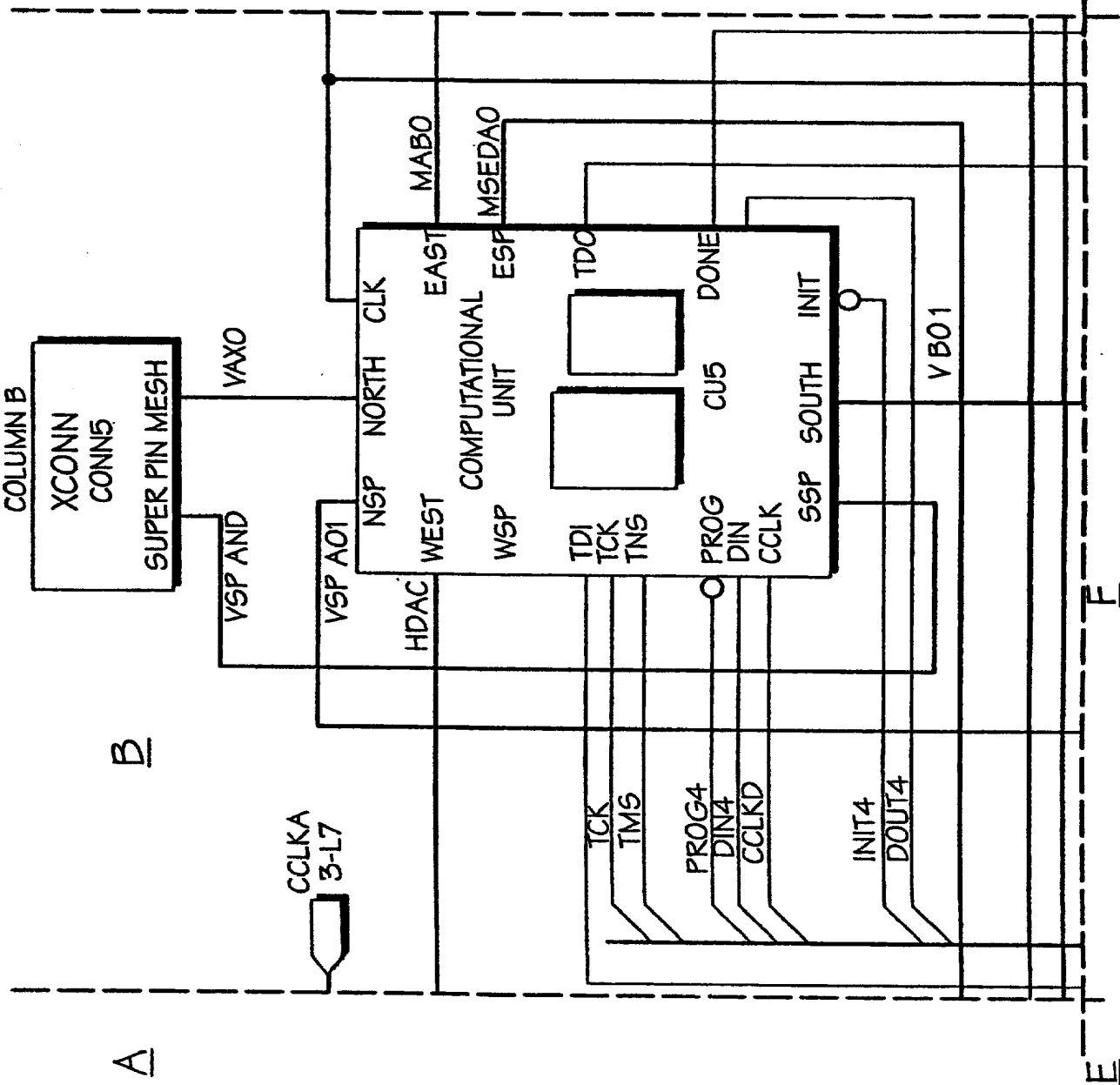


Fig. 60B

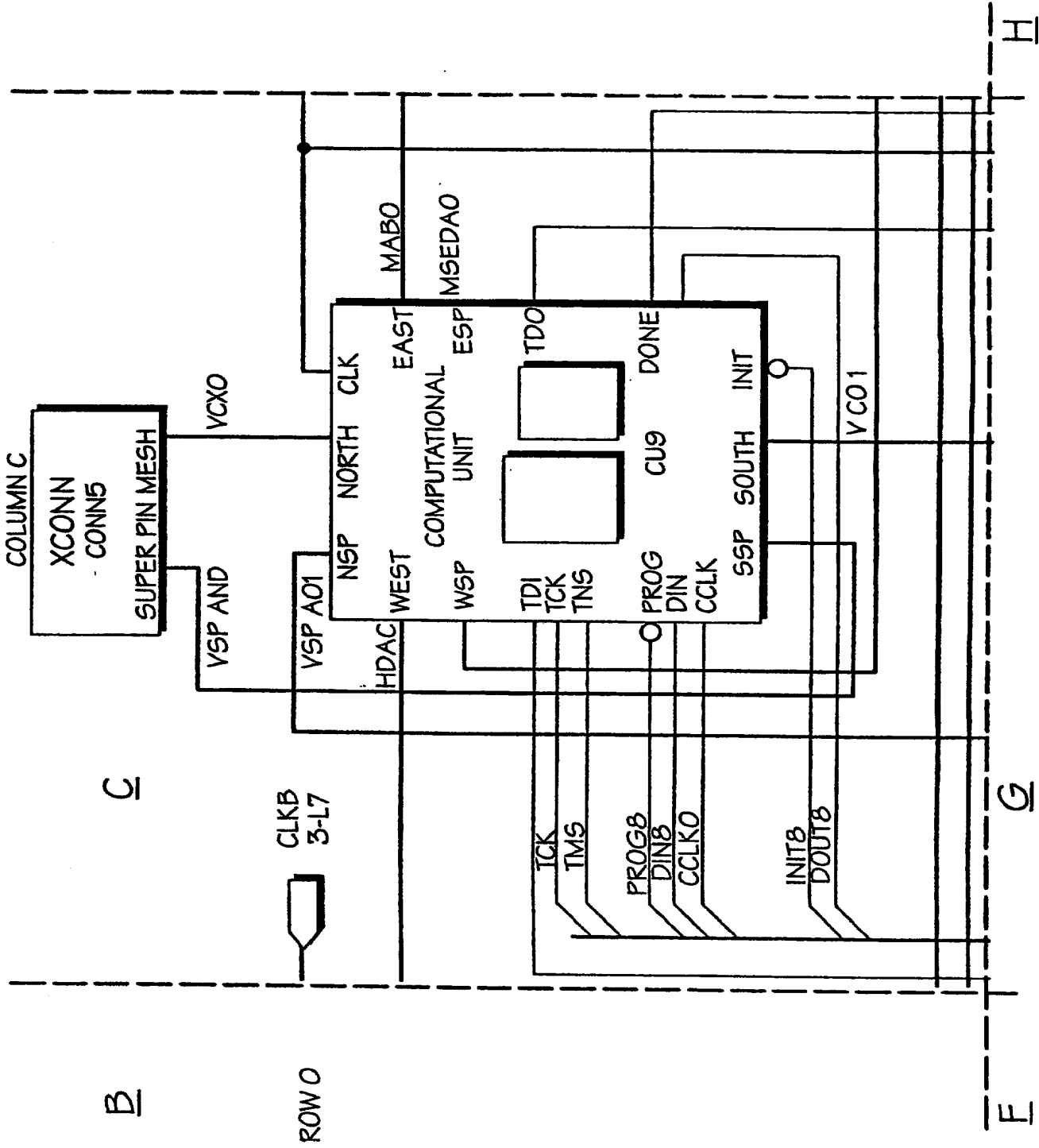


Fig. 60C

	Mean	SD
Age	20.4	1.4
Gender		
Male	100	0
Female	0	0
Marital status		
Married	0	0
Single	100	0
Religion		
Hindu	100	0
Muslim	0	0
Christian	0	0
Others	0	0
Education		
Below primary	0	0
Primary	100	0
Secondary	0	0
Tertiary	0	0
Above tertiary	0	0
Occupation		
Unemployed	100	0
Employed	0	0
Self-employed	0	0
Others	0	0
Income		
Below 1000	100	0
1000-2000	0	0
2000-3000	0	0
3000-4000	0	0
4000-5000	0	0
5000-6000	0	0
6000-7000	0	0
7000-8000	0	0
8000-9000	0	0
9000-10000	0	0
Above 10000	0	0
Health status		
Good	100	0
Poor	0	0
Very poor	0	0
Others	0	0
Family size		
Below 5	100	0
5-10	0	0
10-15	0	0
15-20	0	0
Above 20	0	0
Family income		
Below 10000	100	0
10000-20000	0	0
20000-30000	0	0
30000-40000	0	0
40000-50000	0	0
50000-60000	0	0
60000-70000	0	0
70000-80000	0	0
80000-90000	0	0
90000-100000	0	0
Above 100000	0	0
Family health status		
Good	100	0
Poor	0	0
Very poor	0	0
Others	0	0
Family education		
Below primary	0	0
Primary	100	0
Secondary	0	0
Tertiary	0	0
Above tertiary	0	0
Family occupation		
Unemployed	100	0
Employed	0	0
Self-employed	0	0
Others	0	0
Family income		
Below 10000	100	0
10000-20000	0	0
20000-30000	0	0
30000-40000	0	0
40000-50000	0	0
50000-60000	0	0
60000-70000	0	0
70000-80000	0	0
80000-90000	0	0
90000-100000	0	0
Above 100000	0	0
Family health status		
Good	100	0
Poor	0	0
Very poor	0	0
Others	0	0
Family education		
Below primary	0	0
Primary	100	0
Secondary	0	0
Tertiary	0	0
Above tertiary	0	0
Family occupation		
Unemployed	100	0
Employed	0	0
Self-employed	0	0
Others	0	0
Family income		
Below 10000	100	0
10000-20000	0	0
20000-30000	0	0
30000-40000	0	0
40000-50000	0	0
50000-60000	0	0
60000-70000	0	0
70000-80000	0	0
80000-90000	0	0
90000-100000	0	0
Above 100000	0	0
Family health status		
Good	100	0
Poor	0	0
Very poor	0	0
Others	0	0
Family education		
Below primary	0	0
Primary	100	0
Secondary		

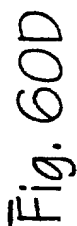


Fig. 60D



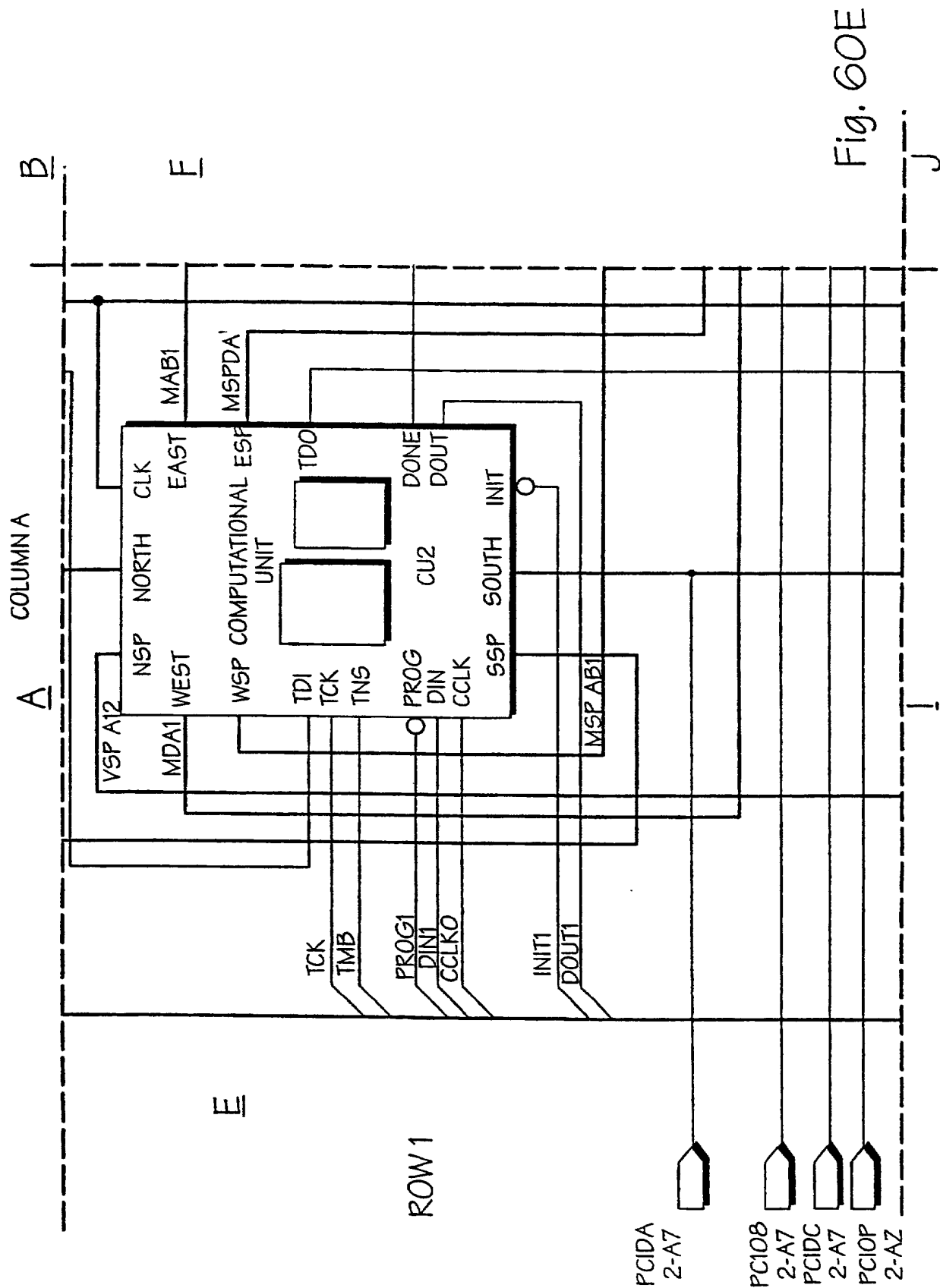


Fig. 60E

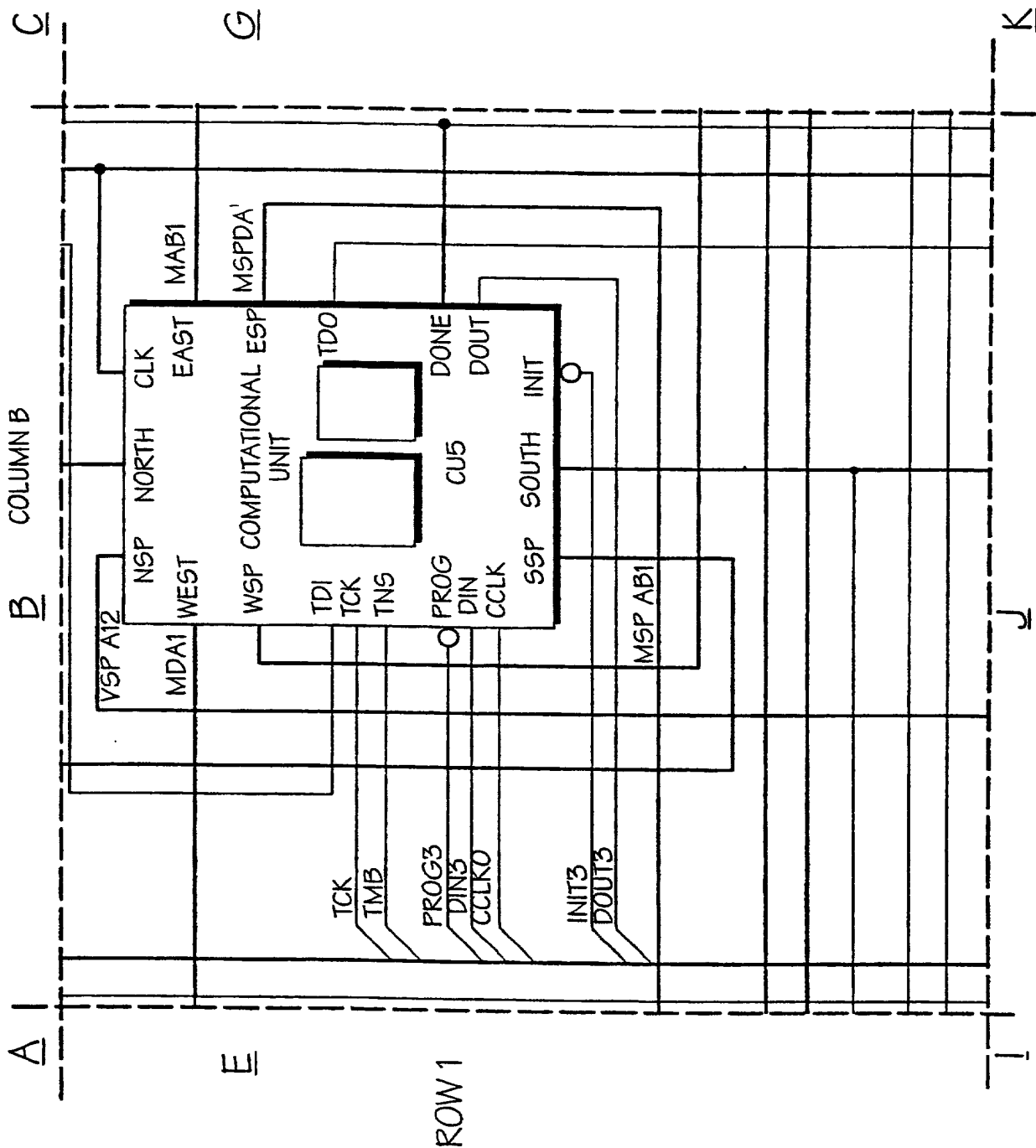


Fig. 60F

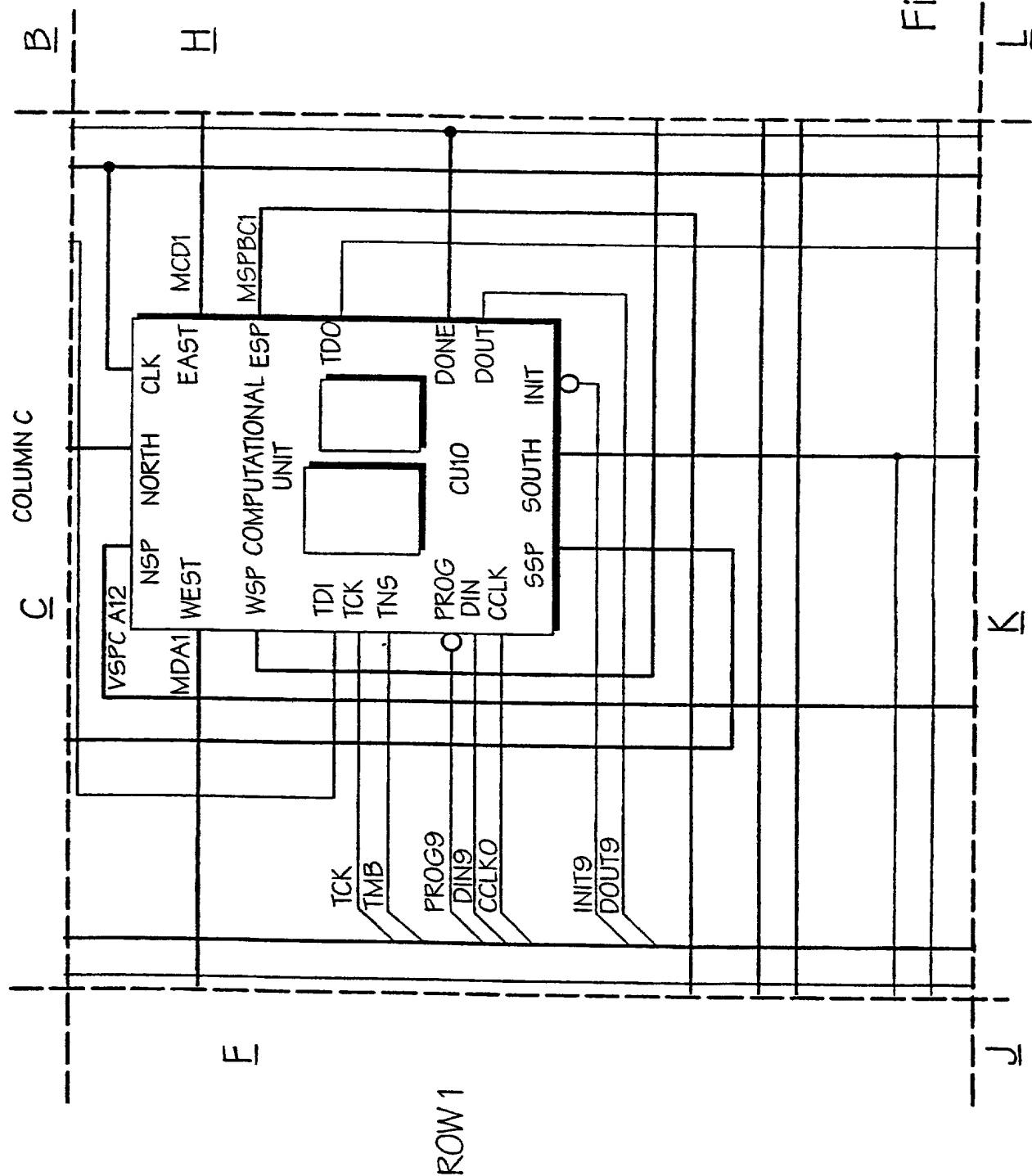


Fig. 60G

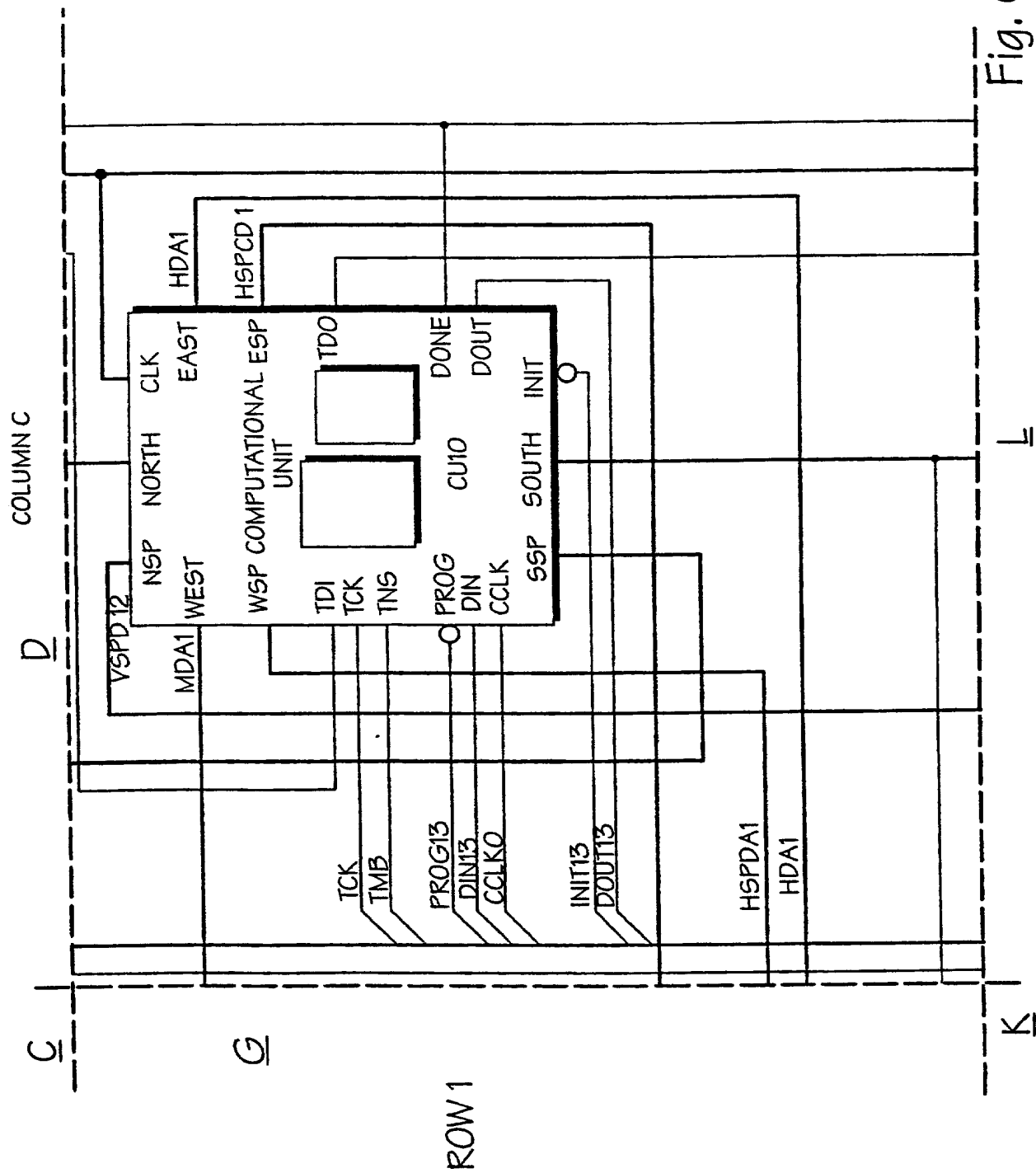


Fig. 60H

COLUMN C

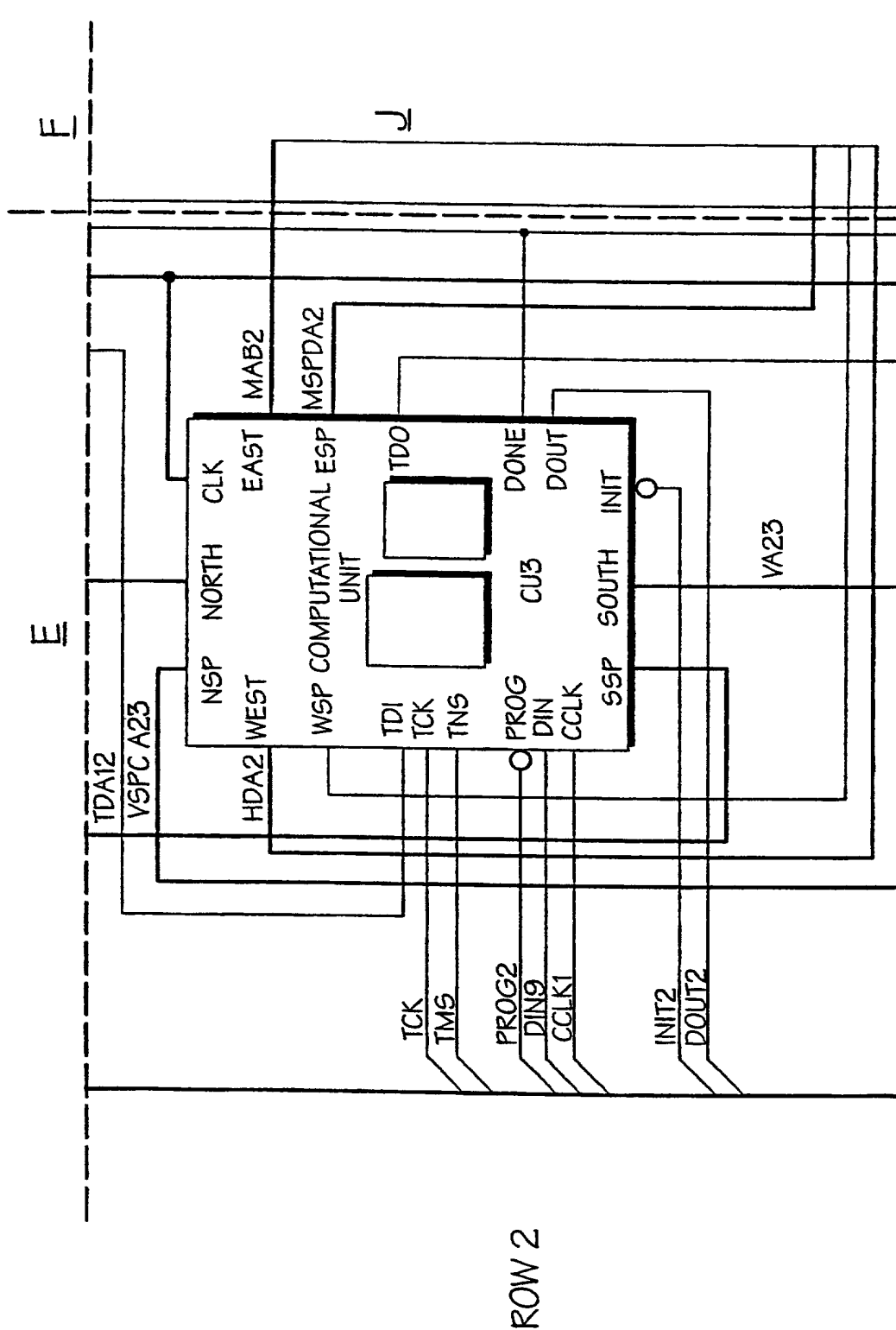
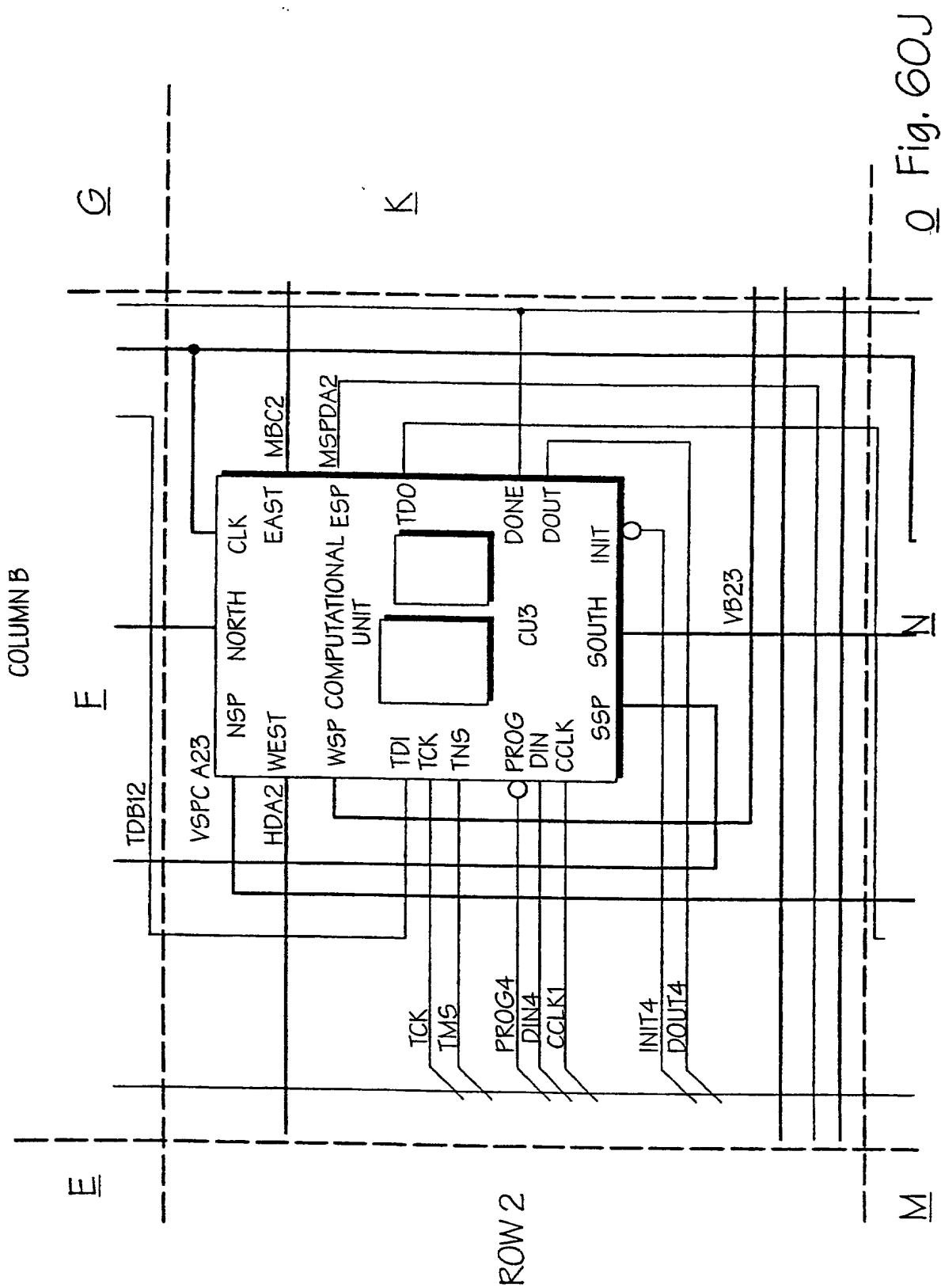


Fig. 601



COLUMN C

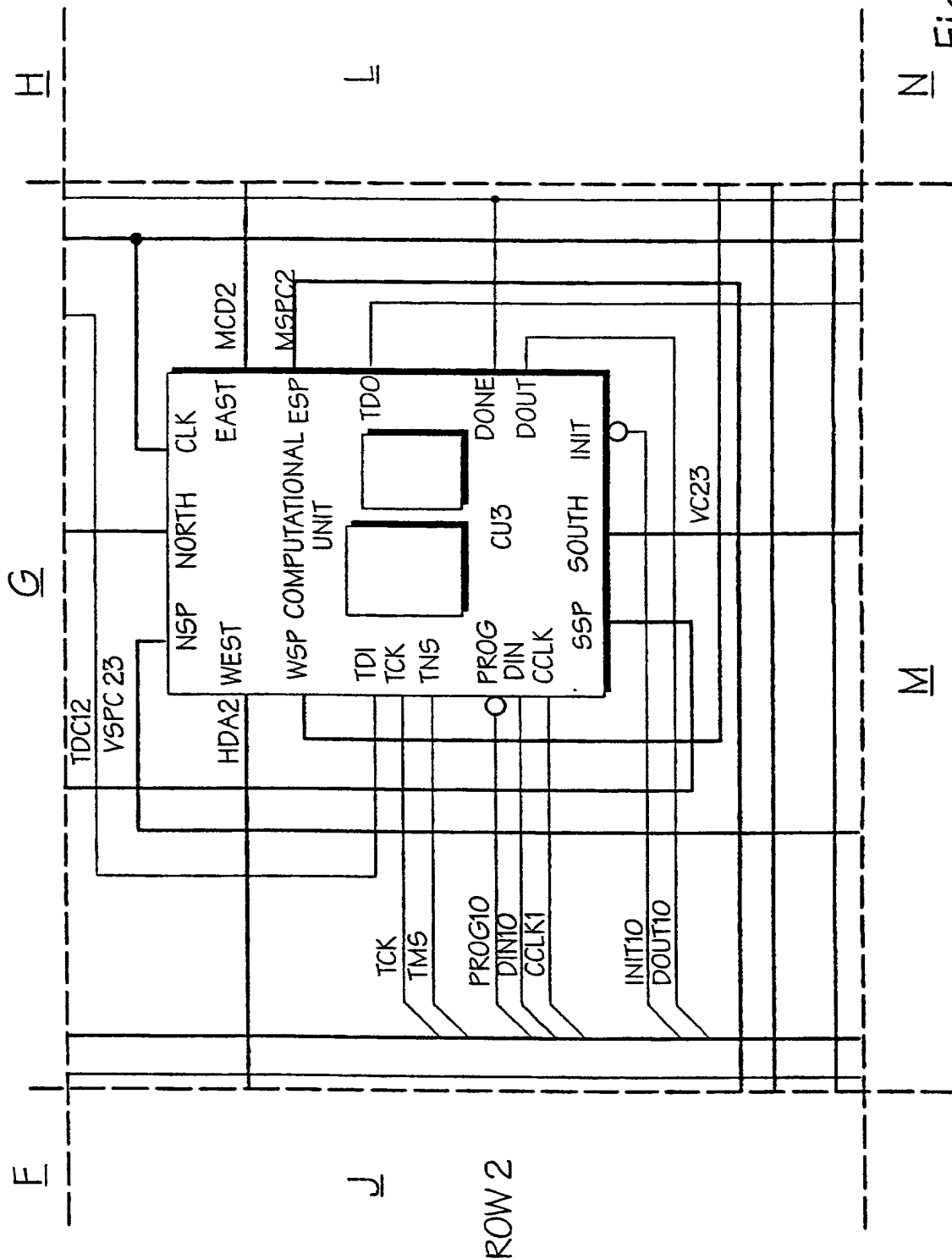
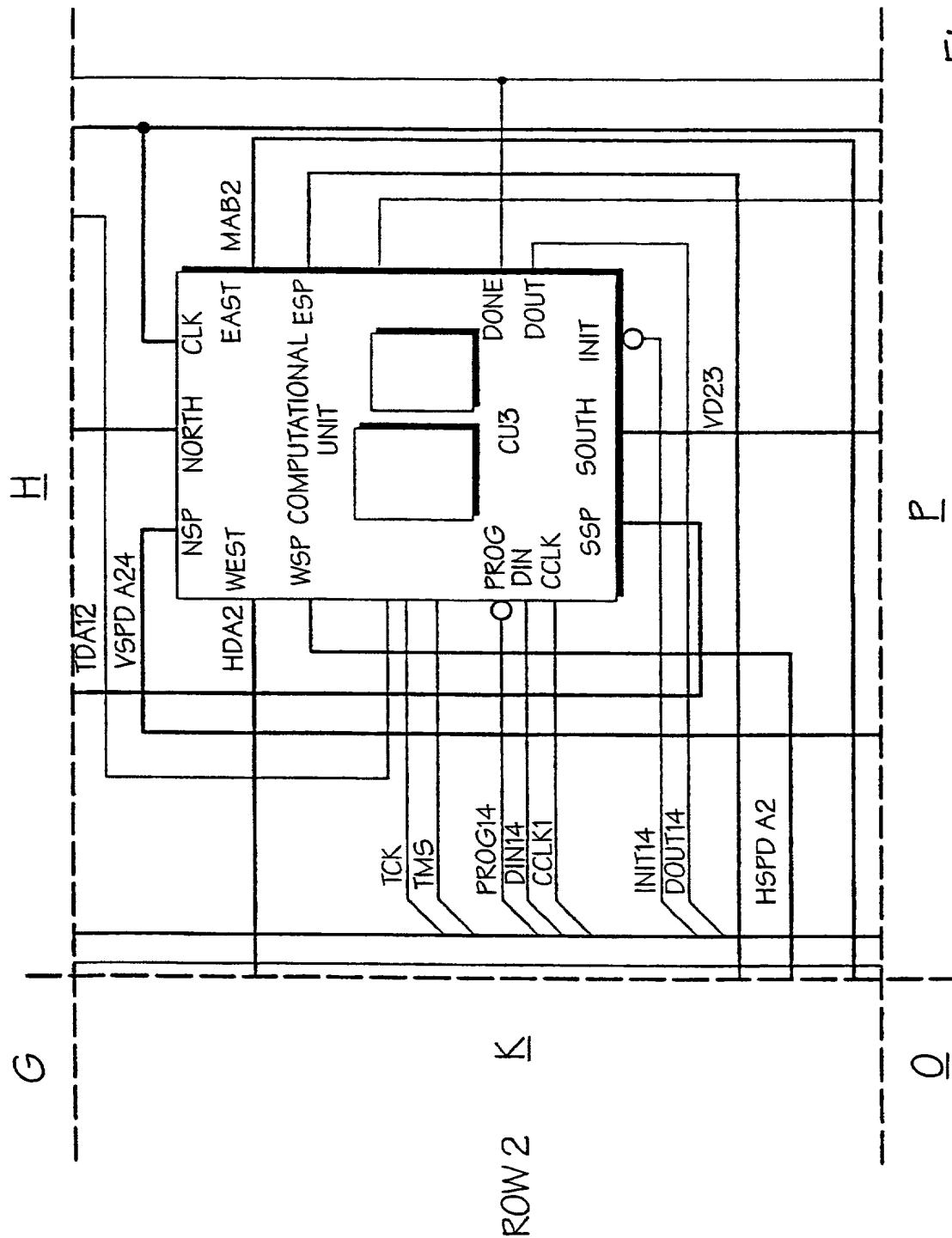


Fig. 60K

COLUMN D





TOP AT 230000

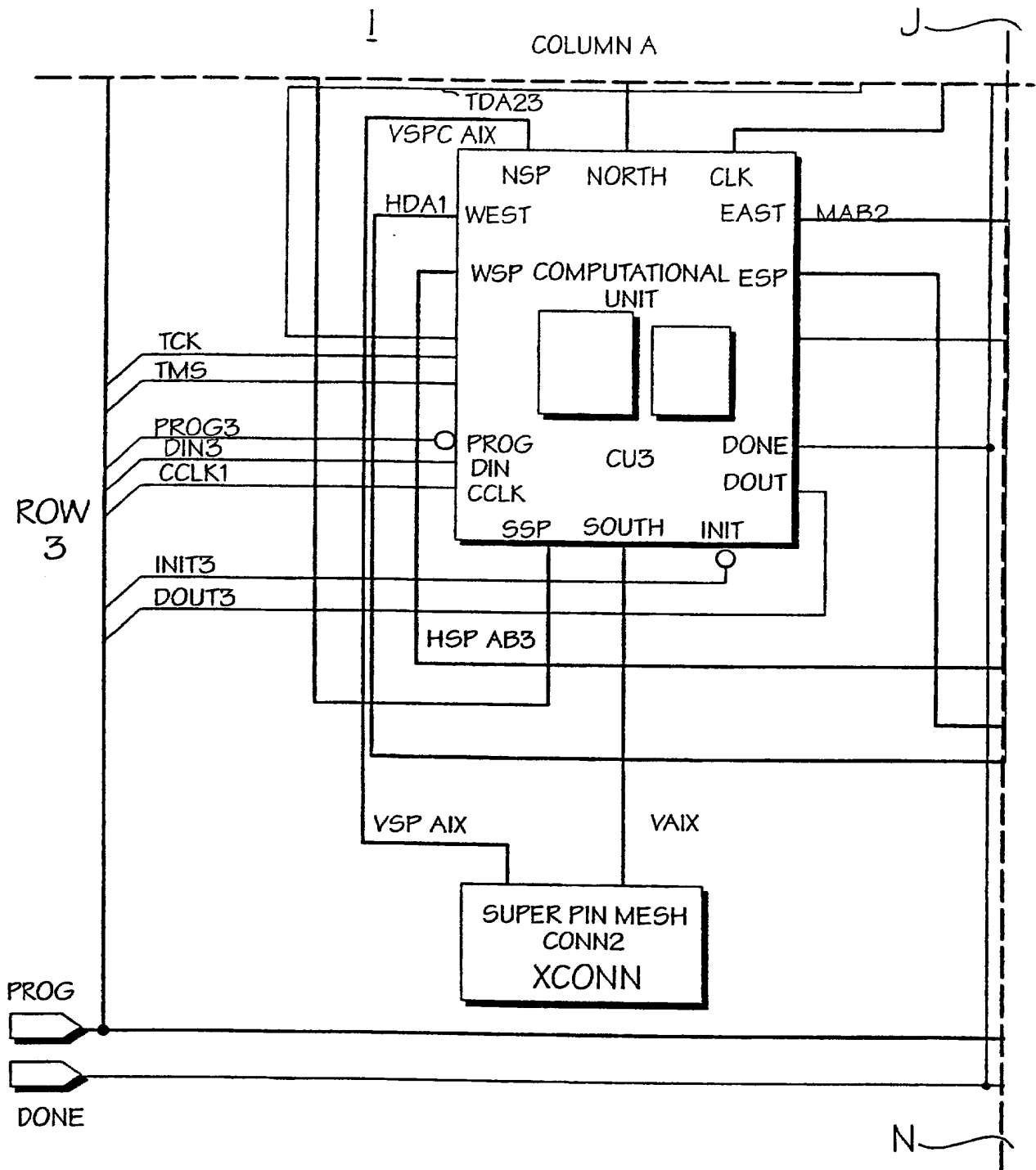


Fig. 60M

TOP SECRET

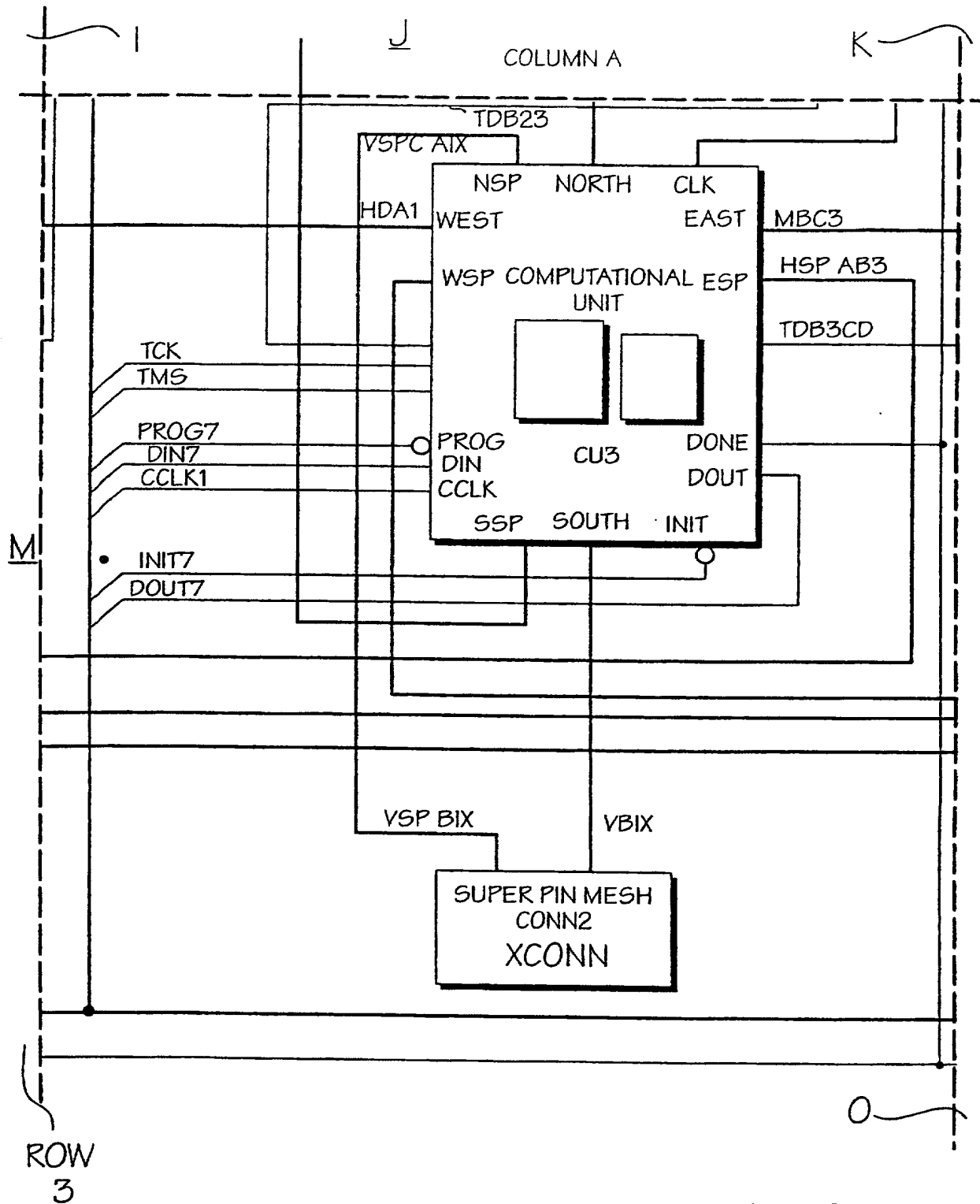


Fig. 60N

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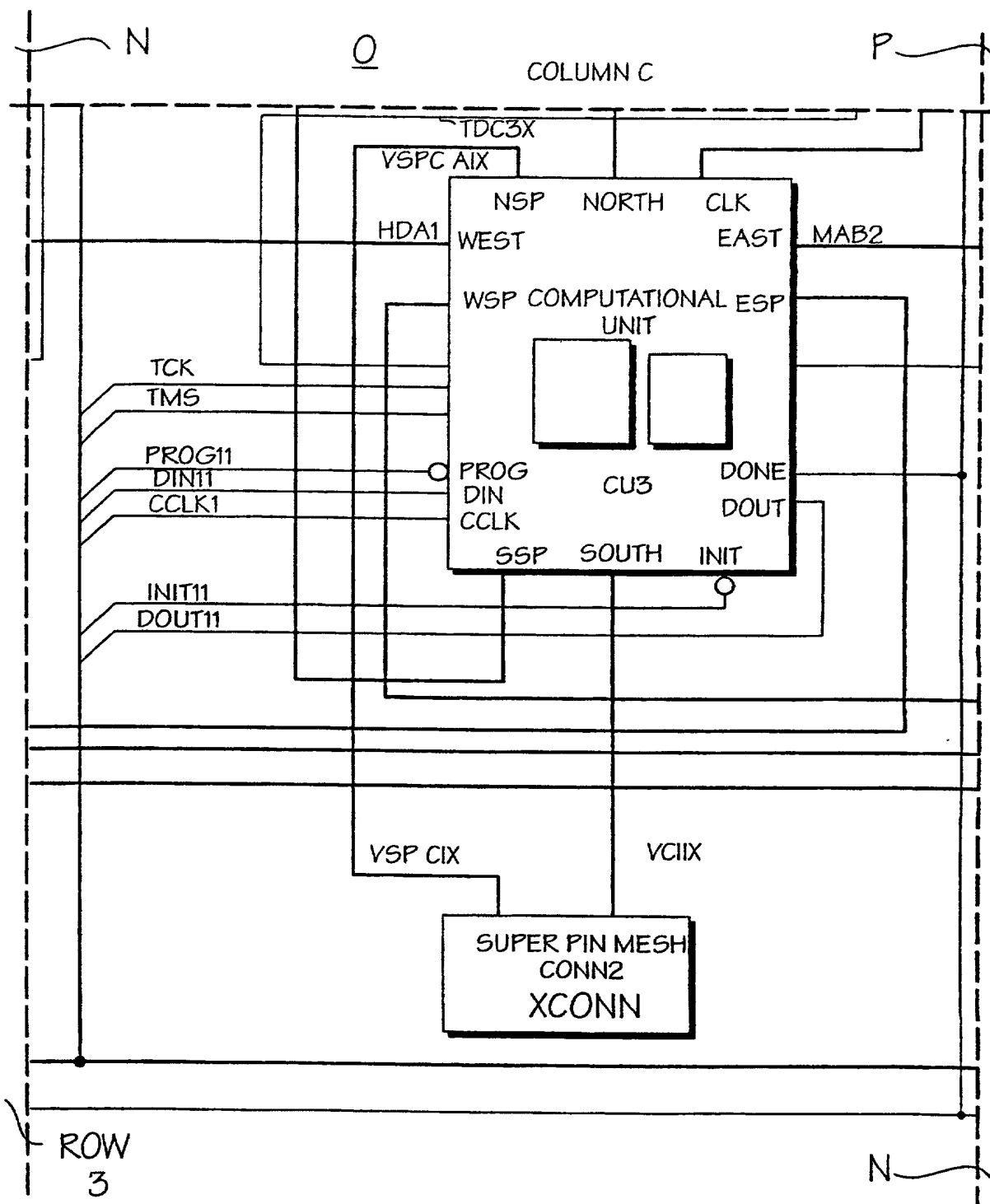


Fig. 600

The diagram illustrates a hardware component, specifically a 'WSP COMPUTATIONAL UNIT' (CU3), which is a square block with various pins and internal connections. The component is situated within a grid defined by 'COLUMN D' and 'ROW 3'. The component's pins and connections are as follows:

- Top Pins:** TCK, TMS, PROG15, DIN15, CCLK1, INIT15, DOUT, TDO.
- Left Pins:** HSP A3, HD A3, VSPD IX, VSPD IX, TD023, HSPCD3, HDA1.
- Right Pins:** NSP NORTH, CLK EAST, WEST, ESP, WSP COMPUTATIONAL UNIT, TD0, DONE, DOUT, INIT, SSP SOUTH.
- Bottom Pins:** VPIX.

The component is connected to a 'SUPER PIN MESH CONN2 XCONN' block at the bottom. The connections include: TCK, TMS, PROG15, DIN15, CCLK1, INIT15, DOUT, TDO, HSP A3, HD A3, VSPD IX, VSPD IX, TD023, HSPCD3, HDA1, NSP NORTH, CLK EAST, WEST, ESP, WSP COMPUTATIONAL UNIT, TD0, DONE, DOUT, INIT, SSP SOUTH, and VPIX.

Fig. 60P

FIG. 61

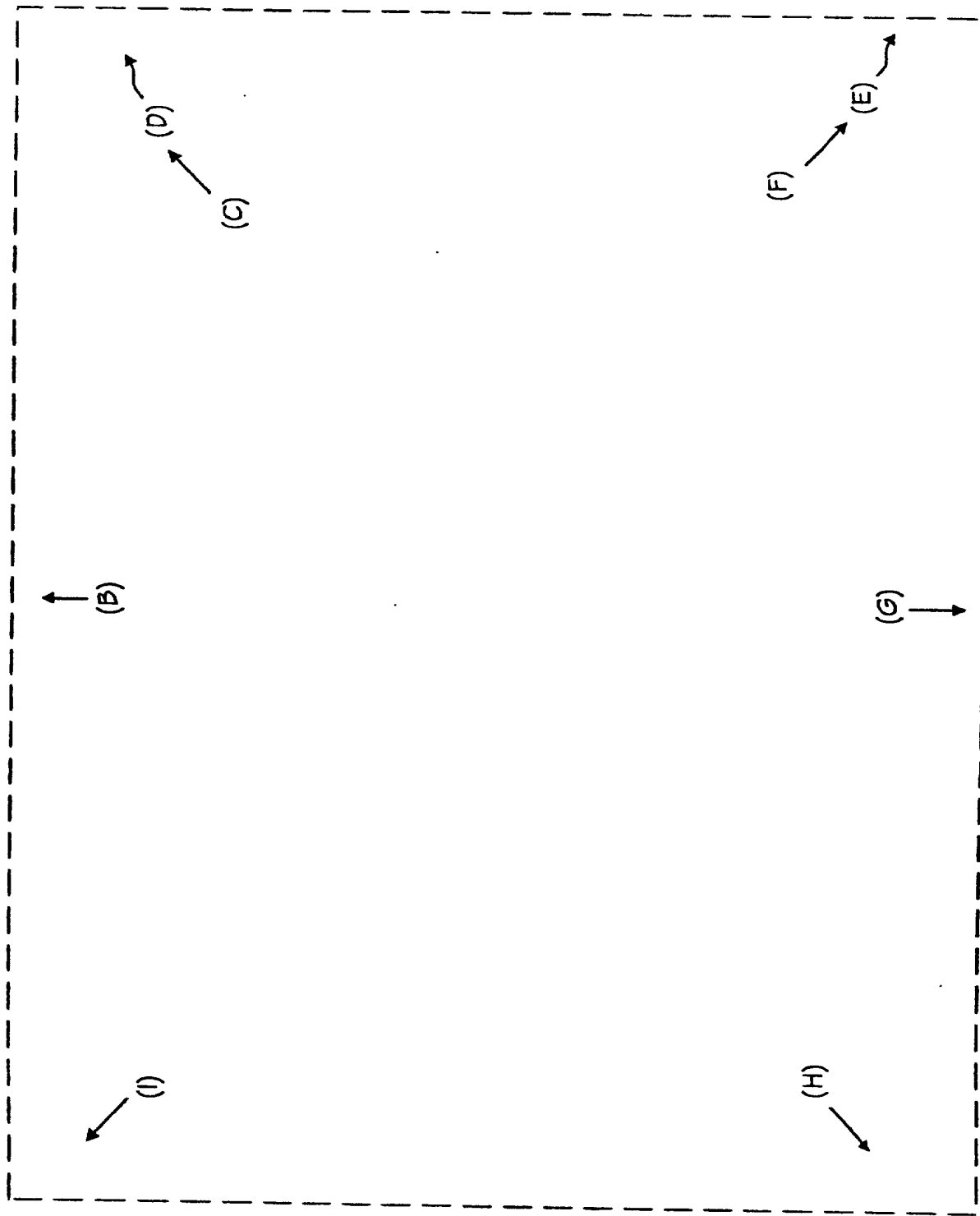


FIG. 61(A)

[illegible]

FIG. 61(B)

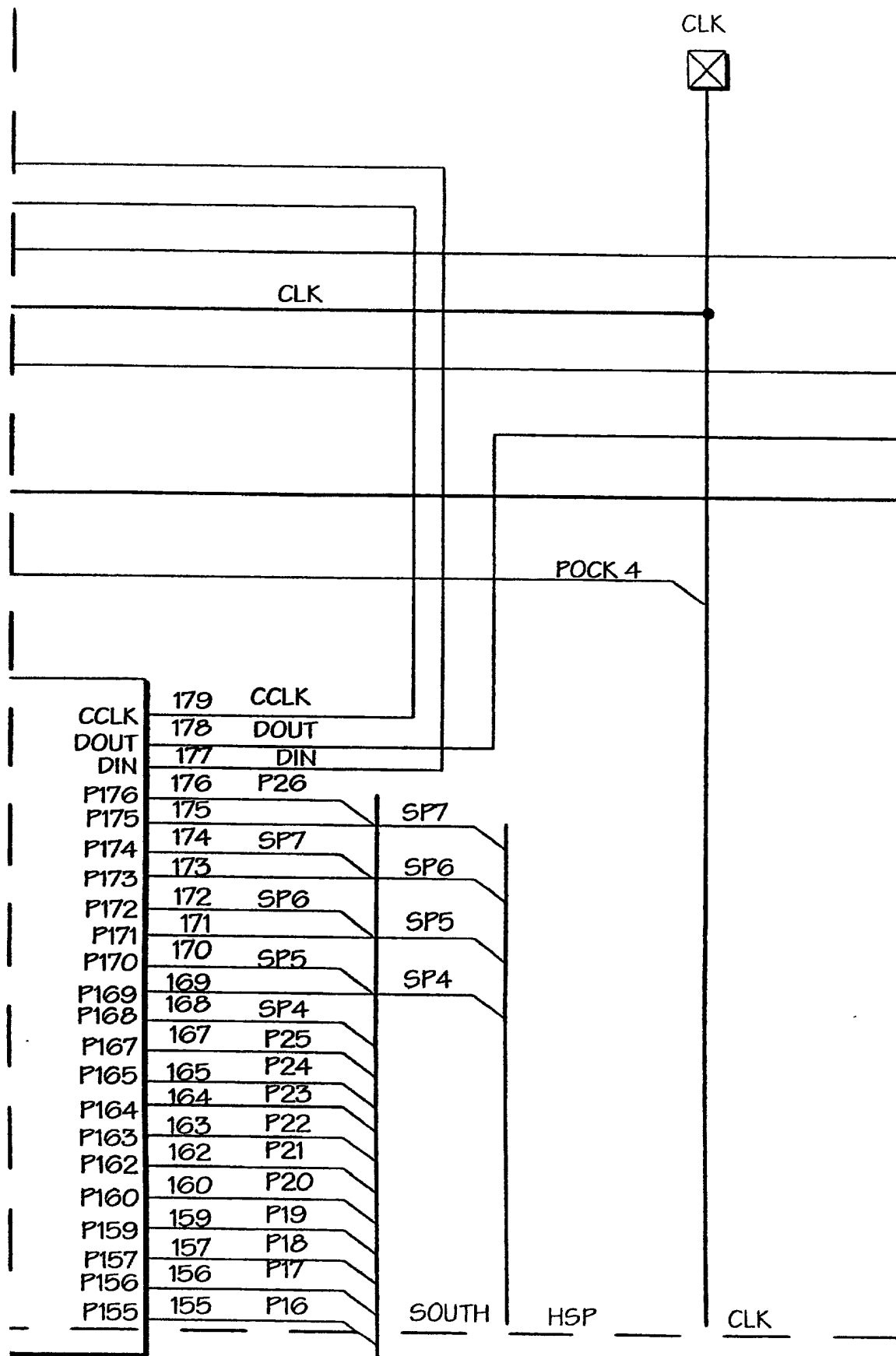


FIG. 61(C)



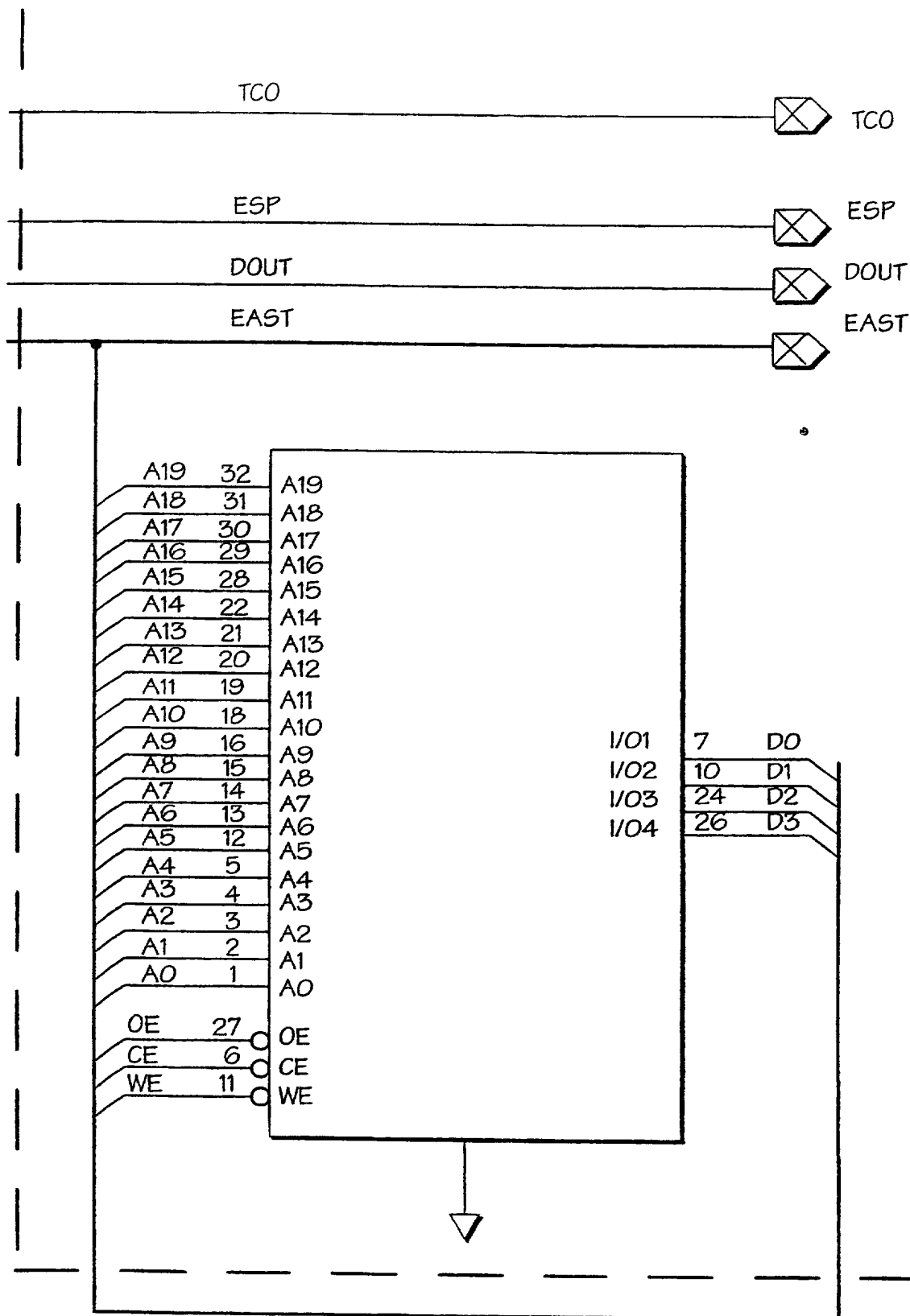


FIG. 61(D)

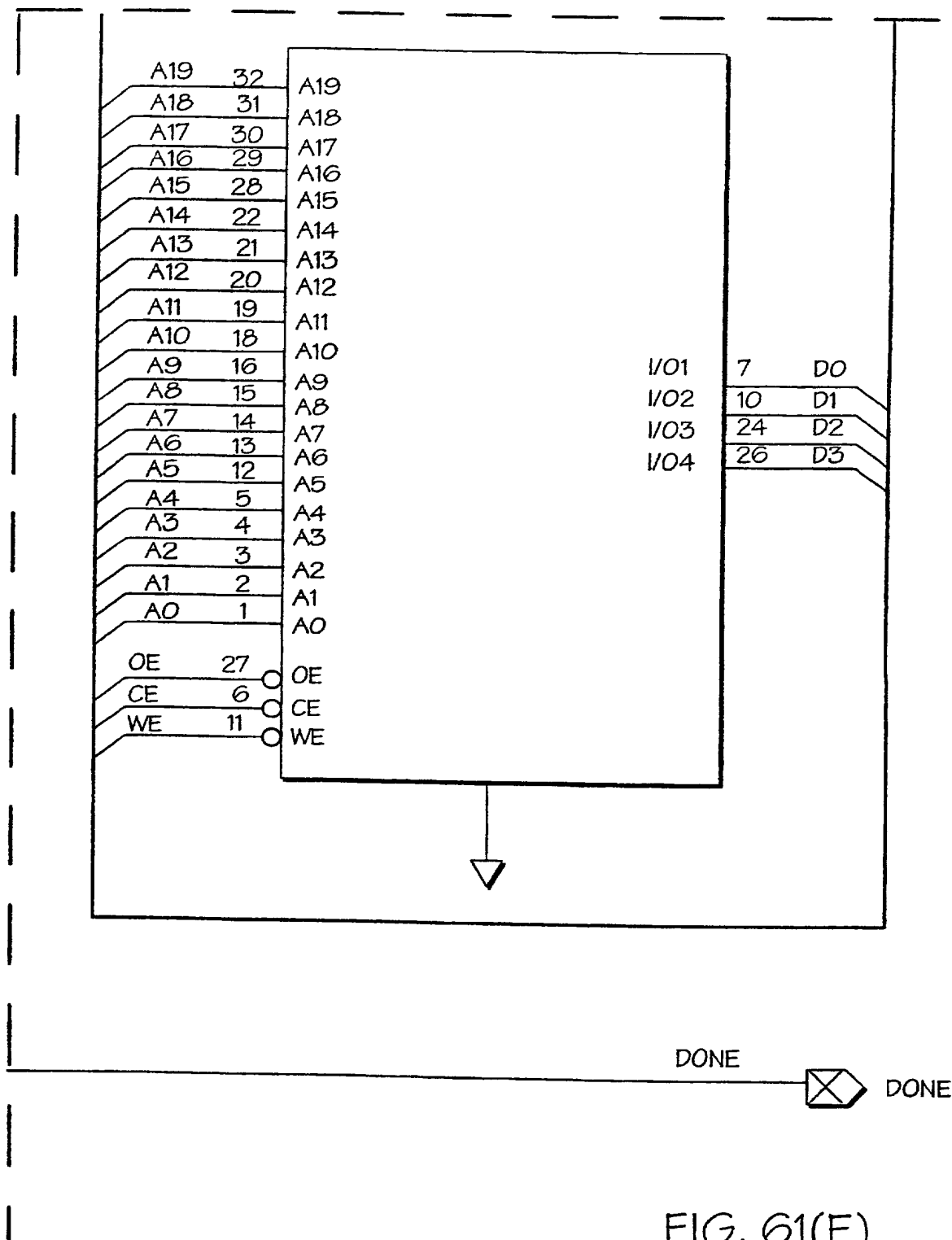
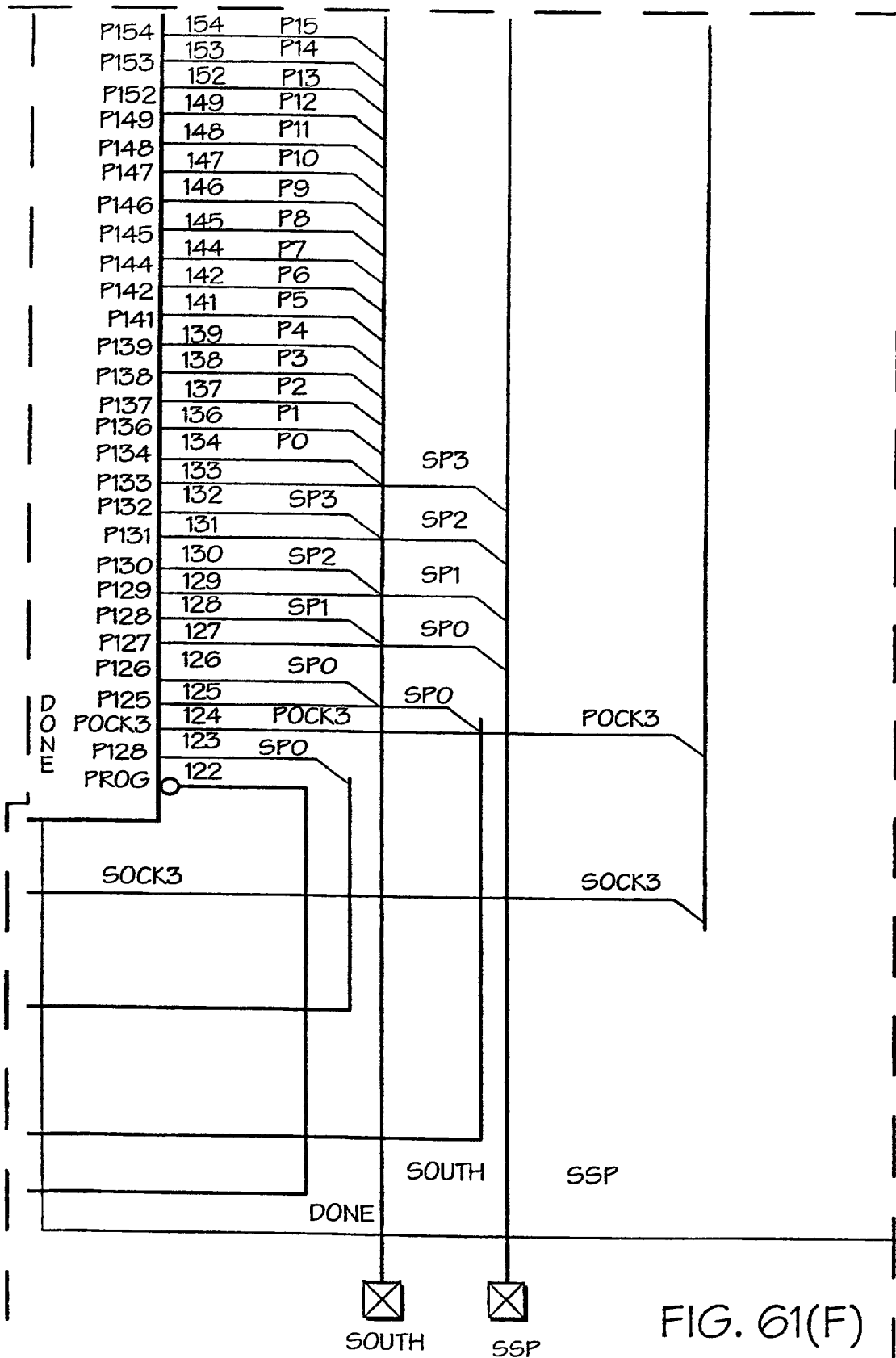


FIG. 61(E)



[illegible]

FIG. 61(G)

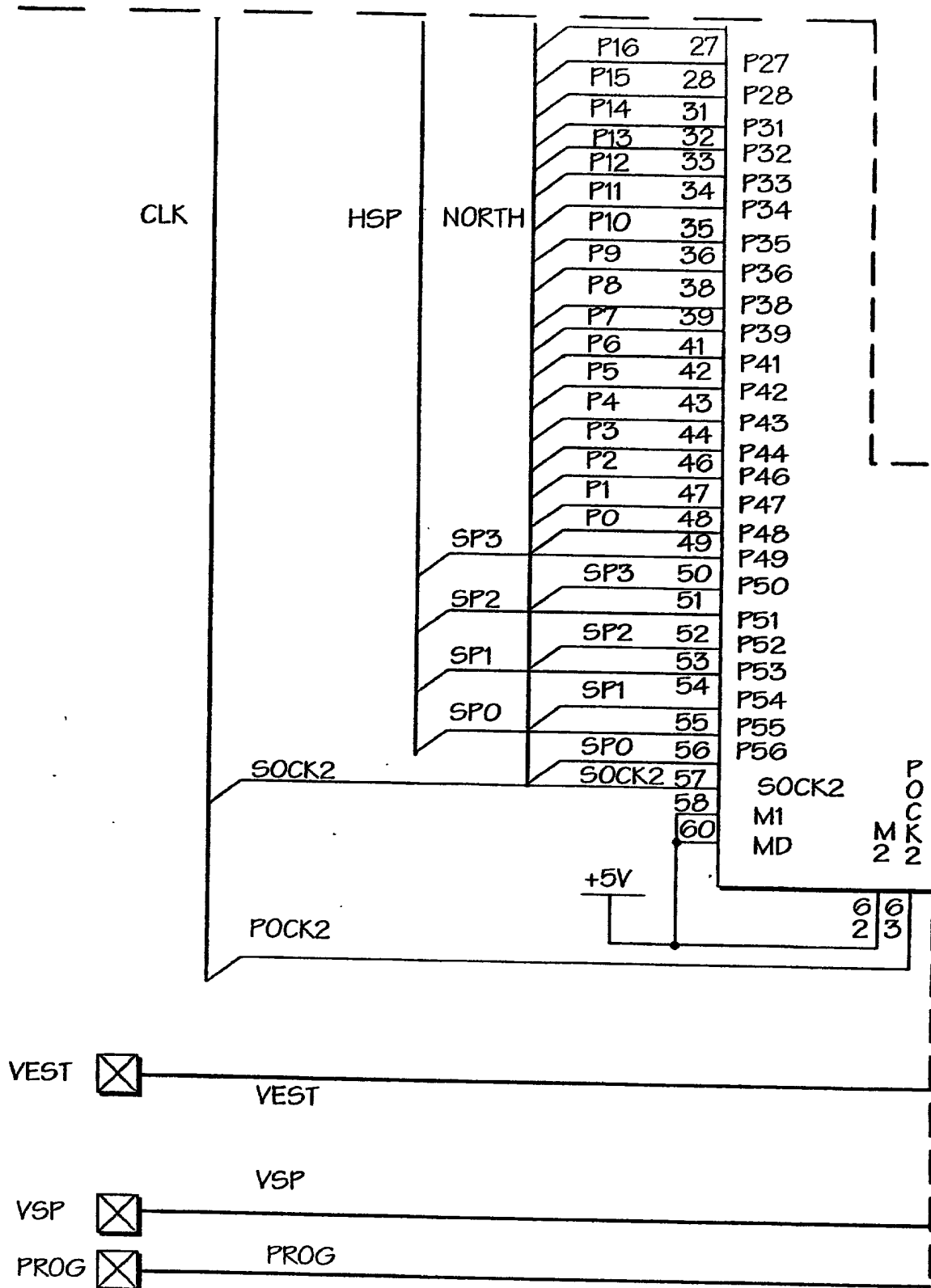


FIG. 61(H)



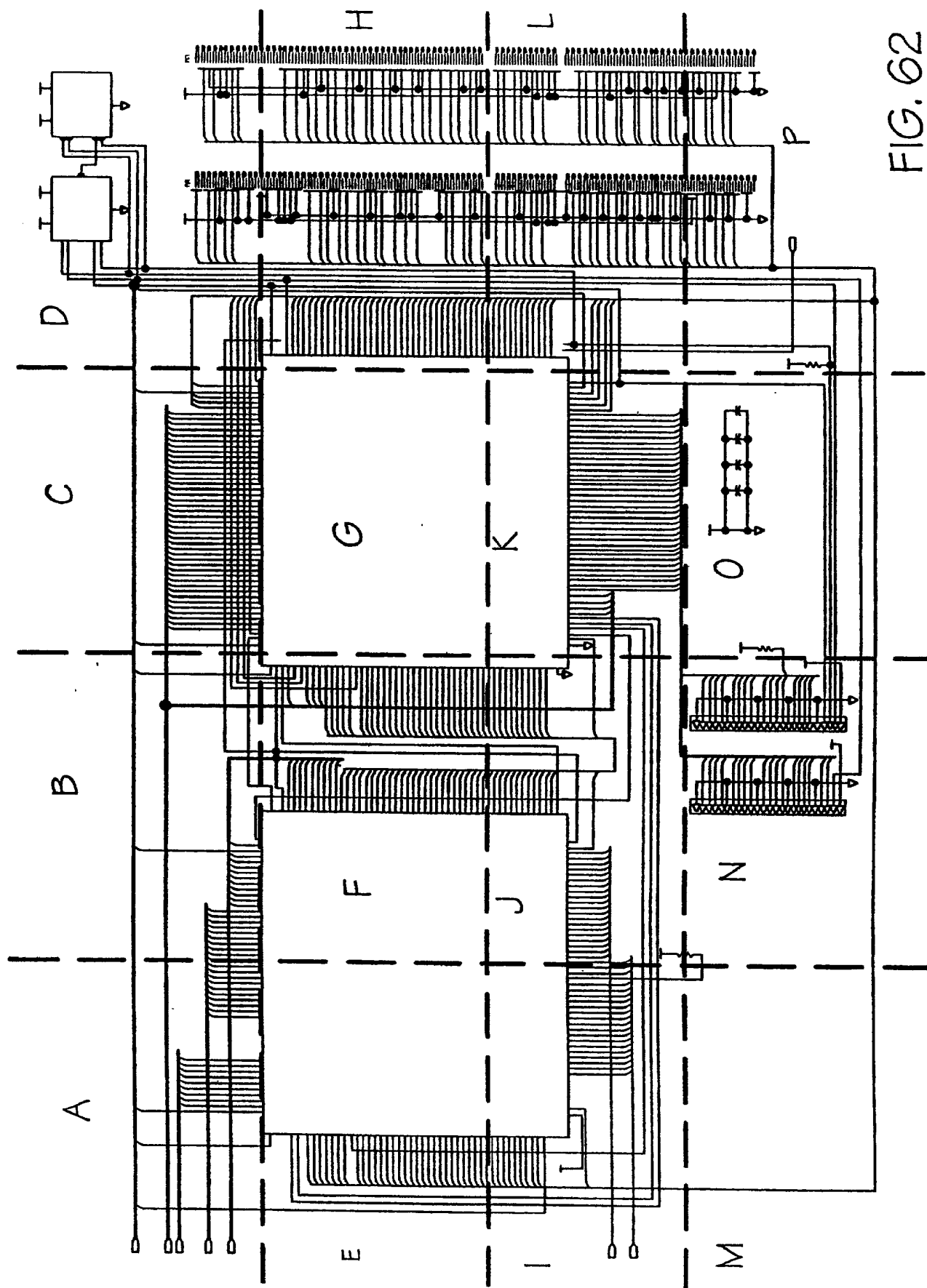
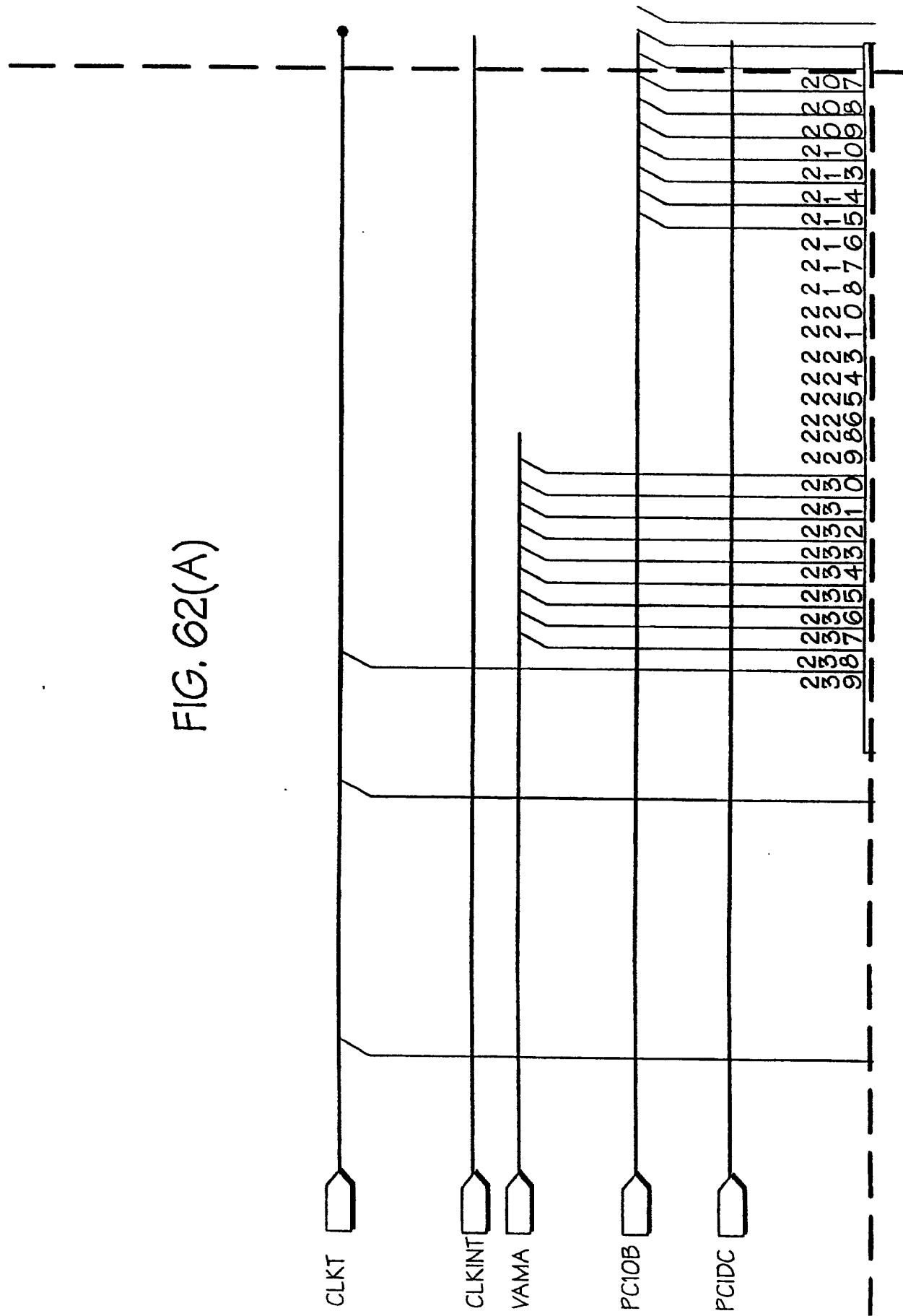


FIG. 62

FIG. 62(A)





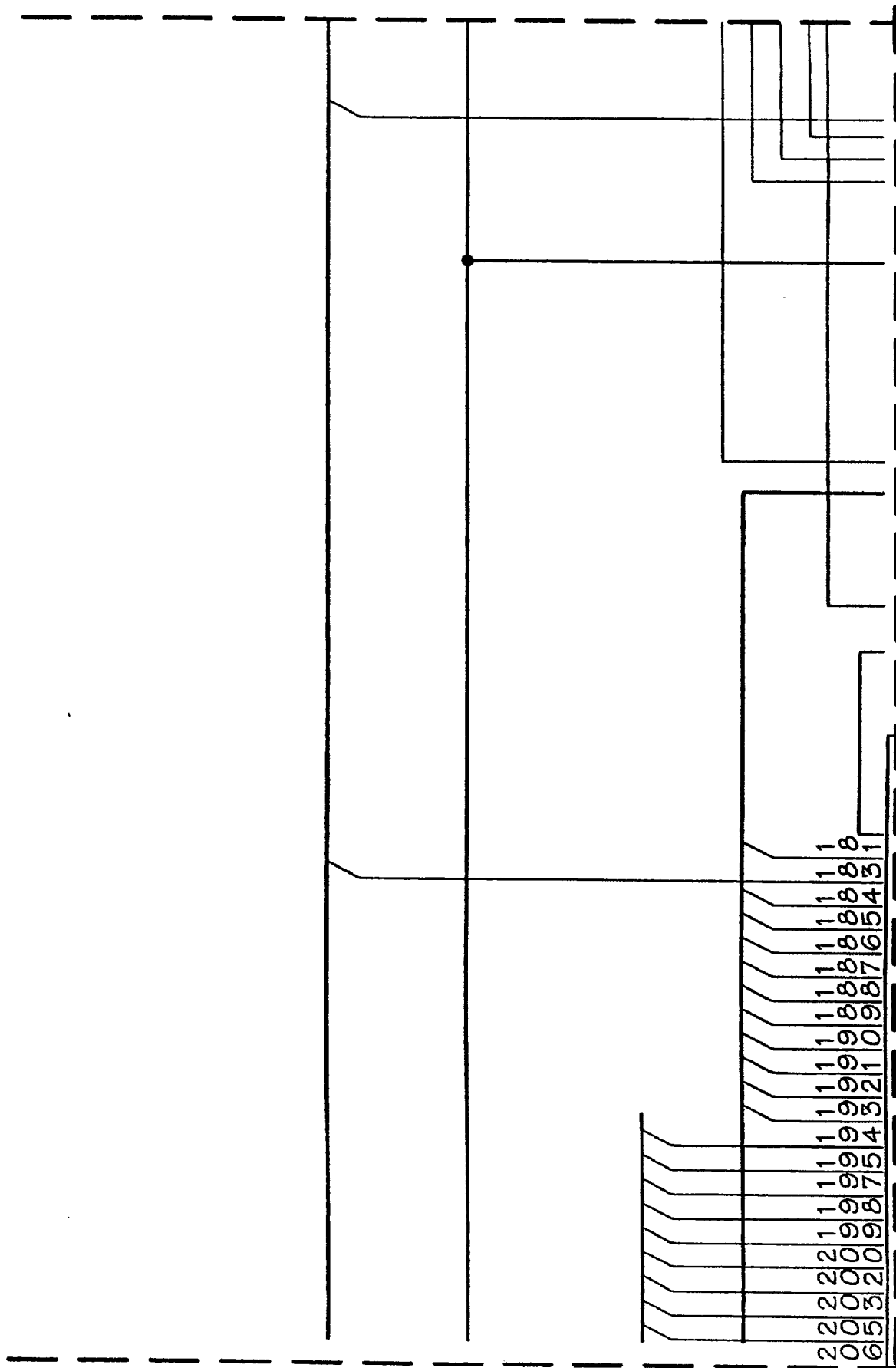
[illegible]

FIG. 62(B)

[illegible]

L

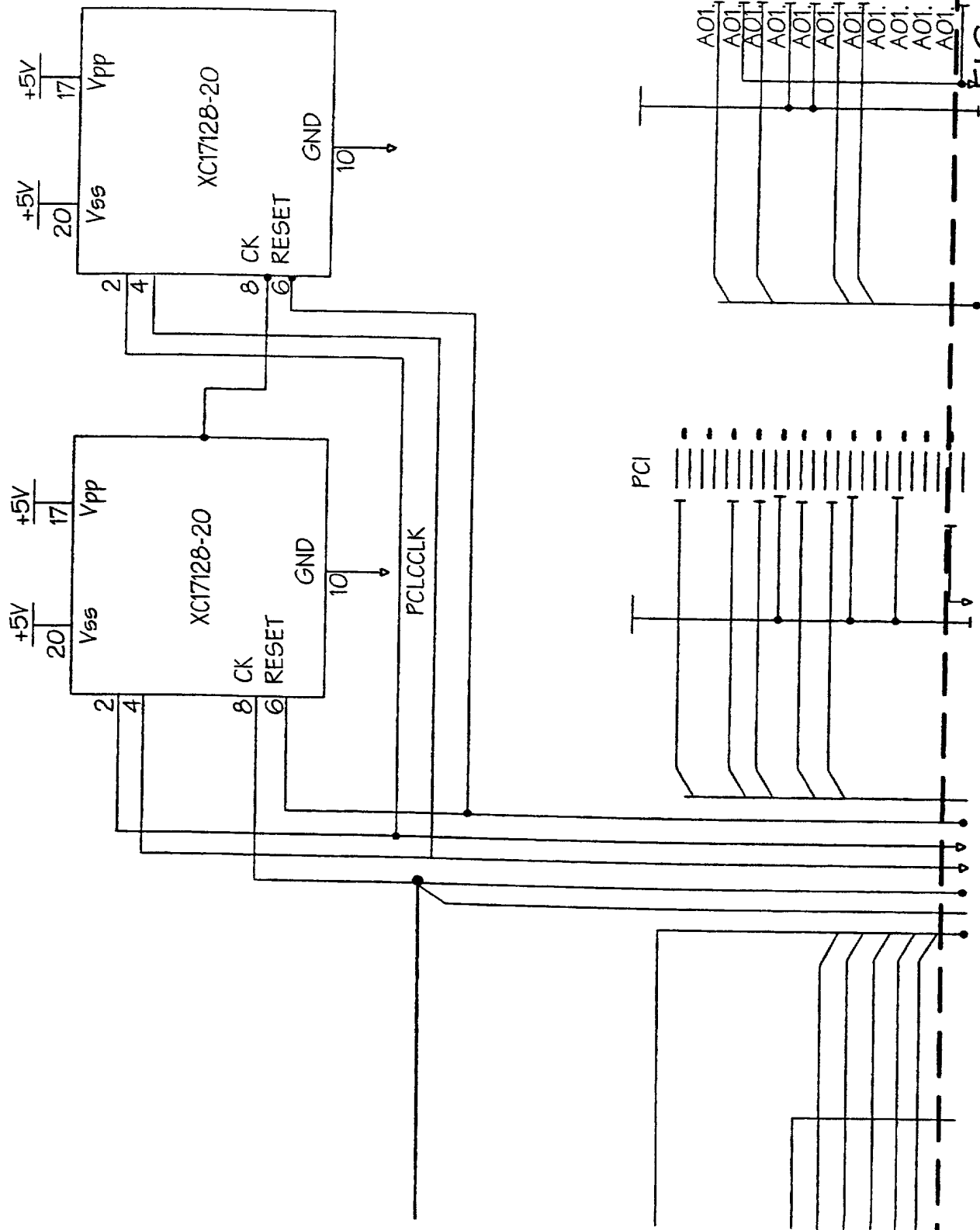


FIG. 62(D)

[illegible]

FIG. 62(E)

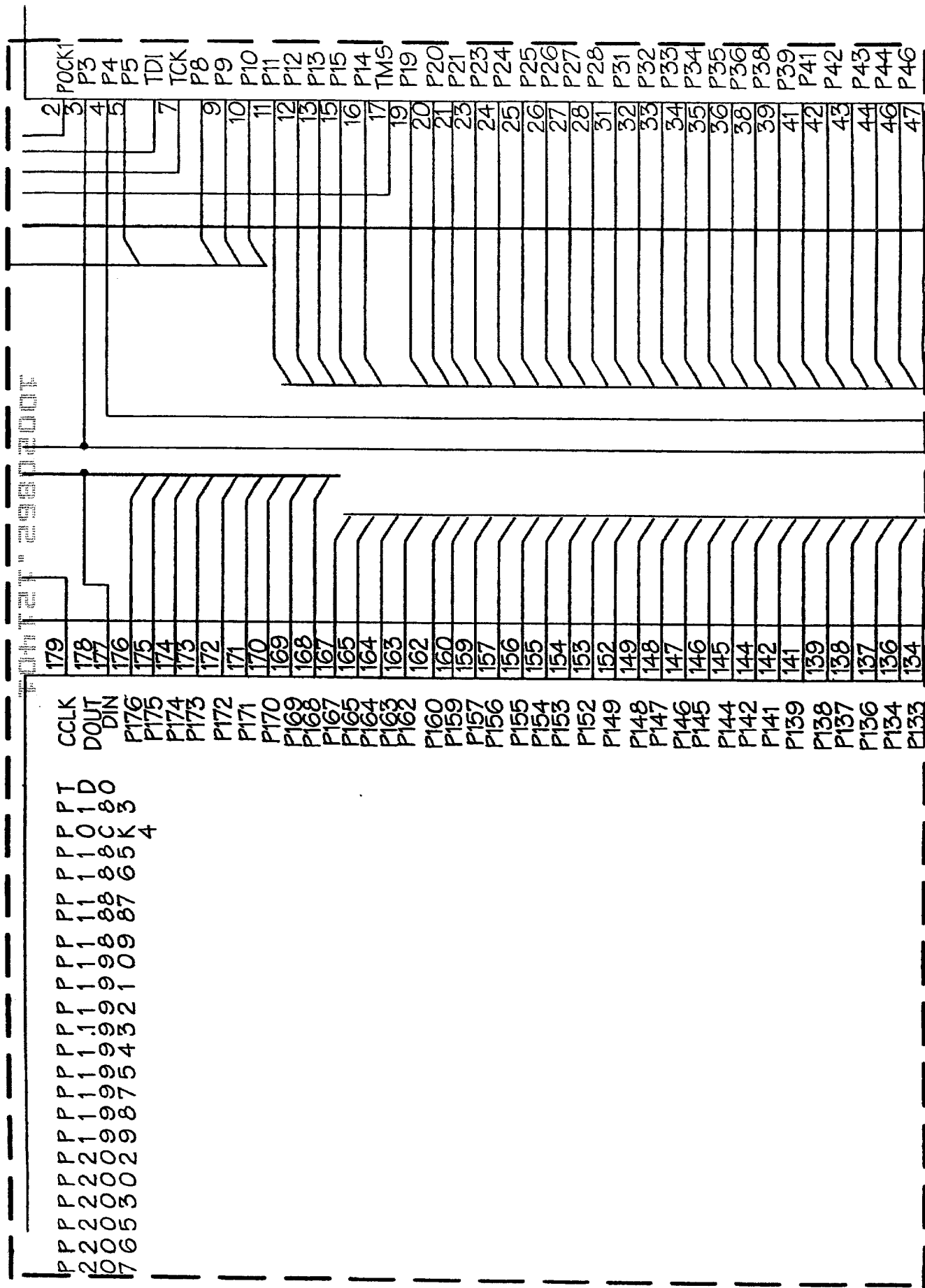


FIG. 62(F)



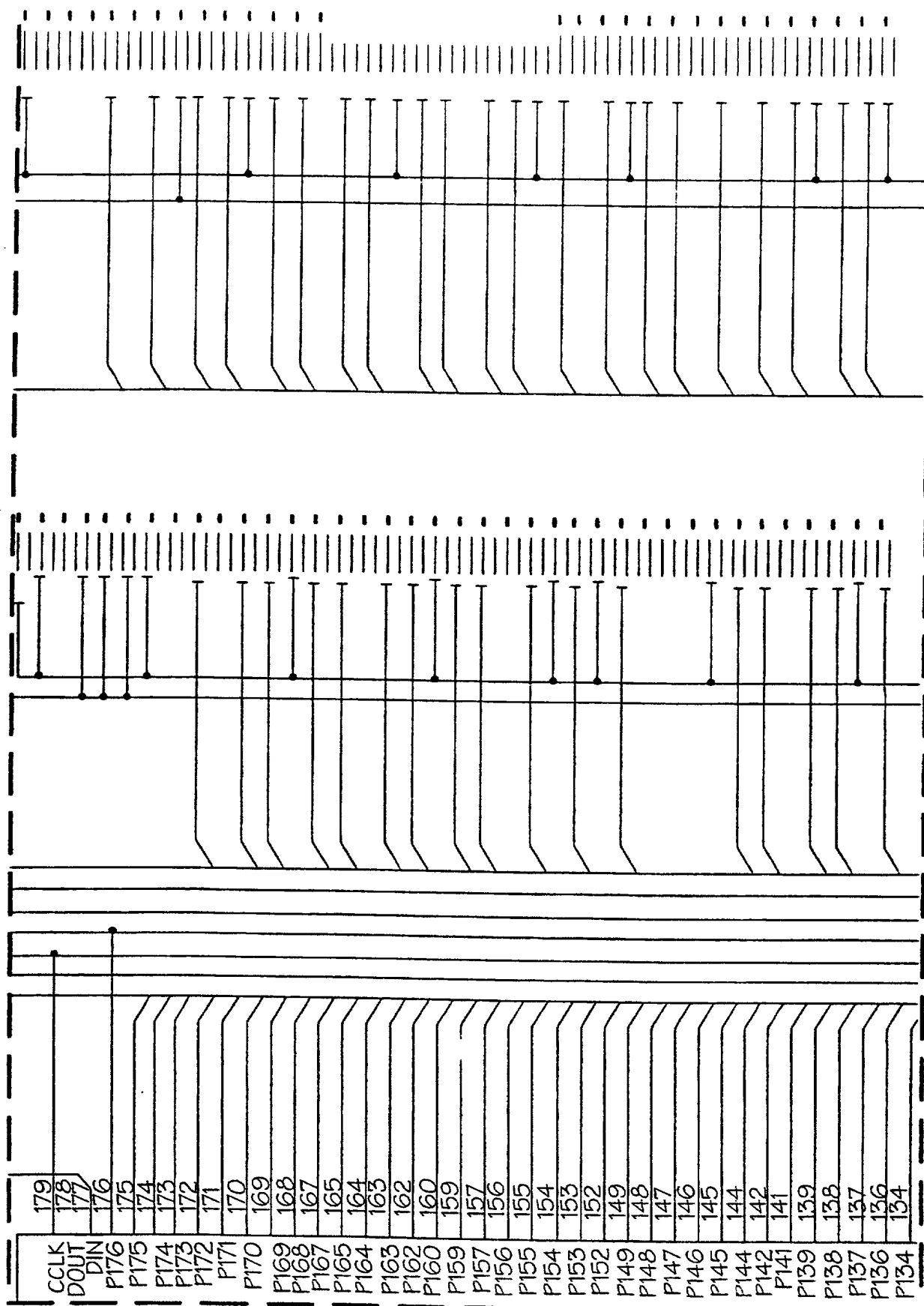
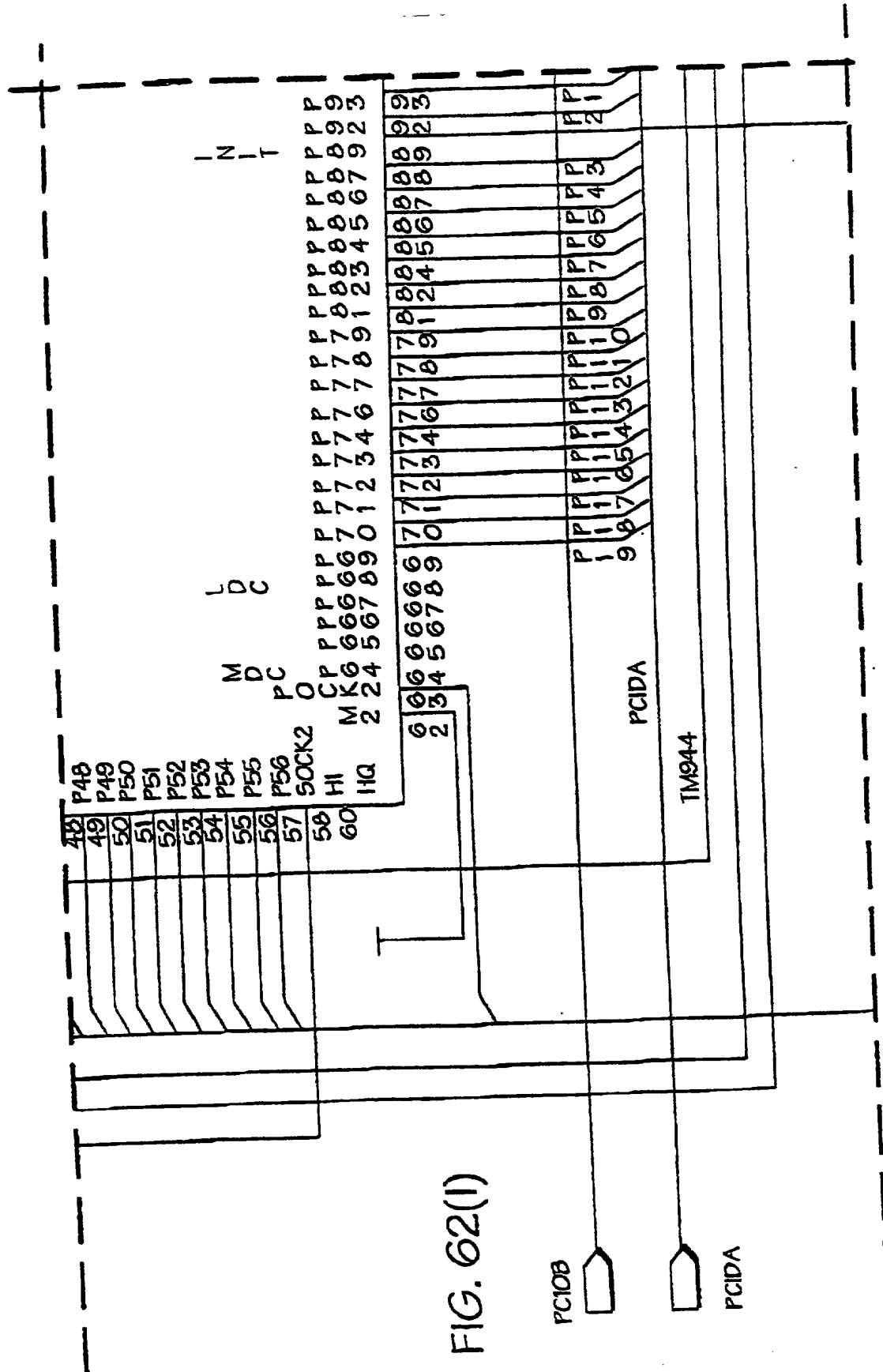


FIG. 62(H)





[illegible]

FIG. 62(J)

FIG. 62(K)

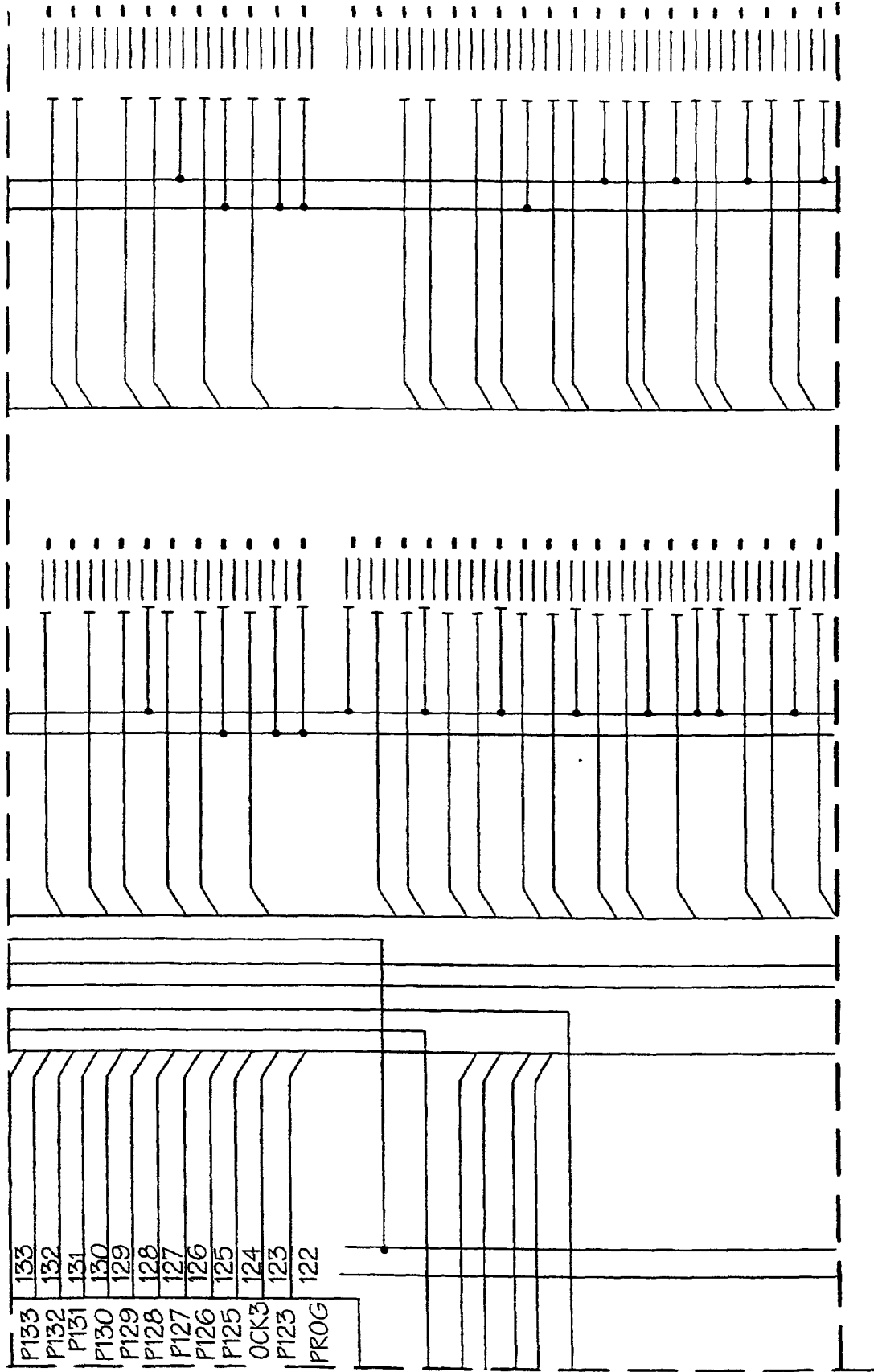
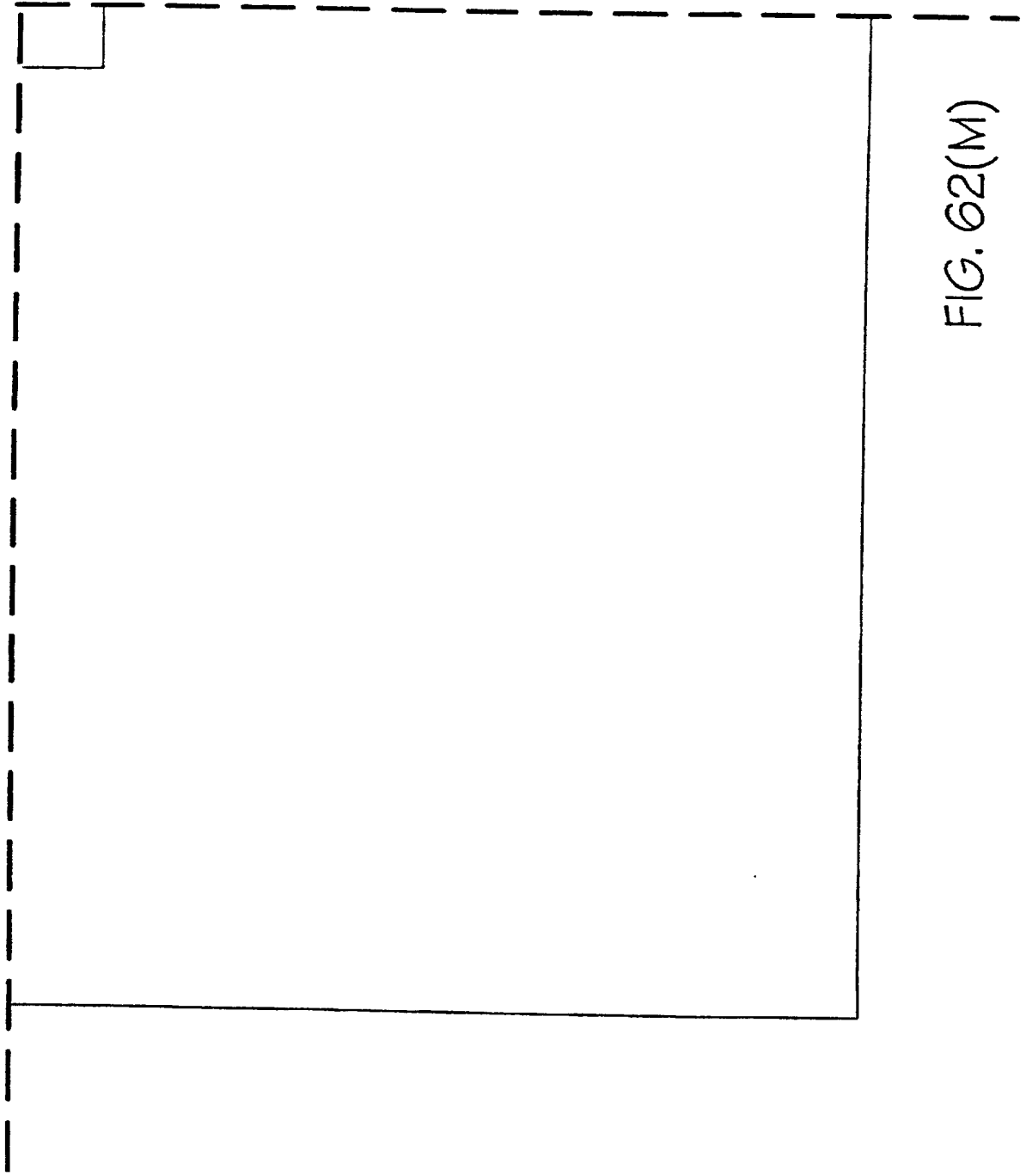


FIG. 62(L)



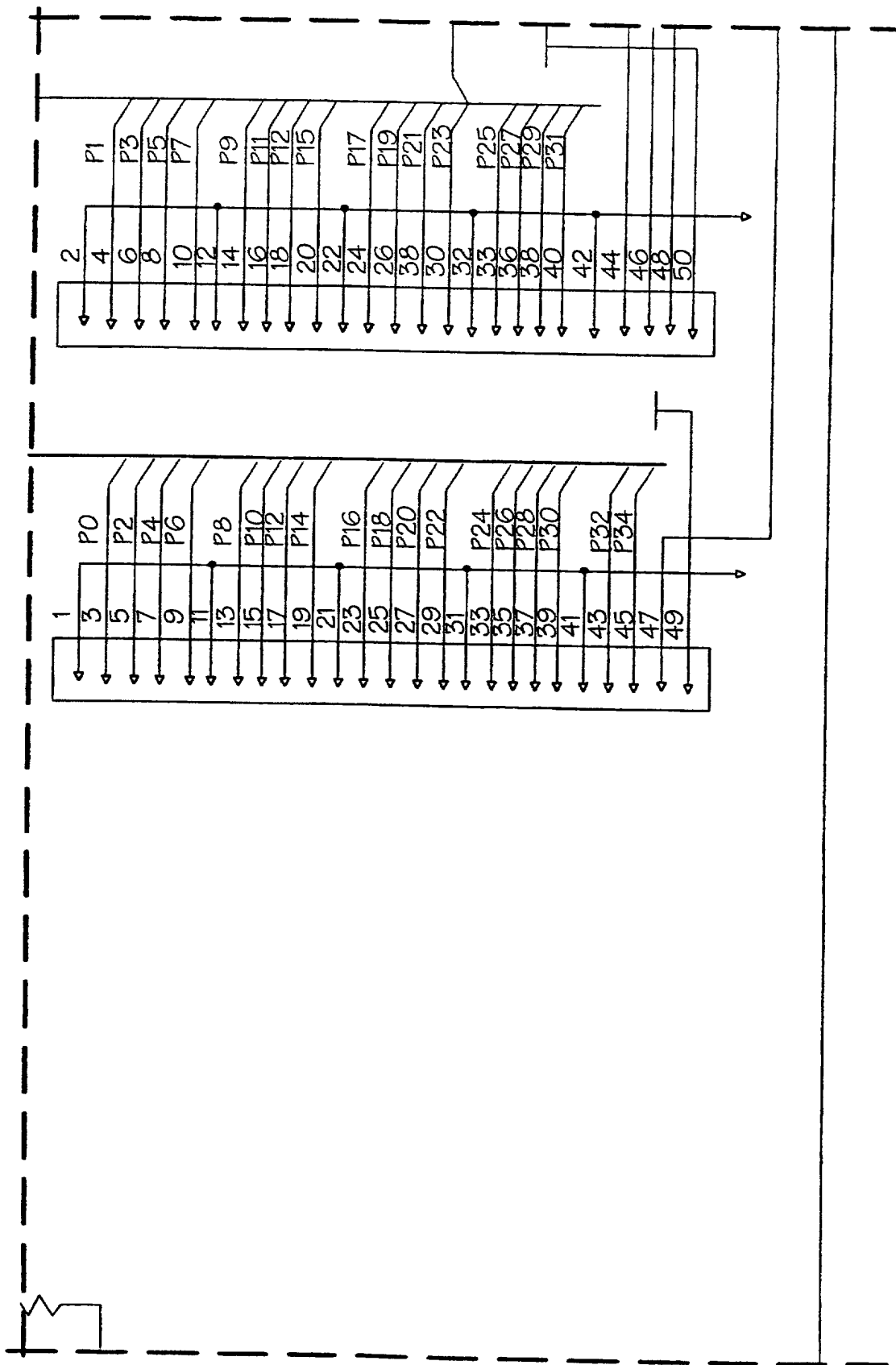


FIG. 62(N)

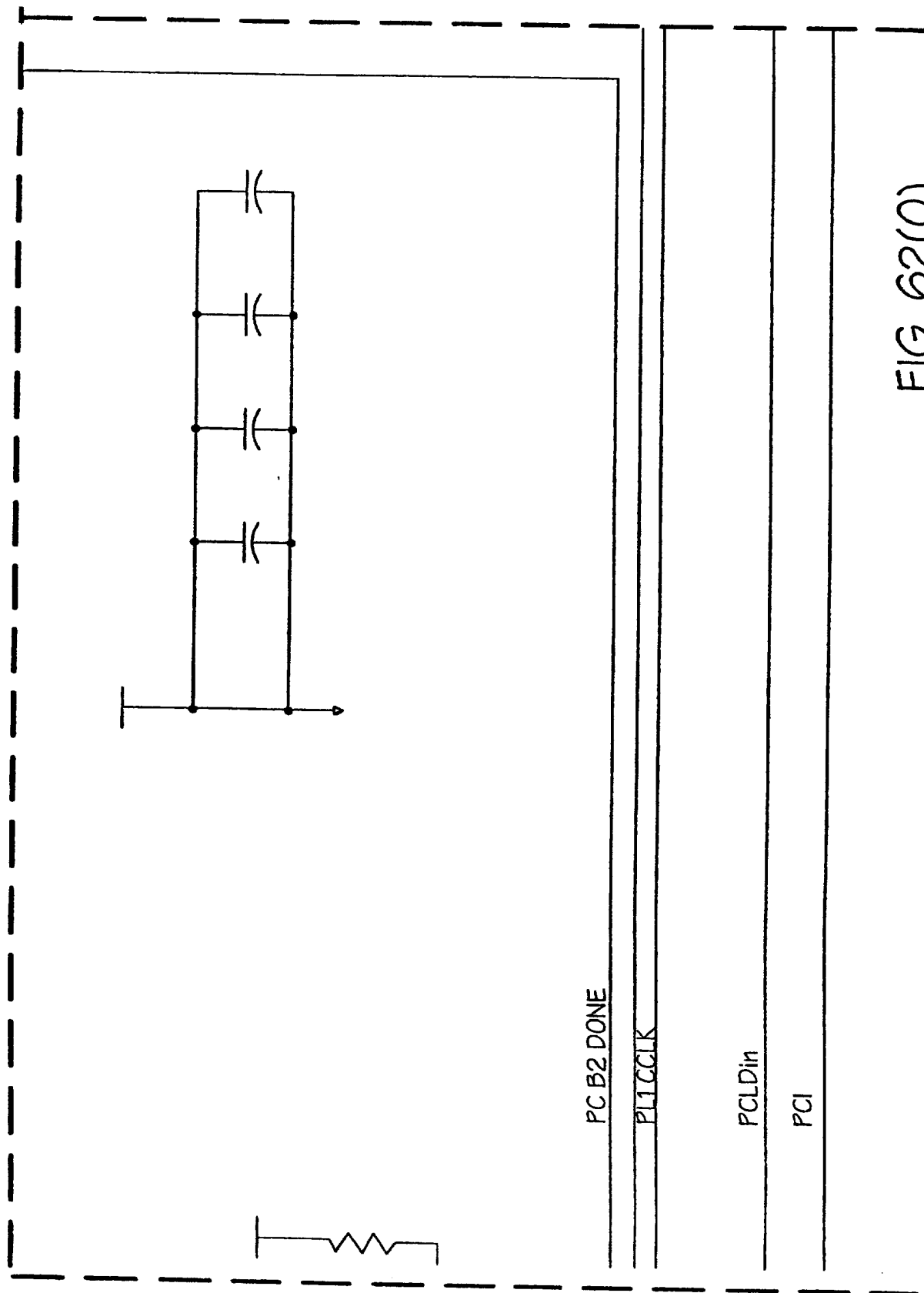


FIG. 62(0)

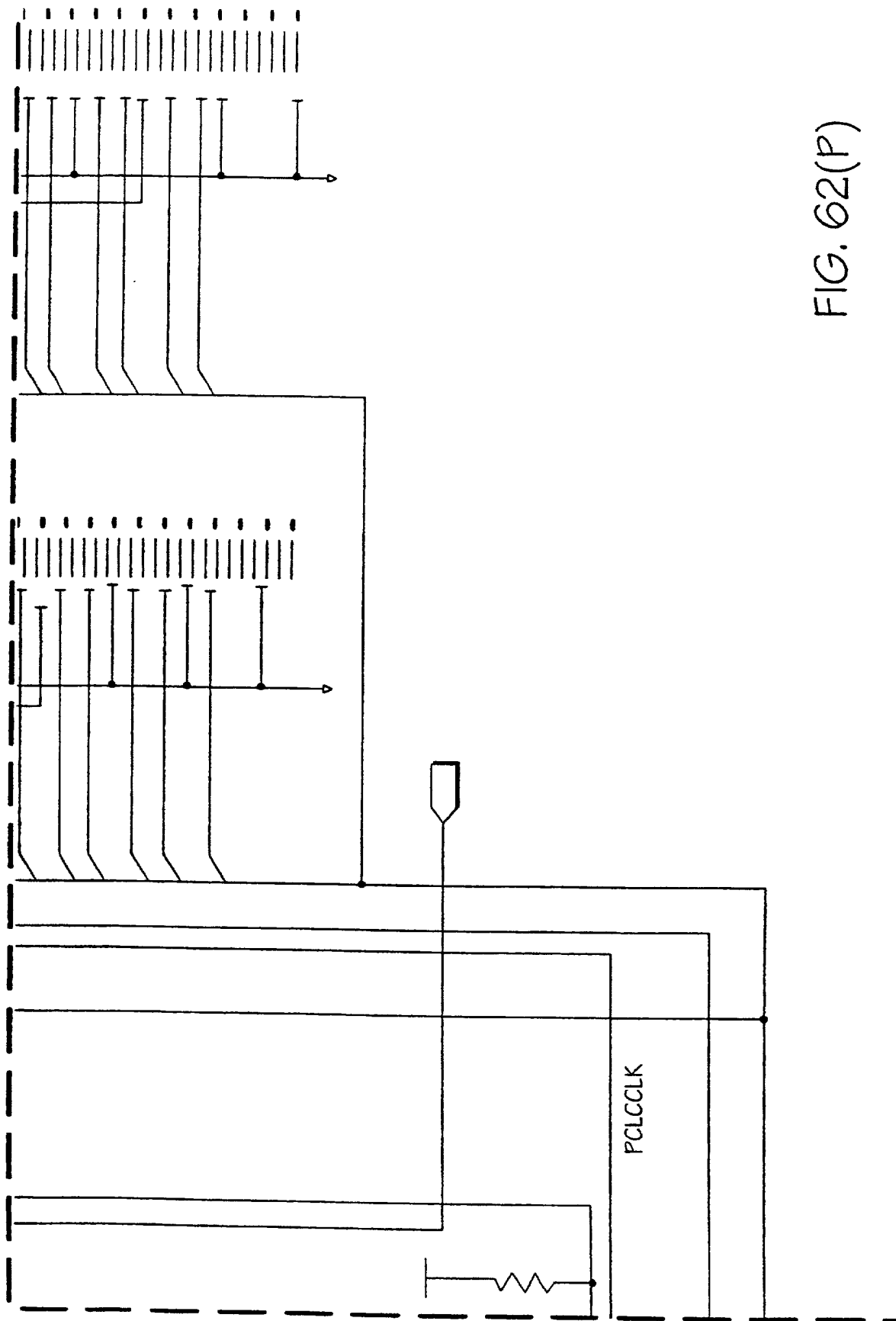


FIG. 62(P)

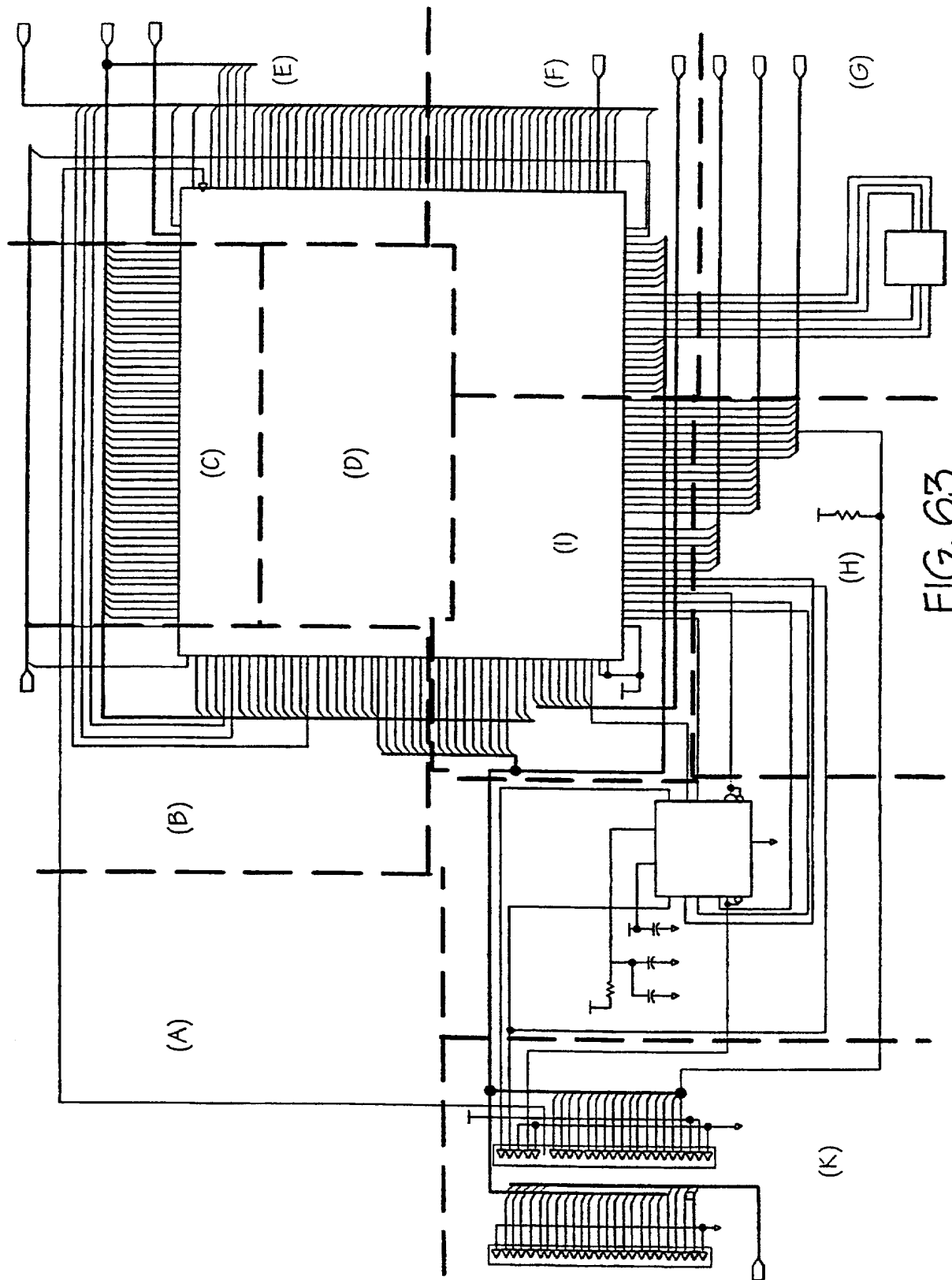
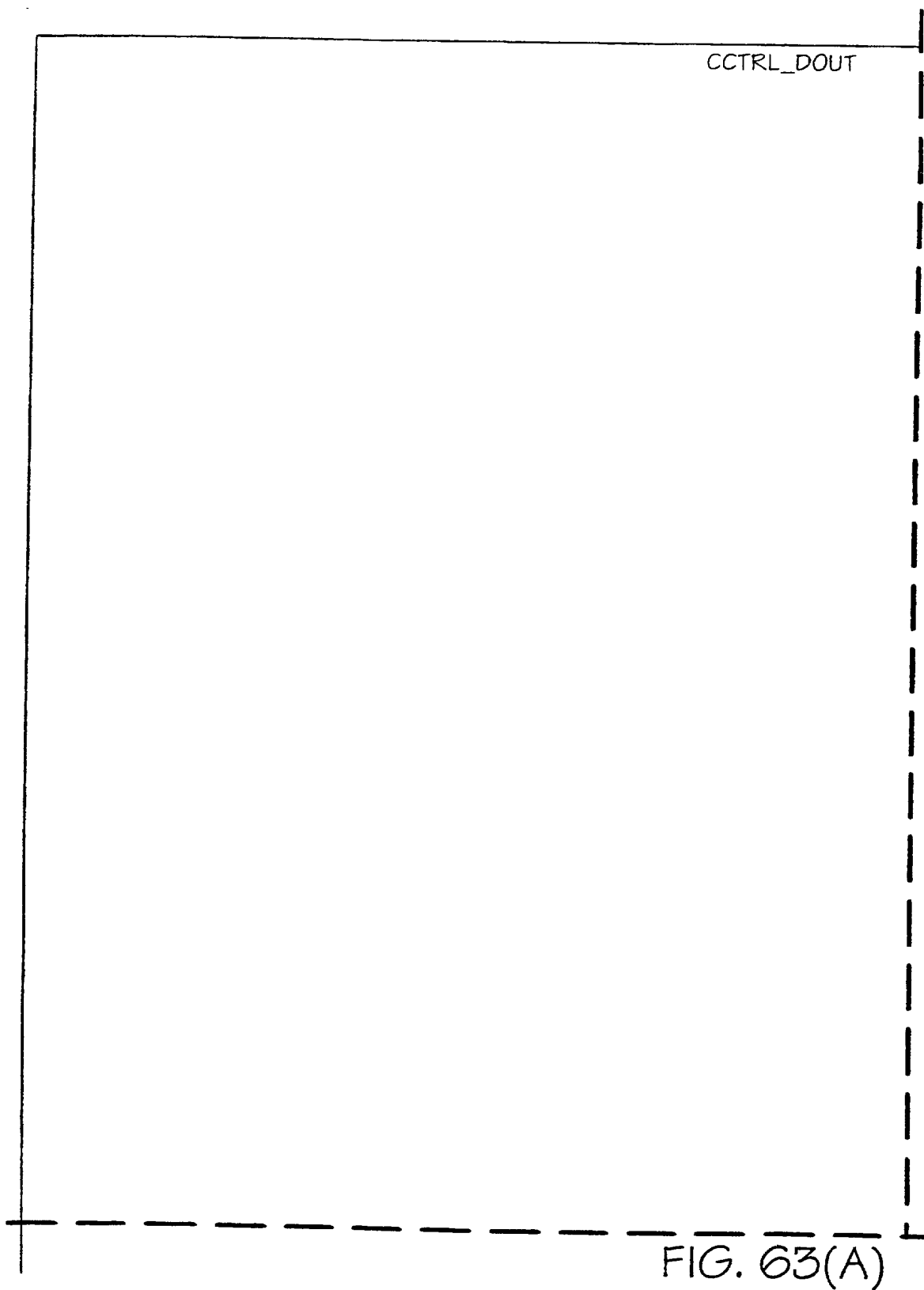


FIG. 63



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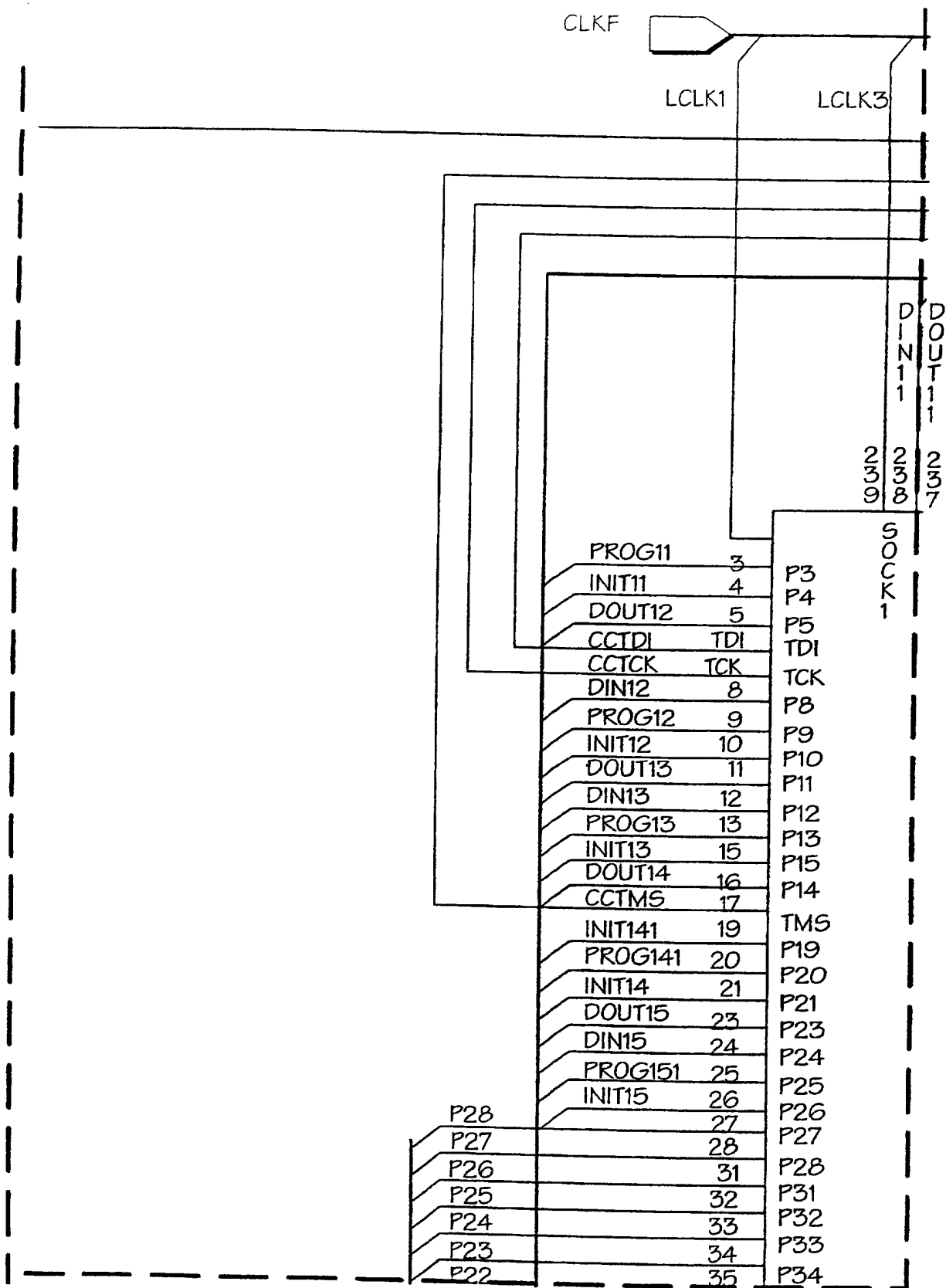


FIG. 63(B)

DOU10	184	P1185
D-N0	185	P1186
PROG0	187	P1187
-N10	188	P1188
DOU11	189	P1190
D-N1	190	P1191
PROG1	191	P1192
-N11	192	P1193
DOU12	193	P1194
D-N2	194	P1195
PROG2	195	P1197
-N12	197	P1198
DOU13	198	P1199
D-N3	199	P2200
PROG3	200	P2202
-N13	202	P2203
DOU14	203	P2205
D-N4	205	P2206
PROG4	206	P2207
-N14	207	P2208
DOU15	208	P2209
D-N5	209	P2210
PROG5	210	P2213
-N15	213	P2214
DOU16	214	P2215
D-N6	215	P2216
PROG6	216	P2217
-N16	217	P2218
DOU17	218	P2220
D-N7	220	P2221
PROG7	221	P2223
-N17	223	P2224
DOU18	224	P2225
D-N8	225	P2226
PROG8	226	P2228
-N18	228	P2229
DOU19	229	P2230
D-N9	230	P2231
PROG9	231	P2232
-N19	232	P2233
DOU110	233	P2234
D-N10	234	P2235
PROG10	235	P2236
-N110	236	P2237
DOU111	237	P2238

FIG. 63(C)

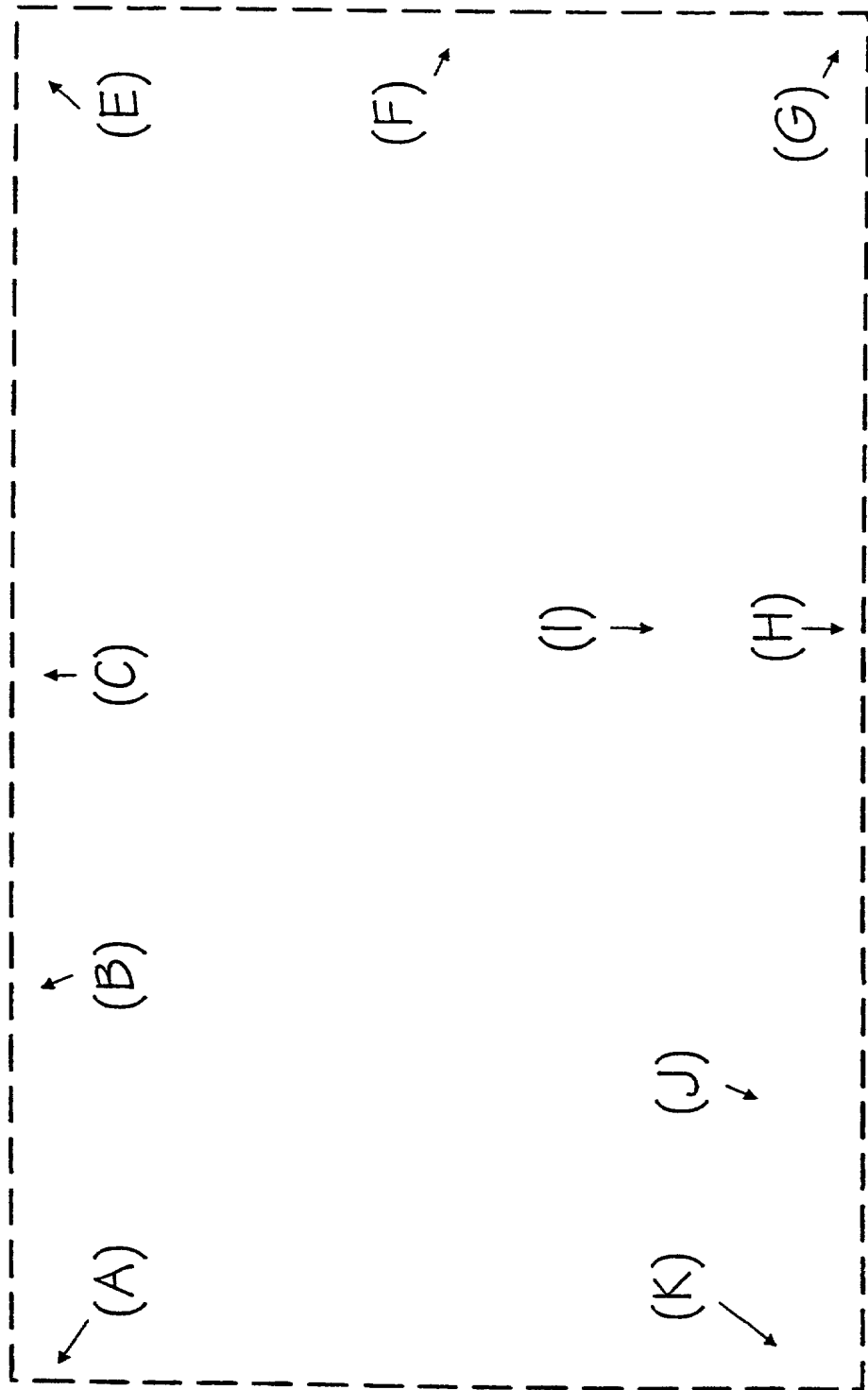


FIG. 63(D)

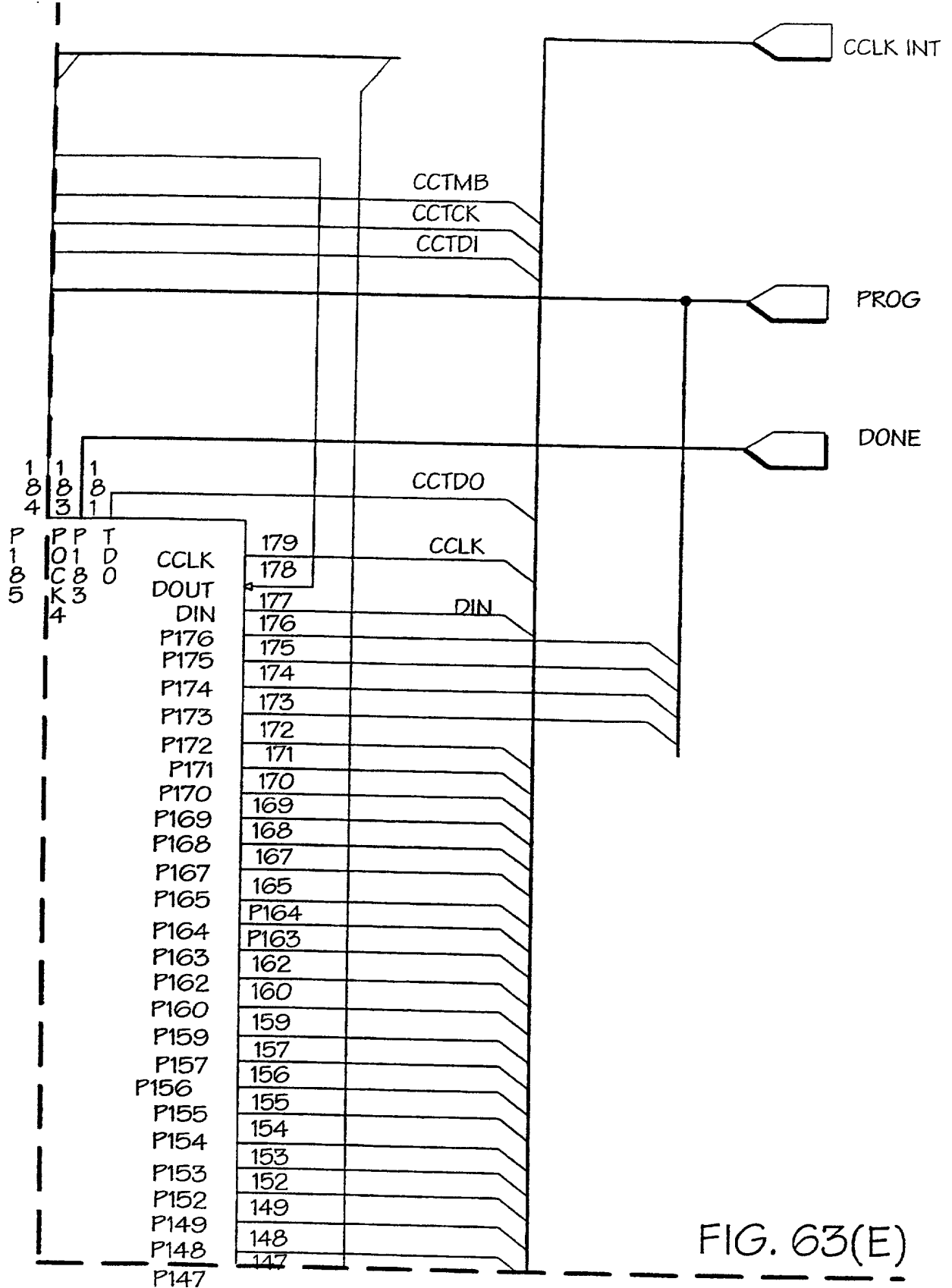


FIG. 63(E)

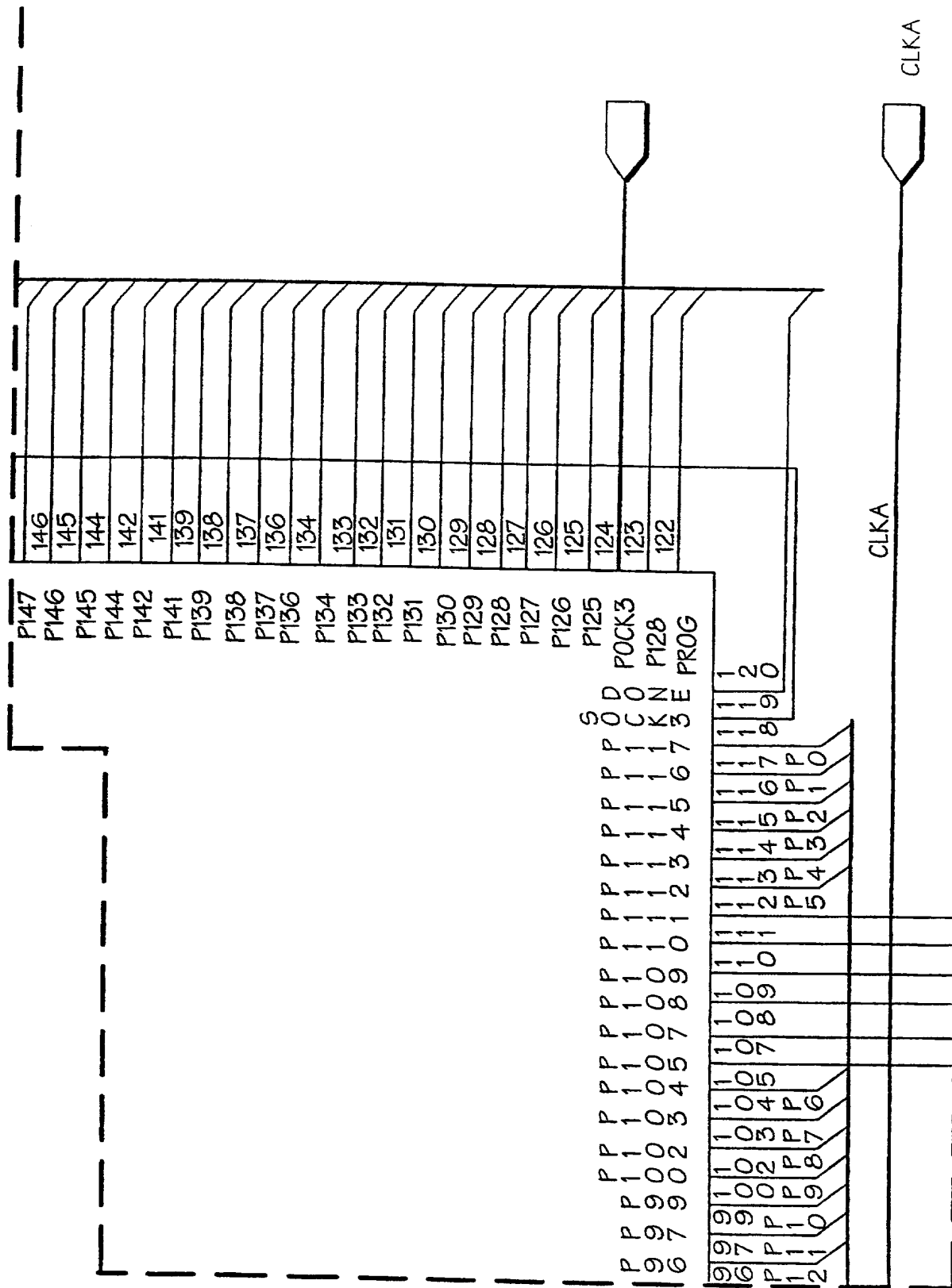
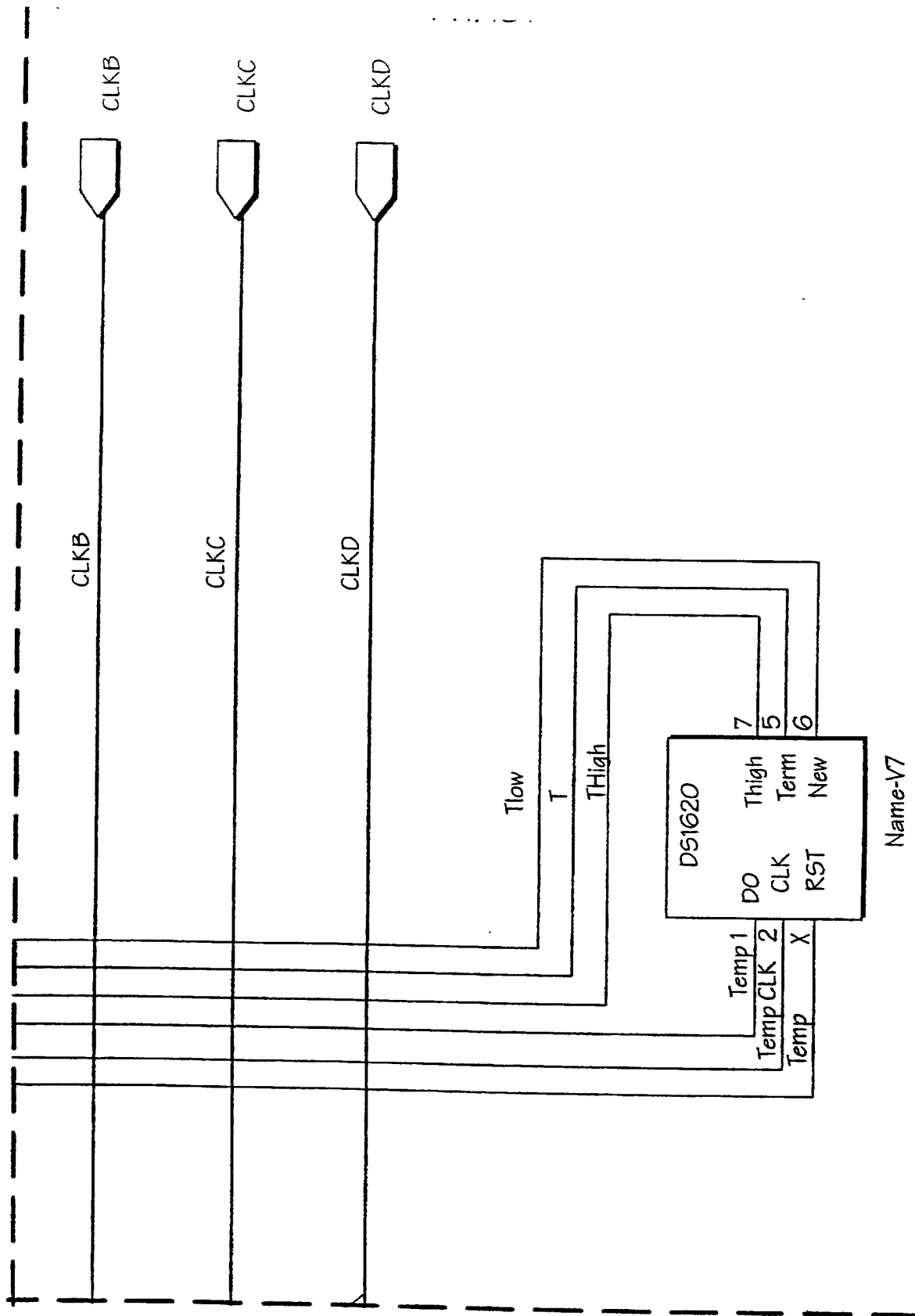


FIG. 63(F)



Name-V7

FIG. 63(G)

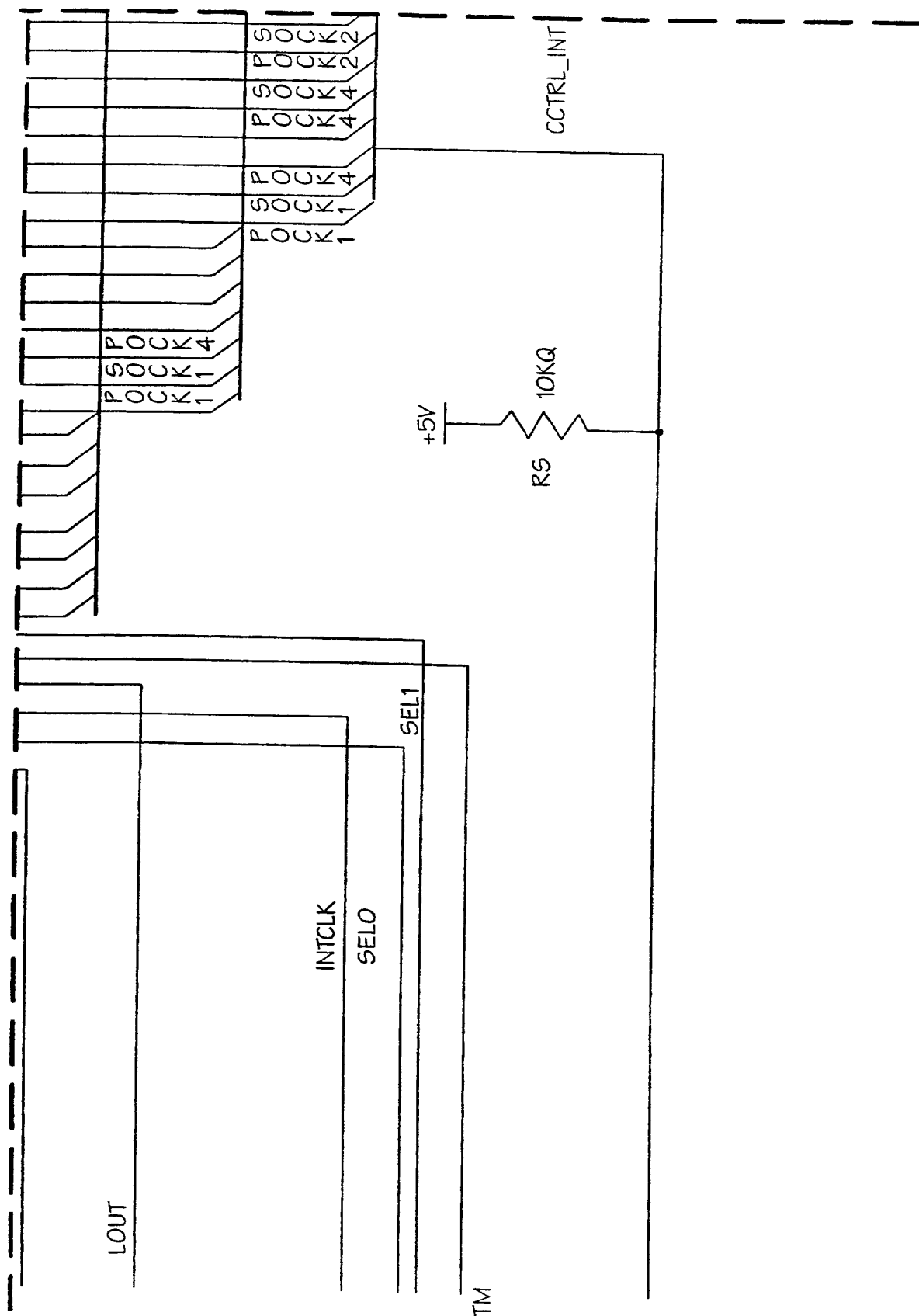


FIG. 63(H)



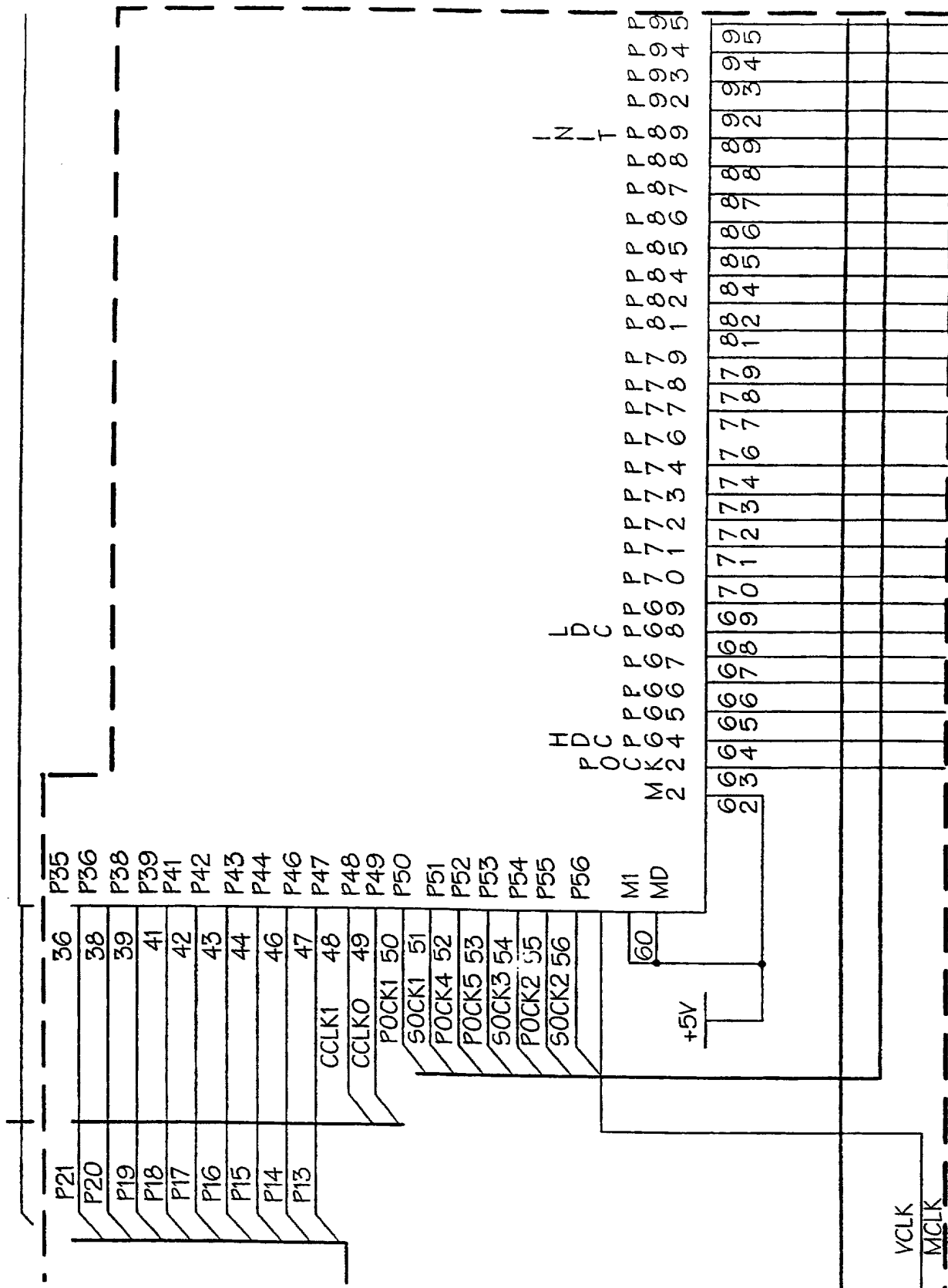


FIG. 63(I)

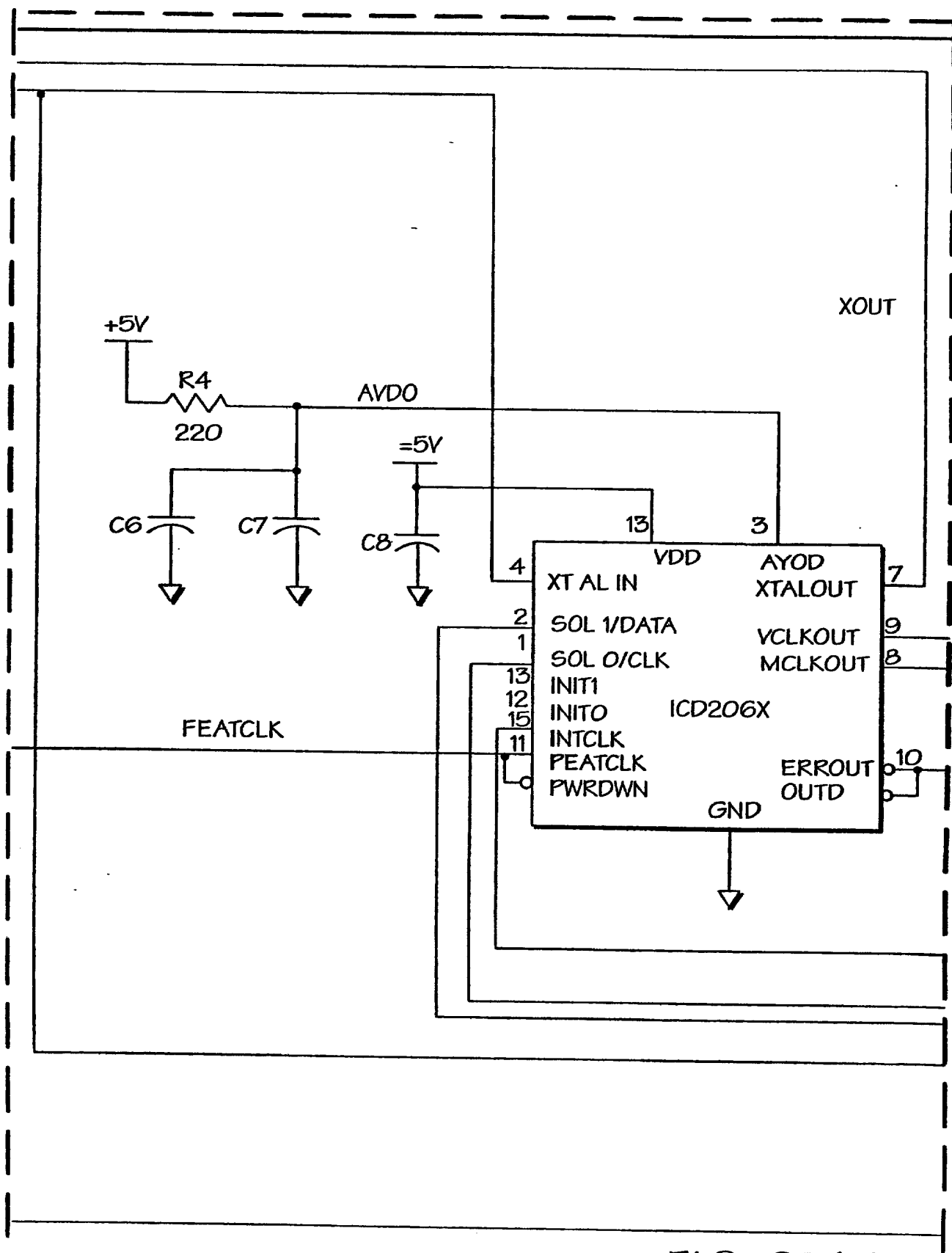


FIG. 63(J)

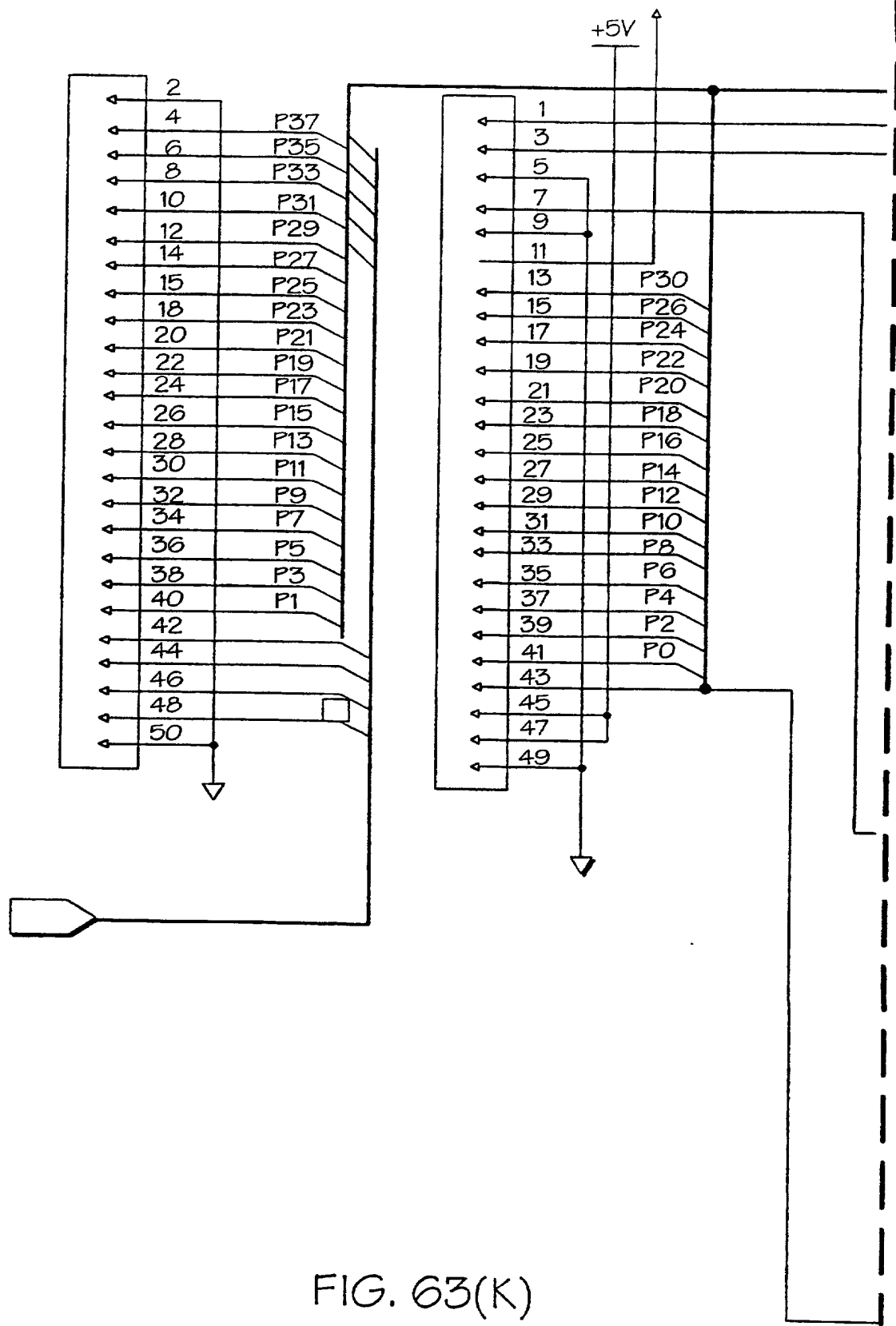


FIG. 63(K)

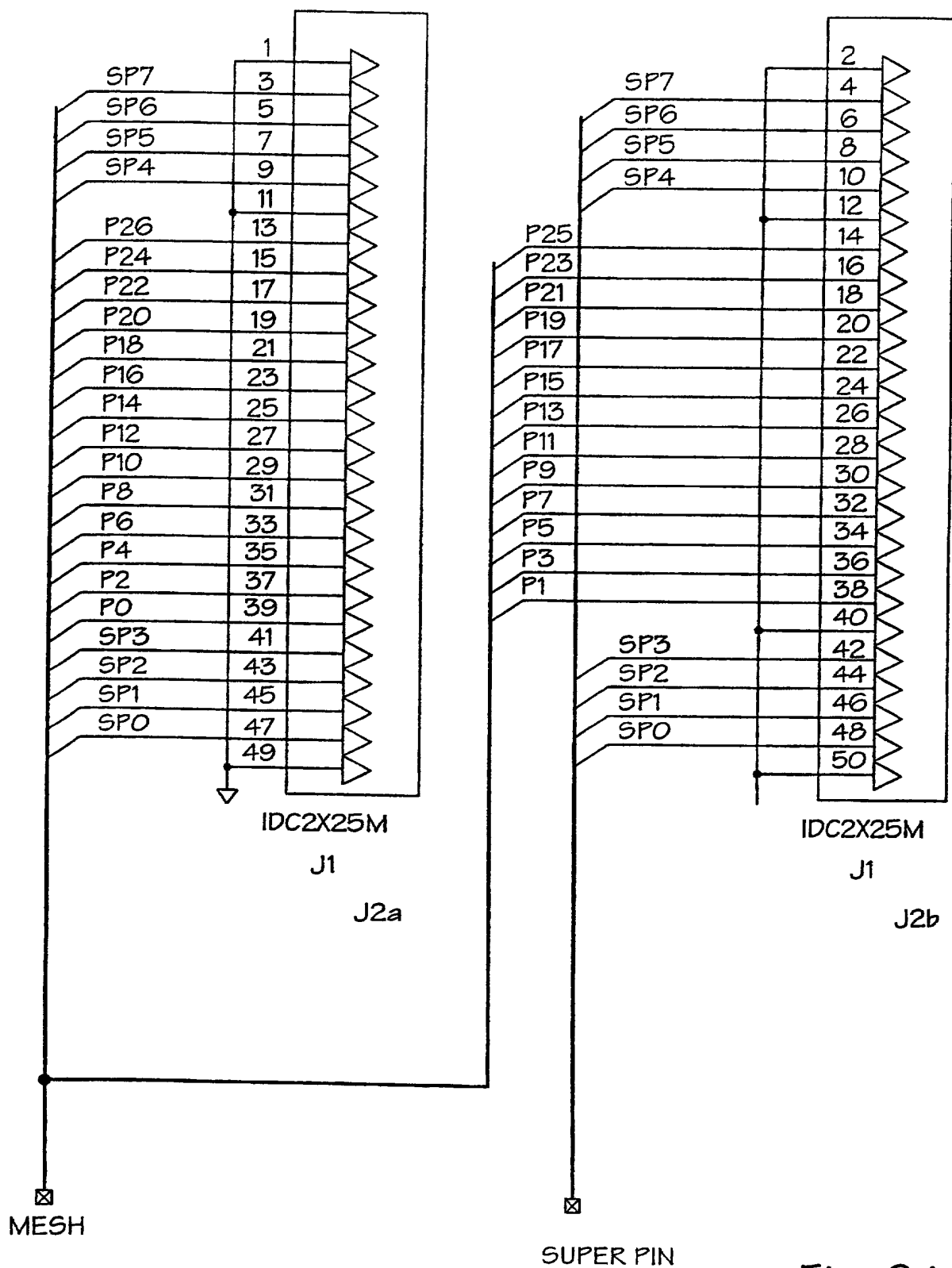


Fig. 64

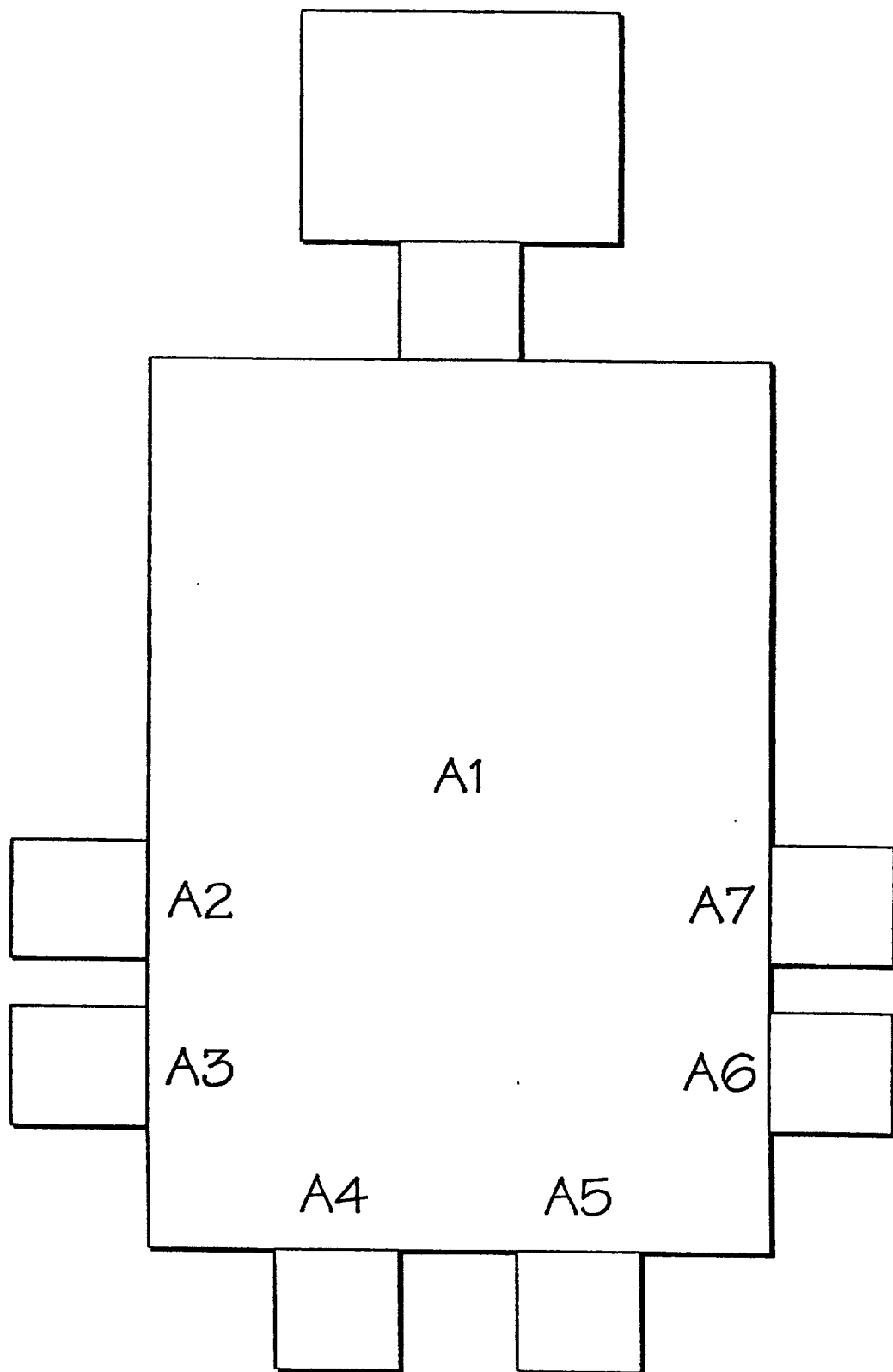


FIG. 65

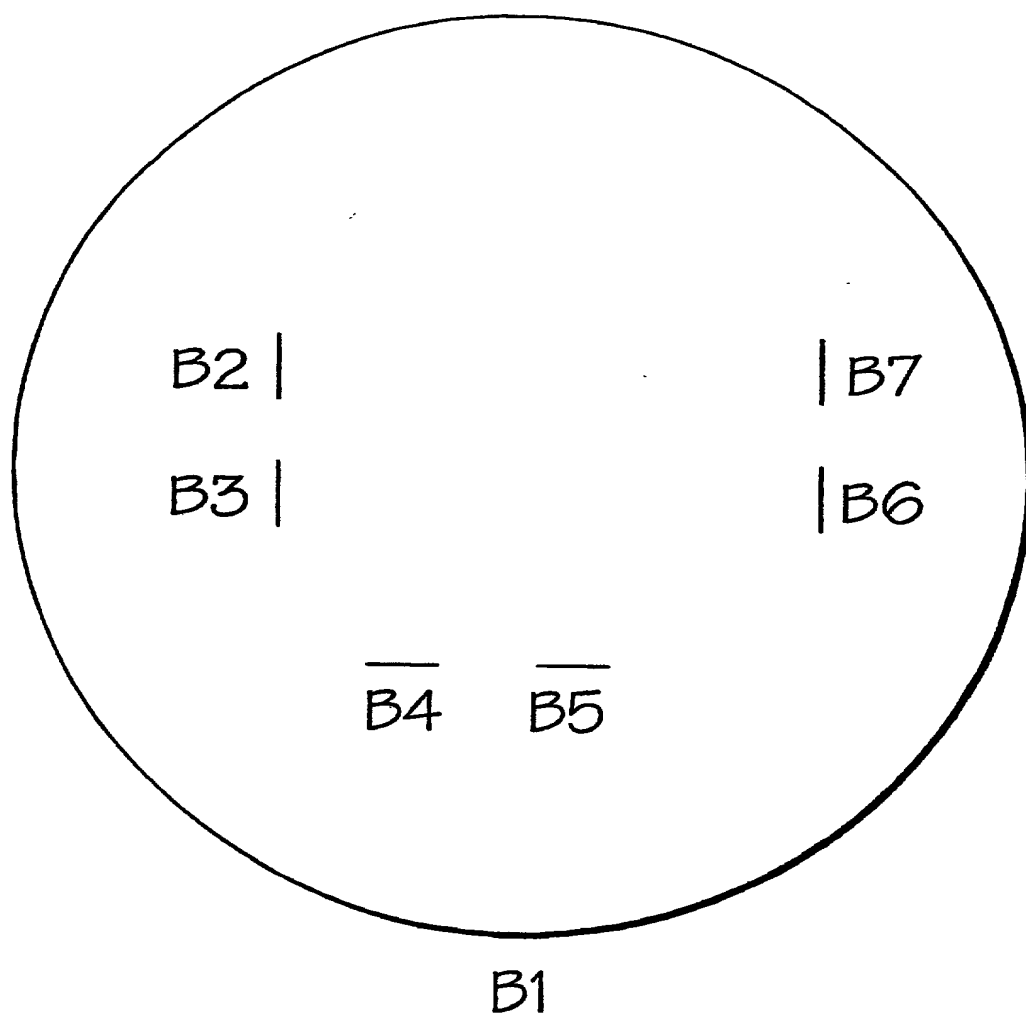


FIG. 66

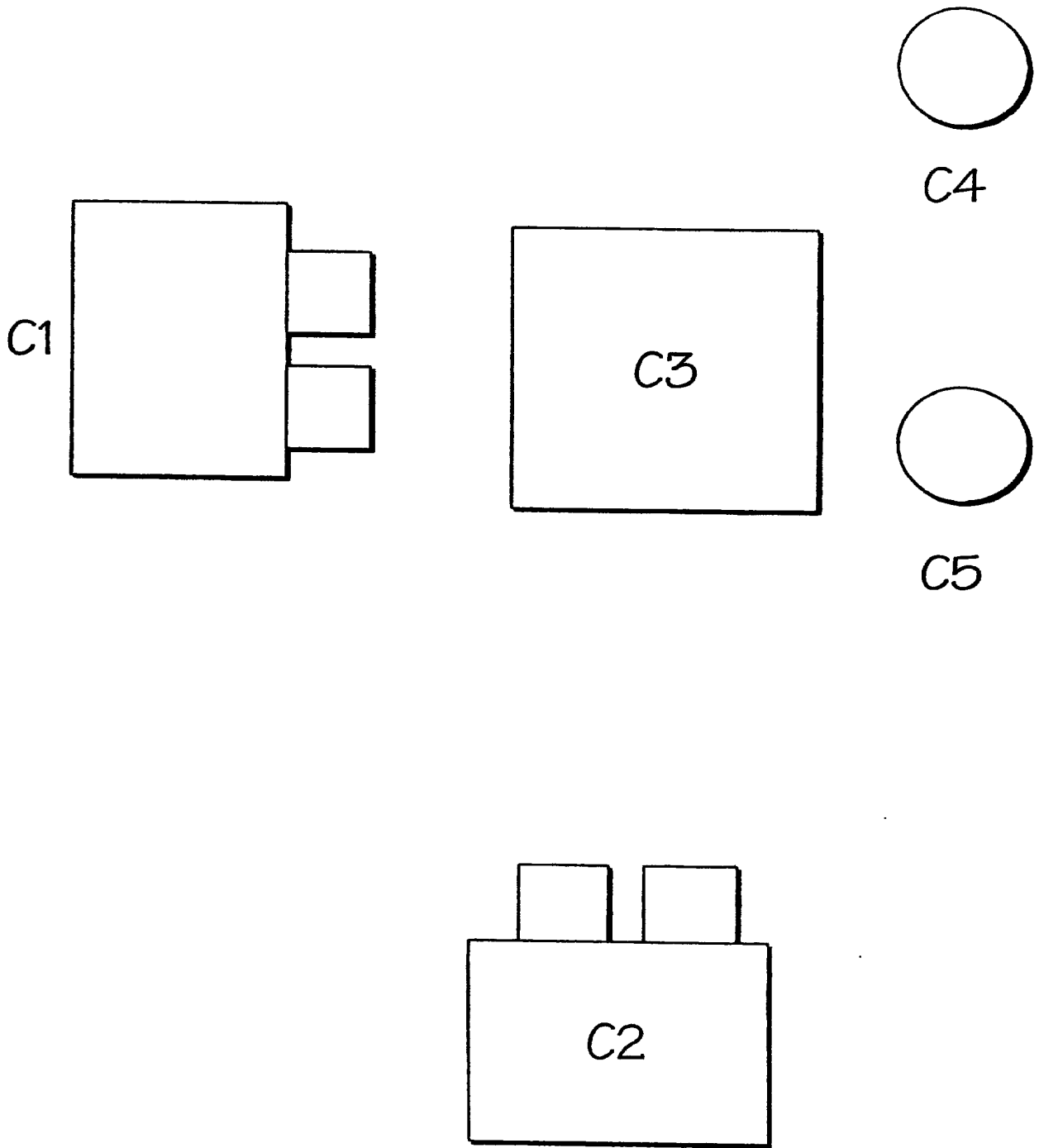


FIG. 67

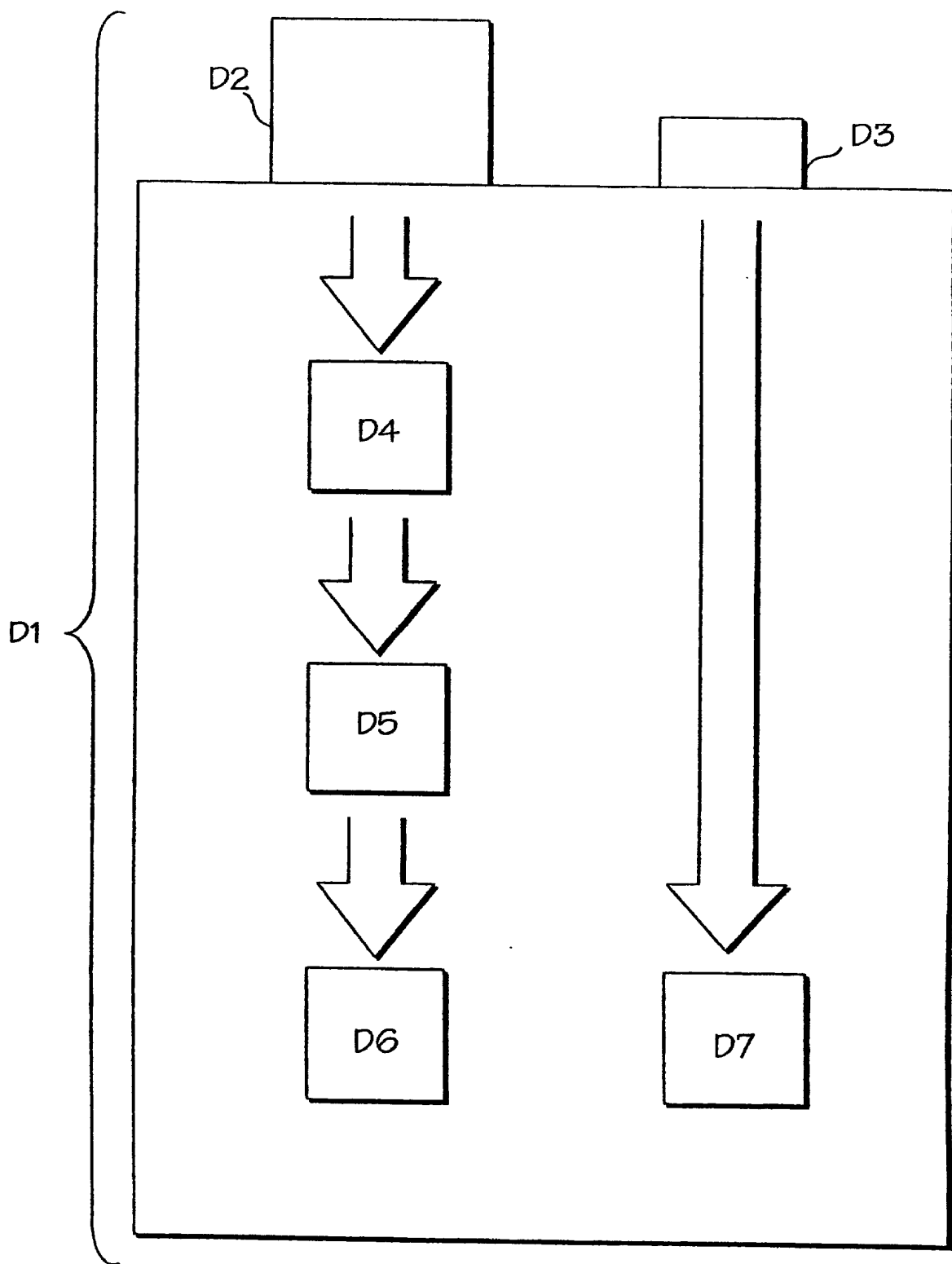


FIG. 68



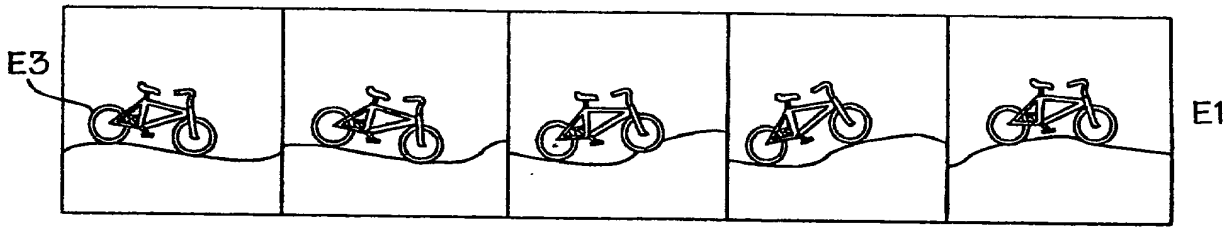


FIG. 69(A)

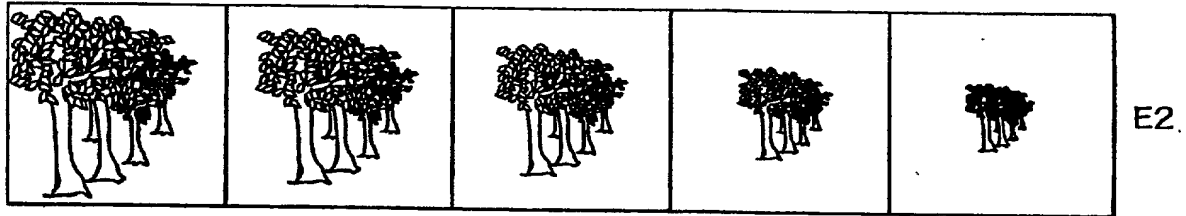


FIG. 69(B)

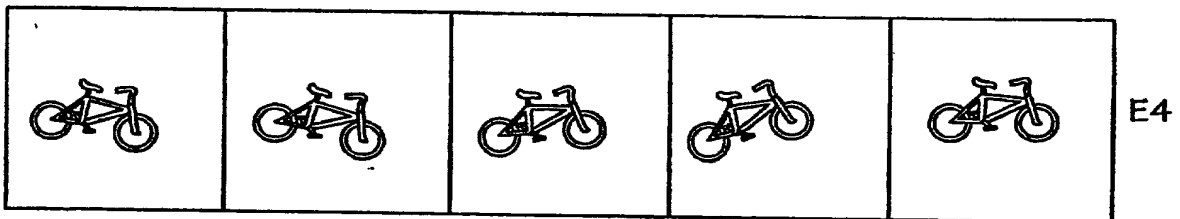


FIG. 69(C)

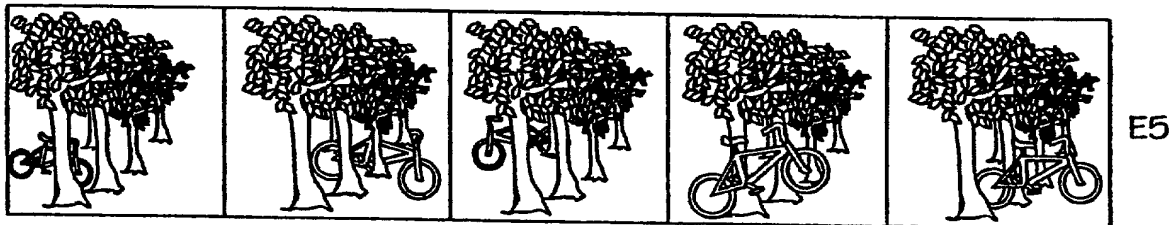


FIG. 69(D)